

## Device Information

HCS00MS

[Printer Friendly Version](#)

NAND-Gate, 2-Input, Quad, Rad-Hard, High-Speed, CMOS, Logic

[Get Datasheet](#)**Ordering Information**

Part No.	Status	Temp.	Package	MSL	SMD	Price US \$	
HCS00DMSR	Active	-	<a href="#">14 Ld SBDIP</a>	N/A	5962R9572401VCC	Contact Us	<a href="#">Buy</a>
HCS00HMSR	Active	-	<a href="#">Other</a>	N/A	-	Contact Us	<a href="#">Buy</a>
HCS00KMSR	Active	-	<a href="#">14 Ld FlatPack</a>	N/A	5962R9572401VXC	Contact Us	<a href="#">Buy</a>

**The price listed is the manufacturer's suggested retail price for quantities between 100 and 999 units. However, prices in today's market are fluid and may change without notice.**

**MSL** = Moisture Sensitivity Level - per IPC/JEDEC J-STD-020

**SMD** = Standard Microcircuit Drawing

**Description**

The Intersil HCS00MS is a Radiation Hardened Quad 2-Input NAND Gate. A high on both inputs forces the output to a Low state.

The HCS00MS utilizes advanced CMOS/SOS technology to achieve high-speed operation. This device is a member of radiation hardened, high-speed, CMOS/SOS Logic Family.

The HCS00MS is supplied in a 14 lead Ceramic flatpack (K suffix) or a SBDIP Package (D suffix).

**Key Features**

- 3 Micron Radiation Hardened SOS CMOS
- Total Dose 200K RAD (Si)
- SEP Effective LET No Upsets: >100 MEV-cm<sup>2</sup>/mg
- Single Event Upset (SEU) Immunity < 2 x 10<sup>-9</sup> Errors/Bit-Day (Typ)
- Dose Rate Survivability: >1 x 10<sup>12</sup> RAD (Si)/s
- Dose Rate Upset >10<sup>10</sup> RAD (Si)/s 20ns Pulse
- Cosmic Ray Upset Immunity < 2 x 10<sup>-9</sup> Errors/Gate Day (Typ)
- Latch-Up Free Under Any Conditions
- Military Temperature Range: -55°C to +125°C
- Significant Power Reduction Compared to LSTTL ICs
- DC Operating Voltage Range: 4.5V to 5.5V
- Input Logic Levels
  - VIL = 30% of VCC Max
  - VIH = 70% of VCC Min
- Input Current Levels I<sub>i</sub> d 5μA at VOL, VOH

**Related Documentation**

- DS** Datasheet(s):
- [Radiation Hardened Quad 2-Input NAND Gate](#)
- SMD** Military SMD(s):
- [Radiation Hardened Quad 2-Input NAND Gate](#)

**Parametric Data**

RH Level	200
----------	-----

**Related Devices**

**PT** [Parametric Table](#)

<a href="#">ACS03MS</a>	NAND Gate, Quad 2-Input, Rad-Hard, Advanced Logic, CMOS
<a href="#">CD4023BMS</a>	NAND, 3-Input, Triple, Rad-Hard, CMOS, Logic
<a href="#">CD4093BMS</a>	AND-Gate, 2-Input, Schmitt Trigger, Quad, Rad-Hard, CMOS, Logic
<a href="#">HCS10MS</a>	NAND-Gate, 3-Input, Triple, Rad-Hard, High-Speed, CMOS, Logic
<a href="#">HCS132MS</a>	NAND-Gate, 2-Input, Schmitt Trigger, Quad, Rad-Hard, High-Speed, CMOS, Logic
<a href="#">HCTS00MS</a>	NAND-Gate, 2-Input, TTL Inputs, Quad, Rad-Hard, High-Speed, CMOS, Logic
<a href="#">HCTS20MS</a>	NAND-Gate, 4-Input, TTL Inputs, Dual, Rad-Hard, High-Speed, CMOS, Logic
<a href="#">HCTS30MS</a>	NAND-Gate, 8-Input, TTL Inputs, Rad-Hard, High-Speed, CMOS, Logic

[Contact Us](#) | [About Us](#) | [Legal](#) | [Privacy](#) | [Subscribe](#) | [Site Map](#) | [Intranet](#)



©2004. All rights reserved.