

## Device Information

HCS04MS

[Printer Friendly Version](#)**Inverter, Hex, Rad-Hard, High-Speed, CMOS, Logic**[Get Datasheet](#)**Ordering Information**

Part No.	Status	Temp.	Package	MSL	SMD	Price US \$
HCS04DMSR	InActive	-	<a href="#">14 Ld SBDIP</a>	N/A	5962R9572501VCC	Contact Us
HCS04HMSR	InActive	-	<a href="#">Other</a>	N/A	-	Contact Us
HCS04KMSR	InActive	-	<a href="#">14 Ld FlatPack</a>	N/A	5962R9572501VXC	Contact Us

**The price listed is the manufacturer's suggested retail price for quantities between 100 and 999 units. However, prices in today's market are fluid and may change without notice.**

**MSL** = Moisture Sensitivity Level - per IPC/JEDEC J-STD-020

**SMD** = Standard Microcircuit Drawing

**Description**

The Intersil HCS04MS is a Radiation Hardened Hex Inverter. A logic level on any input forces the output to the opposite logic state.

The HCS04MS utilizes advanced CMOS/SOS technology to achieve high-speed operation. This device is a member of radiation hardened, high-speed, CMOS/SOS Logic Family.

The HCS04MS is supplied in a 14 lead Ceramic flatpack (K suffix) or a SBDIP Package (D suffix).

**Key Features**

- 3 Micron Radiation Hardened SOS CMOS
- Total Dose 200K RAD (Si)
- SEP Effective LET No Upsets: >100 MEV-cm<sup>2</sup>/mg
- Single Event Upset (SEU) Immunity < 2 x 10<sup>-9</sup> Errors/Bit-Day (Typ)
- Dose Rate Survivability: >1 x 10<sup>12</sup> RAD (Si)/s
- Dose Rate Upset >10<sup>10</sup> RAD (Si)/s 20ns Pulse
- Cosmic Ray Upset Immunity < 2 x 10<sup>-9</sup> Errors/Gate Day (Typ)
- Latch-Up Free Under Any Conditions
- Military Temperature Range: -55°C to +125°C
- Significant Power Reduction Compared to LSTTL ICs
- DC Operating Voltage Range: 4.5V to 5.5V
- Input Logic Levels
  - VIL = 30% of VCC Max
  - VIH = 70% of VCC Min
- Input Current Levels I<sub>i</sub> d 5μA at VOL, VOH

**Related Documentation**



Datasheet(s):

- [Radiation Hardened Hex Inverter](#)



Military SMD(s):

- [Radiation Hardened Hex Inverter](#)

#### Parametric Data

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RH Level

200

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