# Device Information

### HCS132MS

Printer Friendly Version

NAND-Gate, 2-Input, Schmitt Trigger, Quad, Rad-Hard, High-Speed, CMOS, Logic

DS Get Datasheet

## **Ordering Information**

Part No.	Status	Temp.	Package	MSL	SMD	Price US \$	
HCS132DMSR	Active	-	14 Ld SBDIP	N/A	5962R9572601VCC	Contact Us	Buy
HCS132HMSR	Active	-	Other	N/A	-	Contact Us	Buy
HCS132KMSR	Active	-	<u>14 Ld</u> <u>FlatPack</u>	N/A	5962R9572601VXC	Contact Us	Buy

The price listed is the manufacturer's suggested retail price for quantities between 100 and 999 units. However, prices in today's market are fluid and may change without notice. MSL = Moisture Sensitivity Level - per IPC/JEDEC J-STD-020

**SMD** = Standard Microcircuit Drawing

### Description

The Intersil HCS132MS is a Radiation Hardened Quad 2-Input NAND Schmitt Trigger inputs. A high on both inputs forces the output to a Low state.

The HCS132MS utilizes advanced CMOS/SOS technology to achieve high-speed operation. This device is a member of radiation hardened, high-speed, CMOS/SOS Logic Family.

The HCS132MS is supplied in a 14 lead Ceramic flatpack (K suffix) or a SBDIP Package (D suffix).

### **Key Features**

- 3 Micron Radiation Hardened SOS CMOS
- Total Dose 200K RAD (Si)
- SEP Effective LET No Upsets: >100 MEV-cm<sup>2</sup>/mg
- Single Event Upset (SEU) Immunity < 2 x 10<sup>-9</sup> Errors/Bit-Day (Typ)
- Dose Rate Survivability: >1 x 10<sup>12</sup> RAD (Si)/s
- Dose Rate Upset >10<sup>10</sup> RAD (Si)/s 20ns Pulse
- Cosmic Ray Upset Immunity < 2 x 10<sup>-9</sup> Errors/Gate Day (Typ
- Latch-Up Free Under Any Conditions
- Military Temperature Range: -55°C to +125°C
- Significant Power Reduction Compared to LSTTL ICs
- DC Operating Voltage Range: 4.5V to 5.5V
- Input Logic Levels
  - $\circ$  VIL = 30% of VCC Max
  - $\circ$  VIH = 70% of VCC Min
- Input Current Levels Ii d 5µA at VOL, VOH

### **Related Documentation**

http://www.intersil.com/products/deviceinfo.asp?pn=HCS132MS (1 of 2) [05/12/2004 04:07:51 ?]

<ul> <li>Military SMD(s):</li> <li><u>Radiation Hardened Quad 2-Input NAND Schmitt Trigger</u></li> </ul>			
Parametric Data RH Level	200		
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Related Devices	PT Parametri		
ACS03MS	NAND Gate, Quad 2-Input, Rad-Hard, Advanced Logic, CMOS		
<u>CD4023BMS</u>	NAND, 3-Input, Triple, Rad-Hard, CMOS, Logic		
<u>CD4093BMS</u>	AND-Gate, 2-Input, Schmitt Trigger, Quad, Rad-Hard, CMOS, Logic		
HCS00MS	NAND-Gate, 2-Input, Quad, Rad-Hard, High-Speed, CMOS, Logic		
HCS10MS	NAND-Gate, 3-Input, Triple, Rad-Hard, High-Speed, CMOS, Logic		
HCTS00MS	NAND-Gate, 2-Input, TTL Inputs, Quad, Rad-Hard, High-Speed, CM Logic		
	NAND-Gate, 4-Input, TTL Inputs, Dual, Rad-Hard, High-Speed, CMC		
HCTS20MS	Logic		

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