

## Device Information

CD4514BMS

[Printer Friendly Version](#)**Latch, 4-to-16 Line Decoder, Rad-Hard, CMOS, Logic**[Get Datasheet](#)**Ordering Information**

Part No.	Status	Temp.	Package	MSL	SMD	Price US \$	
CD4514BDMSR	Active	-	<a href="#">24 Ld SBDIP</a>	N/A	5962R9668101VJC	Contact Us	<a href="#">Buy</a>
CD4514BHRSR	Active	-		N/A	5962R9668101V9A	Contact Us	<a href="#">Buy</a>
CD4514BKMSR	Active	-	<a href="#">24 Ld FlatPack</a>	N/A	5962R9668101VXC	Contact Us	<a href="#">Buy</a>

**The price listed is the manufacturer's suggested retail price for quantities between 100 and 999 units. However, prices in today's market are fluid and may change without notice.**

**MSL** = Moisture Sensitivity Level - per IPC/JEDEC J-STD-020

**SMD** = Standard Microcircuit Drawing

**Description**

CD4514BMS and CD4515BMS consist of a 4-bit strobed latch and a 4-to-16-line decoder. The latches hold the last input data presented prior to the strobe transition from 1 to 0. Inhibit control allows all outputs to be placed at 0(CD4514BMS) or 1(CD4515BMS) regardless of the state of the data or strobe inputs.

The decode truth table indicates all combinations of data inputs and appropriate selected outputs.

These devices are similar to industry types MC14514 and MC14515.

The CD4514BMS and CD4515BMS are supplied in these 24 lead outline packages:

Braze Seal DIP H4V

Frit Seal DIP H1Z

Ceramic Flatpack H4P

**Key Features**

- High-Voltage Types (20-Volt Rating)
- CD4514BMS Output “High” on Select
- CD4515BMS Output “Low” on Select
- Strobed Input Latch
- Inhibit Control
- 100% Tested for Quiescent Current at 20V
- Maximum Input Current of 1μA at 18V Over Full Package Temperature Range; 100nA at 18V and 25°C
- Noise Margin (Full Package-Temperature Range):
  - 1V at VDD = 5V
  - 2V at VDD = 10V
  - 2.5V at VDD = 15V
- 5V, 10V, and 15V Parametric Ratings
- Standardized, Symmetrical Output Characteristics

- Meets all Requirements of JEDEC Tentative Standard No. 13B, "Standard Specifications for Description of 'B' Series CMOS Devices"

#### Related Documentation

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Datasheet(s):

- [Radiation Hardened CMOS 4-Bit Latch/4-to-16 Line Decoders](#)



Military SMD(s):

- [Radiation Hardened CMOS 4-Bit Latch/4-to-16 Line Decoders](#)

#### Parametric Data

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RH Level

100

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#### Applications

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- Digital Multiplexing
- Address Decoding
- Hexadecimal/BCD Decoding
- Program-counter Decoding
- Control Decoder

#### Related Devices

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[Parametric Table](#)

[HCS154MS](#)

Multiplexer, Digital, 4-to-16, Rad-Hard, High-Speed, CMOS, Logic



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