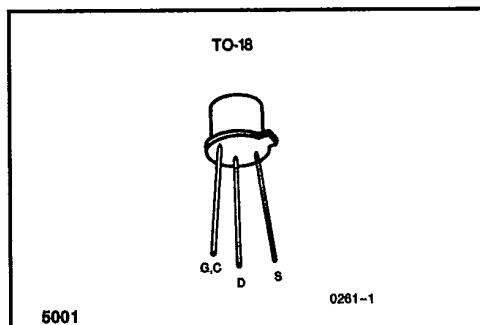


3875081 G E SOLID STATE

**U200-U202**  
**U200-U202**  
**N-Channel JFET Switch**
**FEATURES**

- Low Insertion Loss
- Good OFF Isolation

**PIN CONFIGURATION****ORDERING INFORMATION**

TO-18
U200
U201
U202

**ELECTRICAL CHARACTERISTICS** ( $T_A = 25^\circ\text{C}$  unless otherwise specified)

Symbol	Parameter	Test Conditions	U200		U201		U202		Units
			Min	Max	Min	Max	Min	Max	
$I_{GSS}$	Gate Reverse Current	$V_{GS} = -20V, V_{DS} = 0$		-1		-1		-1	nA
		$T_A = 150^\circ\text{C}$		-1		-1		-1	$\mu\text{A}$
$BV_{GSS}$	Gate-Source Breakdown Voltage	$I_G = -1\mu\text{A}, V_{DS} = 0$	-30		-30		-30		V
		$V_{DS} = 20V, I_D = 10\text{nA}$	-0.5	-3	-1.5	-5	-3.5	-10	
$I_D(\text{off})$	Drain Cutoff Current	$V_{DS} = 10V, V_{GS} = -12V$		1		1		1	nA
		$T_A = 150^\circ\text{C}$		1		1		1	$\mu\text{A}$
$I_{DSS}$	Saturation Drain Current (Note 1)	$V_{DS} = 20V, V_{GS} = 0$	3	25	15	75	30	150	mA
$r_{ds(on)}$	Drain-Source ON Resistance	$V_{GS} = 0, I_D = 0$	$f = 1\text{kHz}$	150		75		50	ohm
		$V_{DS} = 20V, V_{GS} = 0$			30		30		pF
$C_{iss}$	Common-Source Input Capacitance (Note 2)	$V_{DS} = 20V, V_{GS} = 0$	$f = 1\text{MHz}$					30	
		$V_{DS} = 0, V_{GS} = -12V$			8		8		

NOTES: 1. Pulse test required, pulselwidth=300 $\mu\text{s}$ , duty cycle $\leq 3\%$ .

2. For design reference only, not 100% tested.

INTERsil's sole and exclusive warranty obligation with respect to this product shall be that stated in the warranty article of the condition of sale. The warranty shall be exclusive and shall be in lieu of all other warranties, express, implied or statutory, including the implied warranties of merchantability and fitness for a particular use.

NOTE: All typical values have been characterized but are not tested.

10-100