

TEA5701

3 CHANNEL, LARGE BAND HEAD AMPLIFIER FOR VCR

PLAY-BACK MODE

- LOW NOISE PERFORMANCE
- LARGE BANDWIDTH (SVHS PROCESSING CAPABILITY)
- AUTOMATIC OFFSET CANCELLER BETWEEN TWO SELECTED HEADS
- RECORD AMPLIFIER INHIBITION DURING PLAYBACK
- DIRECT DRIVE OF COAXIAL CABLE (500Ω 100pF) OF PLAY-BACK OUTPUT

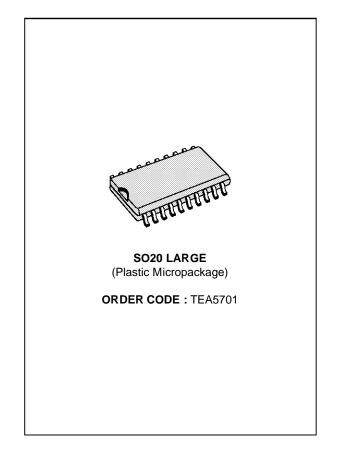
RECORD MODE

- INTEGRATED I/I CONVERTER WITH AUTO-MATIC CONTROL OF TRANSCONDUC-TANCE
- AUTOMATIC RECORD PLAY-BACK SWITCH-ING
- PLAYBACK INHIBITION DURING RECORD MODE
- AUTOMATIC PROTECTION OF RECORD AMPLIFIER AGAINST SHORT CIRCUIT

DESCRIPTION

The TEA5701 is an advanced one chip 3 heads record and playback amplifier for VCR.

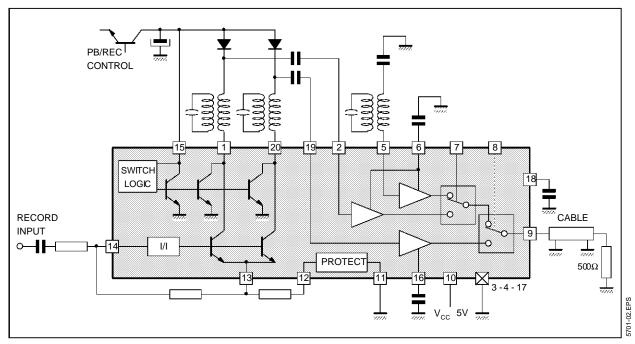
PIN CONNECTIONS



250022000			
RECORDING (OUTPUT CHANNEL 2	1	20 RECORDING OUTPUT CHANNEL 1
PLAY-BAC	(INPUT CHANNEL 2	2	19 PLAY-BACK INPUT CHANNEL 1
	GROUND	3	18 CASCADE INPUT DECOUPLING
	GROUND	4	17 GROUND
PLAY-BACK	KINPUT CHANNEL 3	5	16 DC OFFSET CANCELLER CHANNEL 1
DC OFFSET CANCELLER	CHANNNEL 2 AND 3	6	15 VOLTAGE SUPPLY FOR RECORDING MODE
CH2-CH3	SWITCH CONTROL	7	14 RECORDING INPUT
CH1-CH2 OR 3	SWITCH CONTROL	8	13 FEED-BACK OUTPUT FOR RECORDING MODE
F	PLAY-BACK OUTPUT	9	12 CURRENT LIMITATION INPUT
	V _{CC} = 5V	10	11 GROUND

August 1992 1/8

BLOCK DIAGRAM



FUNCTIONAL DESCRIPTION

TEA5701 is intended for 3 heads VCR applications. It includes all the electrical functions necessary to achieve playback and record processing for VHS and SVHS applications (9MHz).

High performance technology allows very low noise levels (current and voltage). In playback mode a special feature suppresses the DC offset when switching two channels. Optimized play-back output stage gives to the TEA5701 large capability to drive directly a coaxial cable in order to reduce number of external components.

An automatic scanning of recording supply voltage permits that TEA5701 switches automatically in playback or in record mode. The switching threshold voltage from play-back to record and record to playback is fixed to a value which forbids high current peaking through the heads.

The recording amplifier includes a protection system which protects the IC and the application board against overheating in case of short circuit on the recording transconductance components.

The TEA5701 is fully protected against ESD.

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
Vcc	Supply Voltage	6	V
V _{REC}	Supply Voltage	15	٧
T _{stg}	Storage Temperature Range	-40, + 150	°C

THERMAL DATA

Symbol	Parameter	Value	Unit
R _{th (j-a)}	Junction-ambient Thermal Resistance	70	°C/W

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ELECTRICAL OPERATING CHARACTERISTICS

All the operating characteristics are given for ambient temperature 25 °C unless otherwise specified.

Playback Mode

General conditions for play-back: Vcc = 5V, no load on play-back output

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
V _{CC}	Supply Voltage		4.75	5	5.25	V
Icc	Current Supply			45	60	mA
G _{PB}	Play-back Gain	Sine Wave 400mVpp at 600khz on Pin 9	56	60	63	dB
Δ G _{PB}	Gain Difference Between Three Play-back Channels	Sine Wave 3.8MHz, 0.4mVpp on Pins 2 - 5 - 19		0.3		dB
en	Equivalent Input Voltage Noise Level	Measured at 500kHz - CH1 Via Switching Transistor Pin 20 - CH2 Via Switching Transitor Pin 1 - CH3 Grounded		0.4		nV/√Hz
in	Equivalent Input Current Noise Level	Measured at 500kHz - PB Inputs Pins 2 - 5 - 19 not Connected		3		pA/√Hz
CRT	Crosstalk	Sine Wave 3.8MHz, 400mVpp on Pin 9 For selected channel			- 40	dB
		- CH1 input, between pins 19 and 20				
		- CH2 input, between pins 1 and 2				
		- CH3 input, between pin 5 and ground				
FLCPB	Playback Bandwidth Low Cut Off Frequency	Reference Signal Level : Sine Wave 3.8MHz 400mVpp - Play-back Input Capacitors 22nF (pins 2 - 6 - 19) - DC Offset Canceller Capacitor (pins 6 - 16-) 47 nF		20	100	kHz
FHCPB	Playback Bandwidth High Cut Off Frequency	Same Conditions as Above	8	9.5		MHz
Cin	Playback Input Capacitance Pins 2 - 5 - 19			50		pF
R _{in}	Playback Input Resistance Pins 2 - 5 - 19			600		Ω
VDCPB	DC Level on Play-back Output Pin 9 during Playback	With 500 Ω Load Resistor Between Pin 9 and Ground	1.9	2.4	2.9	V
ΔVDC	Head Switch Offset Pin 9 (all switches combinations)				50	mV
SM	Second Harmonic on Playback Output Pin 9	Sine Wave 3.8 MHz 400 mVpp with 500 Ω load Resistor		- 43	- 38	dB
V _{sat}	Maximum Voltage on Pins 1 and 20 at Playback Mode	Input Current Pins 1 and 20 20mADC			100	mV

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ELECTRICAL OPERATING CHARACTERISTICS (continued)

Recording Mode

General conditions for recording mode : V_{REC} = 12V, V_{CC} = 5V, Load resistor 100 Ω on pins 1 and 20

No load on play-back output Pin 9

Transconductance network defined by : $R1 = 5.1\Omega$ 1% pins 12-13

R2 = 1kΩ 1% pins 13-14 R3 = 750Ω 1% pin 14

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
V_{REC}	Recording Supply Voltage		9	12	12.6	V
ICCREC	Current Supply from V _{REC}			50	60	mA
I _{CCI}	Current Supply from V _{CC}			30	37.5	mA
V_{DCREC}	DC Level on Play-back Output Pin 9	With 500 Ω Load Resistor between Pin 9 and Ground	3.1	3.6	4.1	V
	Maximum Recording Current on Each Channel	f = 1.6MHz	40			mApp
	Maximum Recording Current on Each Channel	f = 3.8MHz	35			mApp
g	Transconductance	R1 = 5.1Ω 0 %, R2 = 1000Ω 0 % R3 = 750Ω 0 %, V _{in} = 300 mVpp Measured at 500 kHz		132		mA/V
Δg	Recording Current Difference Between Pins 1 and 20	Sine Wave 3.8MHz - recording = 30mA _{PP}			0.5	dB
REREC	Equivalent Input Resistance			660		Ω
Rs	Output Resistance Pins 1 and 20	$R1 = 5.1\Omega$		100		kΩ
SHREC	Second Harmonic Pins 1 and 20	Output Current on Each Output : 30mApp at 3.8MHz			- 38	dB
FLCREC	Recording Bandwidth Low Cut Off Frequency	Reference Output Current 30mApp at 3.8MHz for – 3dB		20	100	kHz
FHCREC	Recording Bandwidth High Cut Off Frequency	Reference Output Current 30mApp at 500kHz for – 3dB	8	9.5		MHz
	Maximum Input Current Pin 12	Pin 12 Connected to V _{REC} = 12V			100	mA
	Maximum Saturation Voltage on Pin12	Input Current Pin 12 : 50mA		100	150	mV
IM	Intermodulation	- Luminance = 30mApp 3.8MHz		- 50		dB
		- Chrominance = 7.5mApp, 600kHz Measured at 3.8MHz ± 600kHz				

Switching Levels

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
V _{H8}	Threshold Voltage for Head 1 Selection on Pin 8		2.4		Vcc	V
V_{L8}	Threshold Voltage for Head 2 or 3 Selection on Pin 8		0		1.5	V
I _{H8}	Input Current Pin 8 for H1 Selected	Pin 8 Connected to V _{CC}			50	μΑ
I _{L8}	Output Current Pin 8 for H2 or 3 Selected	Pin 8 Connected to Ground			- 50	μΑ
V _{H7}	Threshold Voltage for Head 2 Selection on Pin 7		2.4		Vcc	V
V_{L7}	Threshold Voltage for Head 3 Selection on Pin 7		0		1.5	V
I _{H7}	Input Current Pin 7 for Head 2 Selected	Pin 7 Connected to V _{CC}			50	μΑ
I _{L7}	Output Current Pin 7 for Head 3 Selected	Pin 7 Connected to Ground			- 50	μΑ
	Switching Time from H1 Selected to H2 Selected	Switching Pulse from 5 to 0V Applied Pin 8		250	500	ns
	Switching Time from H2 Selected to H1 Selected	Switching Pulse from 0 to 5V Applied Pin 8		250	500	ns

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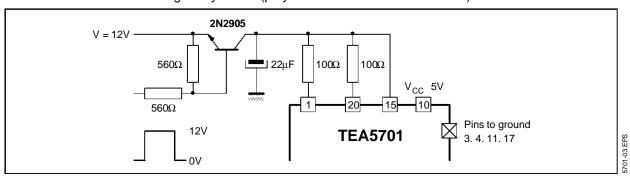


ELECTRICAL CHARACTERITICS (continued) **Switching Levels**

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
VRPB	Recording Supply Voltage Threshold (pin 15) for Switching from Record to Playback		0.15	0.3	0.5	V
VPBR	Recording Supply Voltage Threshold (pin 15) for Switching from Playback to record		0.25	0.4	0.6	V
	Delay Time for Suppression of Play-back Output Signal on Pin 9 (playback to record)	See Measurement Conditions End of Paragraph		30		μs
	Delay Time for Presence of Playback Output Signal on Pin 9 (record to play-back)	See Measurements Conditions End of Paragraph		20		ms
	Delay Time for Suppression of Recording Signals Pins 1 and 20 (record to playback)	See Measurements Conditions End of Paragraph		4		ms
	Delay Time for Suppression of Recording Signals Pin 1 and 20 (playback to record)	See Measurements Conditions End of Paragraph		200		μs
SVR	Supply Voltage Rejection	Gain Measure Made Between Playback Output Pin 9 and V _{CC} (0.5mVpp on Pin 10)	15	20	25	dB

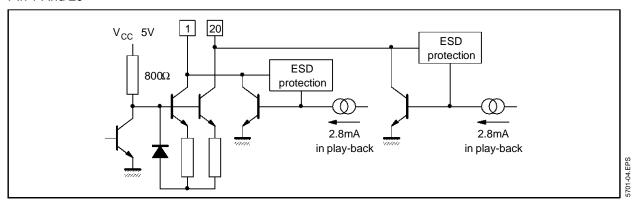
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Test Conditions for Measuring Delay Times (play-back to record and vice versa)



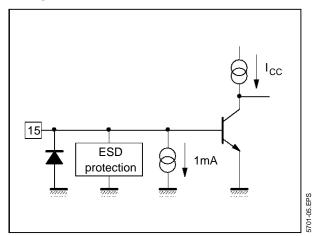
INPUTS/OUTPUTS EQUIVALENT INTERNAL DIAGRAM

Pin 1 And 20

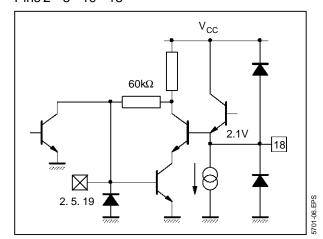


INPUTS/OUTPUTS EQUIVALENT INTERNAL DIAGRAM (continued)

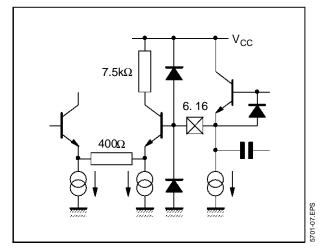
Pin 15



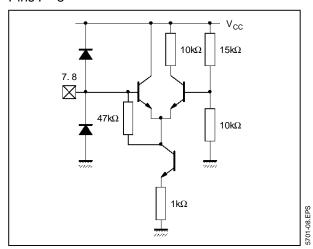
Pins 2 - 5 - 19 - 18



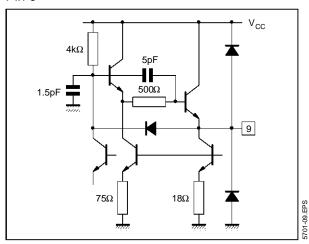
Pins 6 - 16



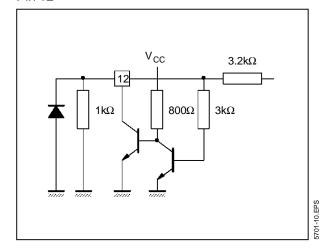
Pins 7 - 8



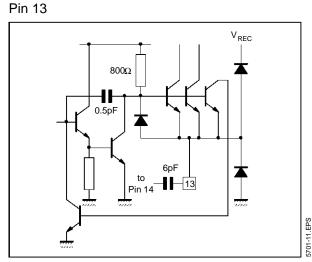
Pin 9

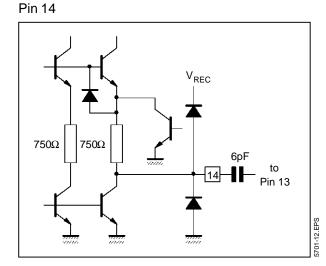


Pin 12

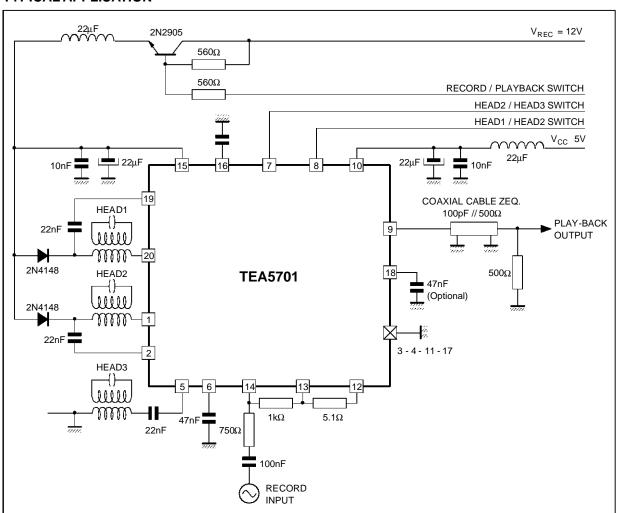


INPUTS/OUTPUTS EQUIVALENT INTERNAL DIAGRAM (continued)



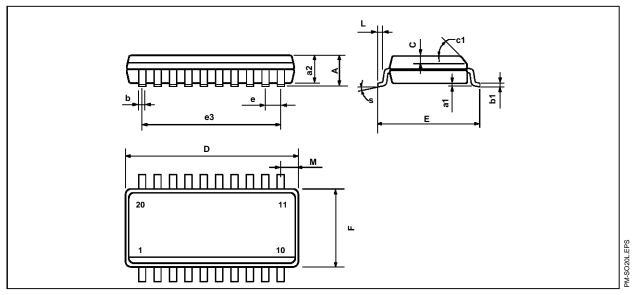


TYPICAL APPLICATION



PACKAGE MECHANICAL DATA

SO20 LARGE - PLASTIC MICROPACKAGE



Dimensions		Millimeters			Inches	
Dimensions	Min.	Тур.	Max.	Min.	Тур.	Max.
Α			2.65			0.104
a1	0.1		0.2	0.004		0.008
a2			2.45			0.096
b	0.35		0.49	0.014		0.019
b1	0.23		0.32	0.009		0.013
С		0.5			0.020	
c1			45°	(typ.)	•	
D	12.6		13.0	0.496		0.510
E	10		10.65	0.394		0.419
е		1.27			0.050	
e3		11.43			0.450	
F	7.4		7.6	0.291		0.300
L	0.5		1.27	0.020		0.050
М			0.75			0.030
S			8° (ı	max.)		

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