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Renesas Technology Corp. Customer Support Dept. April 1, 2003



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Silicon N-Channel MOS FET



ADE-208-1327 (Z) 1st. Edition Mar. 2001

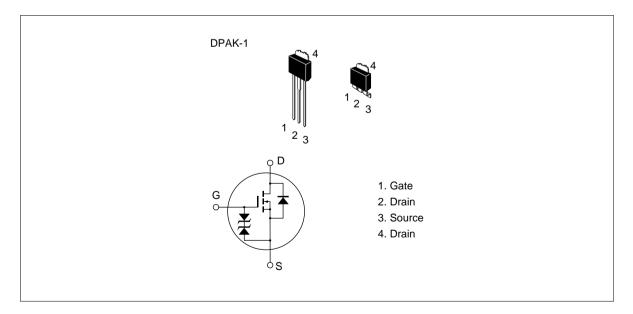
Application

High speed power switching

Features

- Low on-resistance
- High speed switching
- Low drive current
- No secondary breakdown
- Suitable for switchingregulator, DC-DC converter

Outline



Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	250	V
Gate to source voltage	V _{GSS}	±30	V
Drain current	I _D	1	A
Drain peak current	I D(pulse) *1	2	A
Body to drain diode reverse drain current	I _{DR}	1	А
Channel dissipation	Pch*2	10	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes 1. PW 10 µs, duty cycle 1 %

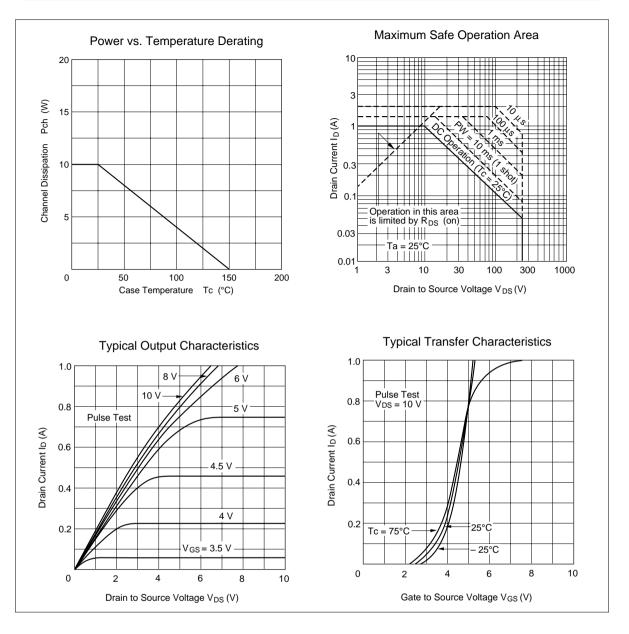
2. Value at Tc = 25 °C

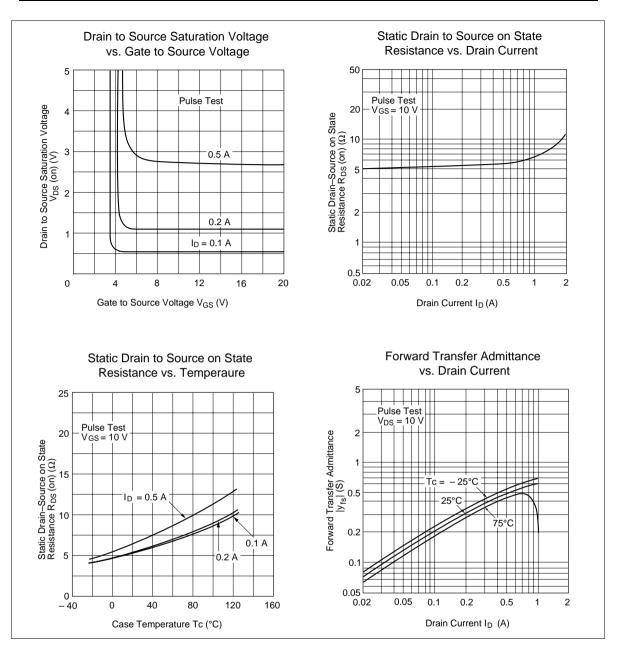
Electrical Characteristics (Ta = 25°C)

Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(\text{BR})\text{DSS}}$	250	_	_	V	$I_{\rm D} = 10 \text{ mA}, V_{\rm GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±30	_	_	V	$I_{g} = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I _{GSS}	—	_	±10	μA	$V_{GS} = \pm 25 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	_	_	100	μA	$V_{\rm DS} = 200 \text{ V}, \text{ V}_{\rm GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	2.0	_	3.0	V	$V_{\rm DS} = 10 \text{ V}, \text{ I}_{\rm D} = 1 \text{ mA}$
Forward transfer admittance	y _{fs}	0.3	0.5	_	S	$V_{\rm DS} = 10 \text{ V}, \text{ I}_{\rm D} = 0.5 \text{ A}^{*1}$
Static drain to source on state resistance	$R_{\text{DS(on)}}$	—	5.5	8.0		$I_{\rm D} = 0.5 \text{ A}, \text{ V}_{\rm GS} = 10 \text{ V}^{*1}$
Input capacitance	Ciss	_	60	_	pF	$V_{\rm DS} = 10 \ V, \ V_{\rm GS} = 0,$
Output capacitance	Coss	_	30	_	pF	f = 1 MHz
Reverse transfer capacitance	Crss	_	5	_	pF	
Turn-on delay time	t _{d(on)}	_	5	_	ns	$V_{\rm GS} = 10 \ V, \ I_{\rm D} = 0.5 \ A,$
Rise time	t,	_	6	_	ns	R _L = 60
Turn-off delay time	$t_{d(off)}$	_	10	—	ns	
Fall time	t _f	_	4.5	_	ns	
Body to drain diode forward voltage	V_{DF}	—	0.96	_	V	$I_{F} = 1 \text{ A}, V_{GS} = 0$
Body to drain diode reverse recovery time	t _{rr}	—	160	_	ns	$I_F = 7 \text{ A}, V_{GS} = 0,$ $di_F/dt = 100 \text{ A}/\mu\text{s}$
Note 1 Pulse test						

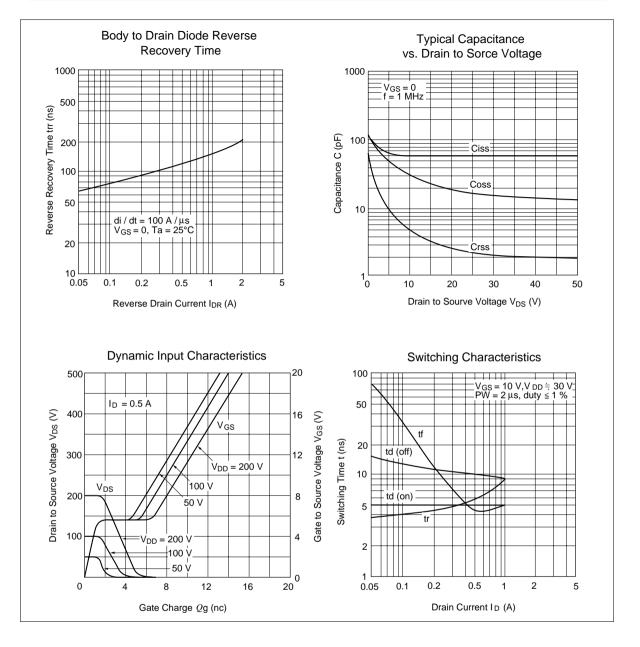
Note 1. Pulse test



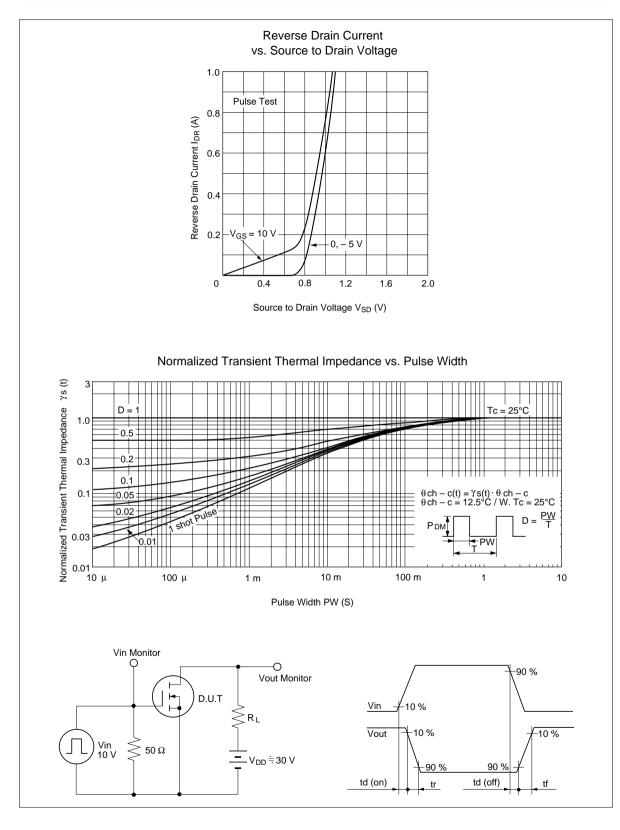






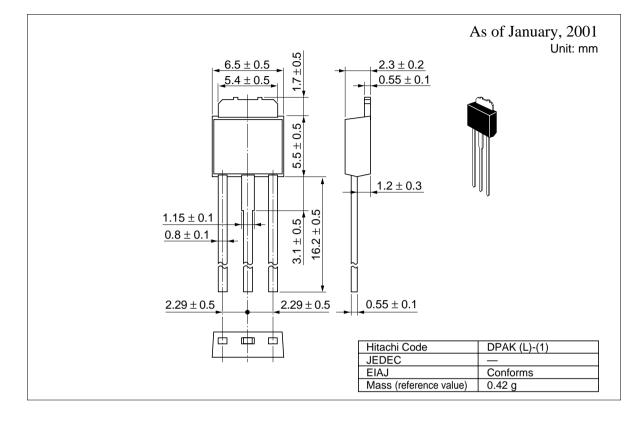


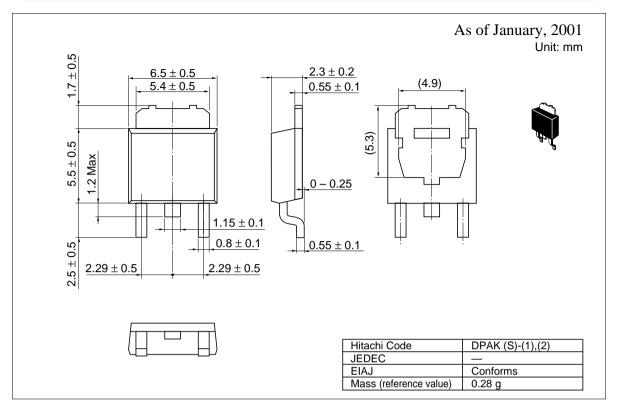
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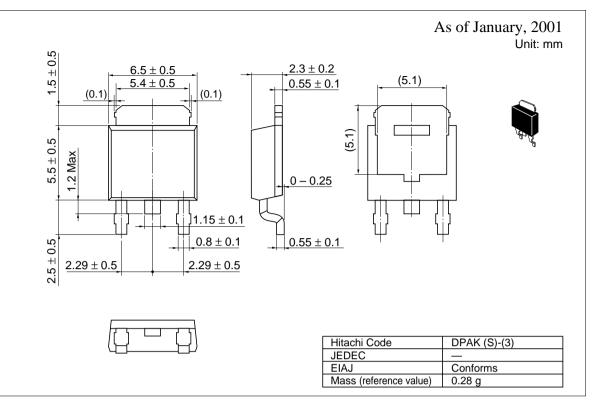


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Package Dimensions







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