TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SG00FE

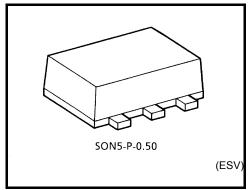
2 Input NAND Gate

Features

- High-level output current: $I_{OH}/I_{OL} = \pm 8 \text{ mA} \text{ (min)}$ at $V_{CC} = 3.0 \text{ V}$
- High-speed operation: t_{pd} = 2.5 ns (typ.)

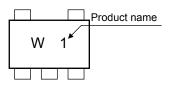
at V_{CC} = 3.3 V,15pF

- Operating voltage range: V_{CC} = 0.9~3.6 V
- 5.5-V tolerant inputs.
- 3.6-V power down protection output.

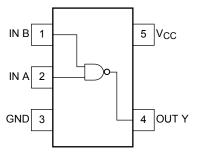


Weight: 0.003 g (typ.)

Marking



Pin Assignment (top view)



Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Value	Unit	
Power supply voltage	V _{CC}	-0.5~4.6	V	
DC input voltage	VIN	-0.5~7.0	V	
		-0.5~4.6 (Note 1)	V	
DC output voltage	Vout	-0.5~V _{CC} + 0.5 (Note 2)		
Input diode current	I _{IK}	-20	mA	
Output diode current	I _{OK}	-20 (Note 3)	mA	
DC output current	IOUT	±25	mA	
DC V _{CC} /ground current	ICC	±50	mA	
Power dissipation	PD	150	mW	
Storage temperature	T _{stg}	-65~150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: $V_{CC} = 0V$

Note 2: High or Low State. I_{OUT} absolute maximum rating must be observed.

Note 3: V_{OUT} < GND

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Truth Table

А	В	Y
L	L	Н
L	Н	Н
Н	L	Н
Н	Н	L

IEC Logic Symbol



Operating Ranges

Characteristics	Symbol	Value	Unit		
Power supply voltage	V _{CC}	0.9~3.6	V		
Input voltage	V _{IN}	0~5.5	V		
Output voltage	Vout	0~3.6 (Note 4)	V		
	VOUT	0~V _{CC} (Note 5)	v		
Output Current		±8.0 (Note 6)			
	1	±4.0 (Note 7)			
		±3.0 (Note 8)	mA		
	I _{OH} /I _{OL}	±1.7 (Note 9)	IIIA		
		±0.3 (Note 10)			
		±0.02 (Note 11)			
Operating temperature	T _{opr}	-40~85	°C		
Input rise and fall time	dt/dV	0~10 (Note 12)	ns/V		

Note 4: $V_{CC} = 0V$

Note 5: High or Low state.

Note 6: $V_{CC} = 3.0 \sim 3.6 \text{ V}$

Note 7: $V_{CC} = 2.3 \sim 2.7 \text{ V}$

Note 8: $V_{CC} = 1.65 \sim 1.95 \text{ V}$

Note 9: $V_{CC} = 1.4 \sim 1.6 V$

Note 10: $V_{CC} = 1.1 \sim 1.3 \text{ V}$

Note 11: $V_{CC}=0.9\;V$

Note 12: $V_{IN} = 0.8\text{-}2.0$ V, $V_{CC} = 3.0$ V

DC Electrical Characteristics

Characteristics Symbol Test Condition			Condition		Ta = 25°C			Ta = -40~85°C		Unit
Characteristics Cymbol		Test	Test Condition		Min	Тур.	Max	Min	Max	Unit
				0.9	V _{CC}	_		V _{CC}	_	-
High-level VIH input voltage	1.1~1.3			V _{CC} × 0.7	_	_	V _{CC} × 0.7	_		
	1.4~1.6			$\begin{array}{c} V_{CC} \\ \times \ 0.65 \end{array}$		_	V _{CC} × 0.65		V	
	1.65~1.95			$\begin{array}{c} V_{CC} \\ \times \ 0.65 \end{array}$		_	V _{CC} × 0.65			
				2.3~2.7	1.7			1.7		
				3.0~3.6	2.0	_		2.0	_	
				0.9	_		GND		GND	
			_			_	$V_{CC} \times 0.3$	_	$V_{CC} \times 0.3$	V
Low-level	VIL						$\begin{array}{c} V_{CC} \\ \times \ 0.35 \end{array}$	_	$\begin{array}{c} V_{CC} \\ \times \ 0.35 \end{array}$	
input voltage				1.65~1.95			V _{CC} × 0.35		V _{CC} × 0.35	
				2.3~2.7	_	_	0.7		0.7	
				3.0~3.6			0.8		0.8	
		VIN = VIH or VIL	I _{OH} =-0.02 mA	0.9	0.75		—	0.75		V
High-level VOH output voltage			I _{OH} = -0.3 mA	1.1~1.3	V _{CC} × 0.75			V _{CC} × 0.75		
	V _{OH}		I _{OH} = -1.7 mA	1.4~1.6	V _{CC} × 0.75			V _{CC} × 0.75		
		I _{OH} = -3.0 mA	1.65~ 1.95	V _{CC} -0.45			V _{CC} -0.45			
			I _{OH} = -4.0 mA	2.3~2.7	2.0		_	2.0		
			I _{OH} = -8.0 mA	3.0~3.6	2.48		_	2.48		
			I _{OL} = 0.02 mA	0.9	_	_	0.1	—	0.1	
Low-level V _{OL} output voltage		I _{OL} = 0.3 mA	1.1~1.3	_	_	$\begin{array}{c} V_{CC} \\ \times \ 0.25 \end{array}$	_	$\begin{array}{c} V_{CC} \\ \times \ 0.25 \end{array}$		
	V _{OL}	V _{IN} = V _{IH}	I _{OL} = 1.7 mA	1.4~1.6	_	_	$\begin{array}{c} V_{CC} \\ \times \ 0.25 \end{array}$	—	$\begin{array}{c} V_{CC} \\ \times \ 0.25 \end{array}$	V
			I _{OL} = 3.0 mA	1.65~ 1.95		_	0.45	—	0.45	
			I _{OL} = 4.0 mA	2.3~2.7	_		0.4	—	0.4	
			I _{OL} = 8.0 mA	3.0~3.6	_		0.4	—	0.4	
Input leakage current	I _{IN}	V _{IN} = 0~5.5V		0~3.6			±0.1	_	±1.0	μA
Power off leakage current	I _{OFF}	V _{IN} = 0~5.5 V _{OUT} = 0~3		0	_	_	1.0	_	10.0	μΑ
Quiescent supply current	Icc	$V_{IN} = V_{CC}$	or GND	3.6	_		1.0		10.0	μΑ

AC Electrical Characteristics (input $t_r = t_f = 3 \text{ ns}$)

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = −40~85°C		Unit
			V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
		$C_L = 10 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9		26.9		_	—	
			1.1~1.3		10.9	20.7	1.0	38.6	
			1.4~1.6		5.9	9.6	1.0	11.3	
			1.65~ 1.95	_	4.5	7.0	1.0	7.5	
			2.3~2.7	_	2.9	4.4	1.0	4.9	
			3.0~3.6	_	2.2	3.5	1.0	4.1	
Propagation delay time			0.9	_	30.0			_	
	tрLH tpHL	$C_L = 15 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	1.1~1.3		12.0	24.2	1.0	42.0	ns
			1.4~1.6		6.5	10.5	1.0	12.6	
			1.65~ 1.95	_	5.0	7.7	1.0	8.0	
			2.3~2.7	_	3.2	4.9	1.0	5.6	
			3.0~3.6	_	2.5	3.8	1.0	4.4	
		$C_L = 30 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9	_	45.0	_	_	—	
			1.1~1.3	_	18.0	33.4	1.0	63.2	
			1.4~1.6	_	8.9	14.8	1.0	17.9	
			1.65~ 1.95	_	6.9	10.3	1.0	10.8	
			2.3~2.7	_	4.4	6.4	1.0	6.8	
			3.0~3.6	_	3.5	4.9	1.0	5.4	
Input capacitance	C _{IN}	—	3.6	—	3		_	—	pF
Power dissipation capacitance	C _{PD}	(Note 13)	0.9~3.6	—	6		_	—	pF

Note 13: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

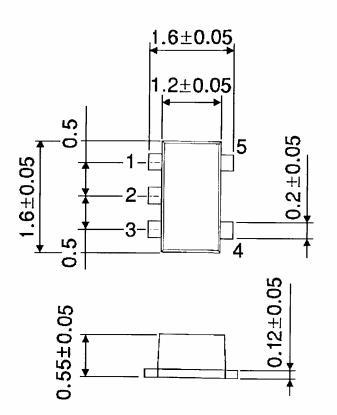
 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

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Package Dimensions

SON5-P-0.50

Unit : mm



Weight: 0.003 g (typ.)

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20070701-EN GENERAL

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