TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7WH157FC

2-Channel Multiplexer

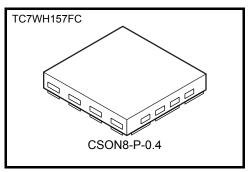
Features

 $\begin{array}{ll} \bullet & \text{High-speed} & :t_{pd} = 4.1 \text{ ns (Typ.) at V}_{CC} = 5 \text{ V} \\ \bullet & \text{Low power dissipation} & :I_{CC} = 2 \mu \text{A(Max.) at Ta} = 25 ^{\circ}\text{C} \\ \bullet & \text{High noise immunity} & :V_{\text{NIH}} = V_{\text{NIL}} = 28 \% \text{V}_{\text{CC}} \text{ (Min.)} \\ \end{array}$

 $:V_{CC}(opr) = 2~5.5 V$

• 5.5-V Tolerant inputs.

Operation voltage range



Weight: 0.002g (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Ratingh	Unit
Power supply viltage	V _{CC}	-0.5~7.0	V
DC input voltage	V _{IN}	-0.5~7.0	V
DC output voltage	V _{OUT}	-0.5~V _{CC} + 0.5 (Note1)	V
Input diode current	I _{IK}	-20	mA
Output diode current	lok	±20 (Note2)	mA
DC output current	lout	±25	mA
DC V _{CC} /GND current	Icc	±50	mA
Power dissipation	PD	150 (Note3)	mW
Storage temperature	T _{stg}	-65~150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note1: High or Low State.

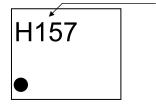
IOUT absolute maximum rating must be observed.

Note2 : $V_{OUT} < GND$, $V_{OUT} > V_{CC}$ Note3 : Mounted on an FR4 board.

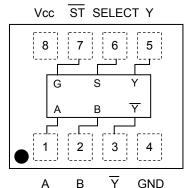
 $(25.4 \text{ mm} \times 25.4 \text{ mm} \times 1.6 \text{ t}, \text{ Cu Pad: } 11.56 \text{ mm}^2)$

Marking

Product name



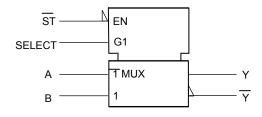
Pin Assignment (top view)



Truth Table

	Input	Outputs				
ST	SELECT	Α	В	Υ	Y	
Н	X	Х	Х	L	Н	
L	L	L	Х	L	Н	
L	L	Η	Х	Η	Ш	
L	Н	Х	L	L	Н	
L	Н	Х	Η	Η	L	

IEC Logic Diagram



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Operating Ranges

Characteristics	Symbol	Rathing	Unit	
Power supply voltage	V _{CC}	2~5.5	V	
Input voltage	V _{IN}	0~5.5	V	
Output voltage	V _{OUT}	0~V _{CC}	V	
Operating temperature	T _{opr}	-40~85	°C	
Input rise and fall time	dt/dv	$0\sim100~(V_{CC}=3.3~V\pm0.3~V)$	ns/V	
input rise and rail time	di/dv	0~20 (V _{CC} = 5 V ± 0.5 V)		

DC Electrical Characteristics

Characteristic	Cumbal	Took oom diking			Ta = 25°C			Ta = -40~85°C		Unit
Characteristic	Symbol	rest	Test condition		Min.	Тур.	Max.	Min.	Max.	Unit
High-level input voltage				2.0	1.5	_	_	1.5	_	V
	V _{IH}		_		V _{CC} × 0.7	_	_	V _{CC} × 0.7	_	
Low-level input voltage		_		2.0	_	_	0.5	_	0.5	
	V _{IL}			3.0~5.5	_		V _{CC} × 0.3	_	V _{CC} × 0.3	
	Voн	V _{IN} = V _{IL} or V _{IH}	I _{OH} = -50 μA	2.0	1.9	2.0	_	1.9	_	V
				3.0	2.9	3.0	_	2.9	_	
High-level output voltage				4.5	4.4	4.5	_	4.4	_	
			I _{OH} = -4 mA	3.0	2.58	_	_	2.48	_	
			I _{OH} = -8 mA	4.5	3.94	_	_	3.80	_	
	V _{OL}	V _{IN} = V _{IL} or V _{IH}	I _{OL} = 50 μA	2.0	_	0.0	0.1	_	0.1	
Low-level output voltage				3.0	_	0.0	0.1	_	0.1	
				4.5	_	0.0	0.1		0.1	
			I _{OL} = 4 mA	3.0	_		0.36		0.44	
			I _{OL} = 8 mA	4.5	_	_	0.36	_	0.44	
Input leakage current	I _{IN}	V _{IN} = 5.5 V or GND		0~5.5	_	_	±0.1	_	±1.0	μΑ
Quiescent supply current	ICC	V _{IN} = V _{CC} or GND		5.5	_	_	2.0	_	20.0	μΑ

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AC Electrical Characteristics (Input: $t_r = t_f = 3 \text{ ns}$)

Characteristic	Symbol		Test condition		Ta = 25°C			Ta = -40~85°C		Unit
	Symbol	V _{CC} (V)	C _L (pF)	Min.	Тур.	Max.	Min.	Max.	Offic	
			3.3 ± 0.3	15	_	6.2	9.7	1.0	11.5	- ns
Propagation deley time	t_{pLH}			50	_	8.7	13.2	1.0	15.0	
$(A,B-Y,\overline{Y})$	t_{pHL}		50.05	15	_	4.1	6.4	1.0	7.5	
			5.0 ± 0.5	50	_	5.6	8.4	1.0	9.5	
Propagation deley time (SELECT-Y , \overline{Y})			3.3 ± 0.3	15	_	8.4	13.2	1.0	15.5	
	t _{pLH} t _{pHL}			50	_	10.9	16.7	1.0	19.0	
		5.0 ± 0.5	15	_	5.3	8.1	1.0	9.5	ns	
			5.0 ± 0.5	50	_	6.8	10.1	1.0	11.5	
Propagation deley time (ST-Y, Y)	t _{pLH}		3.3 ± 0.3	15	_	8.7	13.6	1.0	16.0	ns ns
				50	_	11.2	17.1	1.0	19.5	
	t_{pHL}		5.0 ± 0.5	15	_	5.6	8.6	1.0	10.0	
				50	_	7.1	10.6	1.0	12.0	
Input capacitance	C _{IN}		_		_	4	10	_	10	pF
Power dissipation capacitanse	C _{PD}		(Note 4)		_	20	_	_		pF

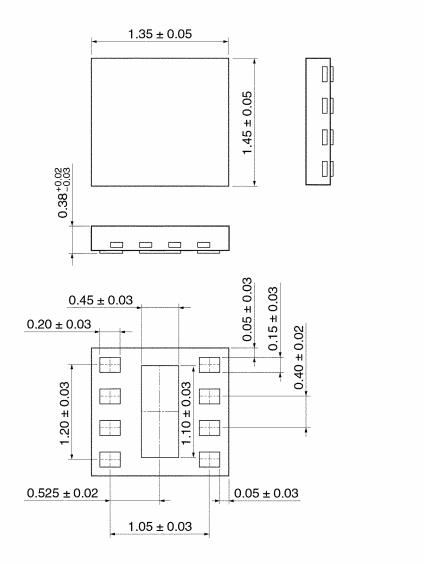
(Note 4): C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:



Package Dimensions

CSON8-P-0.4 Unit: mm



Weight: 0.002 g (Typ.)

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20070701-EN GENERAL

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