TOSHIBA Field-Effect Transistor Silicon P-Channel MOS Type

SSM3J306T

Power management switch Applications

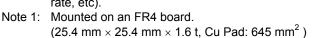
- 4 V drive
- Low ON-resistance: $R_{on} = 225 \text{ m}\Omega \text{ (max)} (@V_{GS} = -4 \text{ V})$ $R_{on} = 117 \text{ m}\Omega \text{ (max)} (@V_{GS} = -10 \text{ V})$

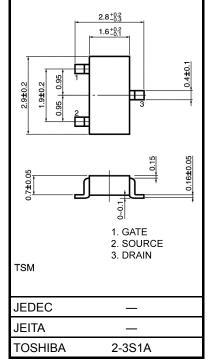
Absolute Maximum Ratings (Ta = 25°C)

Characteris	tic	Symbol	Rating	Unit	
Drain-source voltage		V _{DS}	-30	V	
Gate-source voltage		V _{GSS}	± 20	V	
Drain current	DC	۱ _D	-2.4	A	
	Pulse	I _{DP}	-4.8		
Drain power dissipation		PD (Note 1)	700	mW	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	–55 to 150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).





Weight: 10 mg (typ.)

Electrical Characteristics (Ta = 25°C)

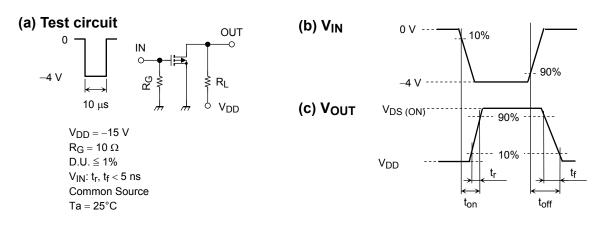
Charact	eristic	Symbol	Test Condition		Min	Тур.	Max	Unit
Drain-source breakdown voltage		V (BR) DSS	$I_D = -1 \text{ mA}, V_{GS} = 0$		-30	_	_	V
		V (BR) DSX	$I_D = -1 \text{ mA}, V_{GS} = +20 \text{ V}$	-15				
Drain cutoff current	t	I _{DSS}	$V_{DS} = -30 \text{ V}, \text{ V}_{GS} = 0$		_	_	-1	μA
Gate leakage curre	ent	I _{GSS}	$V_{GS}=\pm 16~V,~V_{DS}=0$		_	_	±1	μA
Gate threshold volt	age	V _{th}	$V_{DS} = -5 V, I_D = -1 mA$		-1.2		-2.6	V
Forward transfer a	dmittance	Y _{fs}	$V_{DS} = -5 V, I_D = -1 A$	(Note 2)	1.6	3.1		S
Drain-source ON-resistance		R _{DS (ON)}	$I_D = -1 \text{ A}, V_{GS} = -10 \text{ V}$	(Note 2)	—	80	117	mΩ
			$I_D = -0.5 \text{ A}, V_{GS} = -4 \text{ V}$	(Note 2)	_	160	225	
Input capacitance		C _{iss}			_	280	_	
Output capacitance Reverse transfer capacitance		C _{oss}	V _{DS} = –15 V, V _{GS} = 0, f = 1 MHz	_	80	_	pF	
		C _{rss}			—	45		
Total Gate Charge		Qg			—	2.5		
Gate-Source Charge		Q _{gs}	V _{DS} = -15 V, I _{DS} = -2.4 A V _{GS} = -4 V		—	1.3	_	nC
Gate-Drain Charge	ļ	Q _{gd} VGS4 V			_	1.2	_	
Switching time	Turn-on time	t _{on}	$V_{DD} = -15 \text{ V}, \text{ I}_D = -1 \text{ A},$ $V_{GS} = 0 \text{ to } -4 \text{ V}, \text{ R}_G = 10 \Omega$		_	16		
	Turn-off time	t _{off}			_	35		ns
Drain-source forwa	ard voltage	V _{DSF}	$I_D = 2.4 \text{ A}, V_{GS} = 0 \text{ V}$	(Note 2)	_	0.8	1.2	V

Note 2: Pulse test

Unit: mm

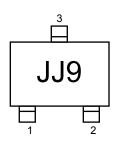
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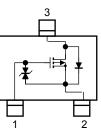
Switching Time Test Circuit



Marking

Equivalent Circuit (top view)





Precaution

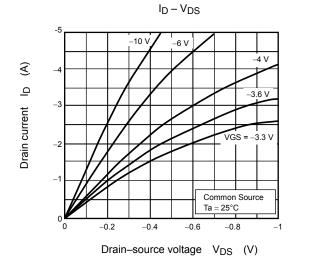
 V_{th} can be expressed as the voltage between gate and source when the low operating current value is $I_D = -1$ mA for this product. For normal switching operation, $V_{GS (on)}$ requires a higher voltage than V_{th} and $V_{GS (off)}$ requires a lower voltage than V_{th} .

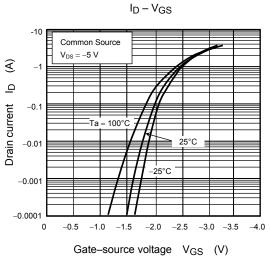
(The relationship can be established as follows: $V_{GS (off)} < V_{th} < V_{GS (on).}$) Take this into consideration when using the device.

Handling Precaution

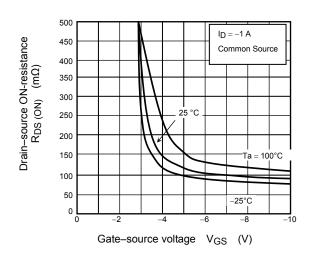
When handling individual devices that are not yet mounted on a circuit board, make sure that the environment is protected against electrostatic discharge. Operators should wear antistatic clothing, and containers and other objects that come into direct contact with devices should be made of antistatic materials.

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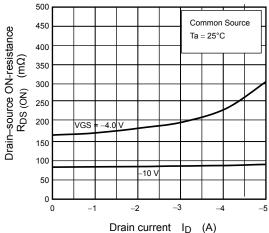


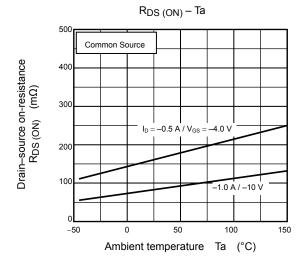




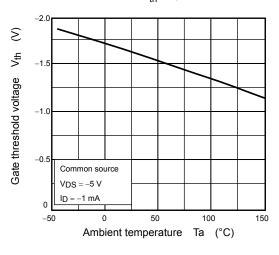


 $R_{DS(ON)} - I_D$

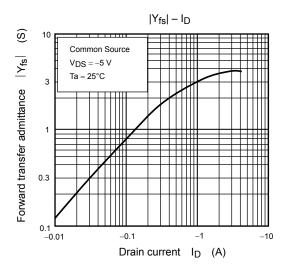


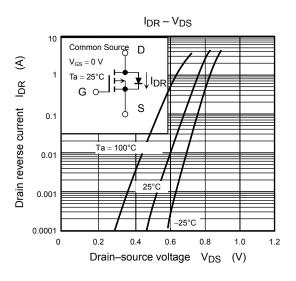


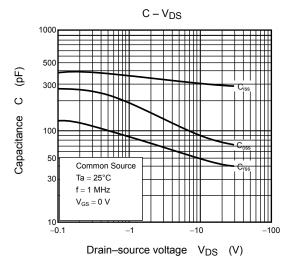
V_{th} – Ta

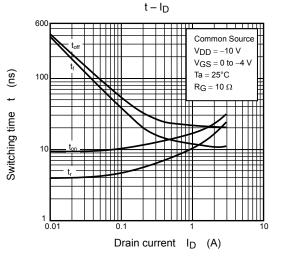


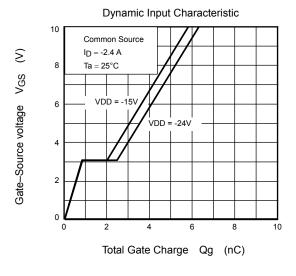
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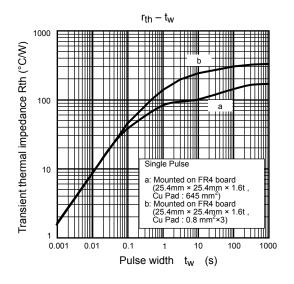


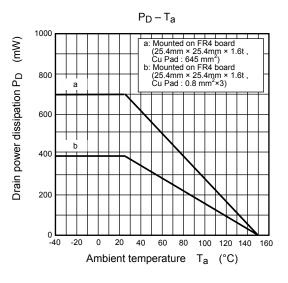






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