Triple Differential 2:1 Multiplexer

The MC10E457/100E457 is a 3-bit differential 2:1 multiplexer. The fully differential data path makes the device ideal for multiplexing low skew clock or other skew sensitive signals. Multiple VBB pins are provided to ease AC coupling input signals.

The higher frequency outputs provide the device with a >1.0GHz bandwidth to meet the needs of the most demanding system clock.

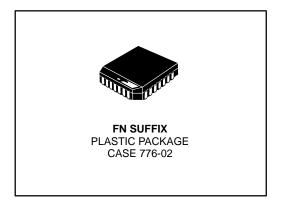
Both, separate selects and a common select, are provided to make the device well suited for both data path and random logic applications.

The differential inputs have internal clamp structures which will force the Q output of a gate in an open input condition to go to a LOW state. Thus, inputs of unused gates can be left open and will not affect the operation of the rest of the device. Note that the input clamp will take affect only if both inputs fall 2.5V below V_{CC} .

- Differential D and Q; VBB available
- 700ps Max. Propagation Delay
- High Frequency Outputs
- Separate and Common Select
- Extended 100E VEE Range of -4.2V to -5.46V
- Internal 75kΩ Input Pulldown Resistors

MC10E457 MC100E457

TRIPLE DIFFERENTIAL 2:1 MULTIPLEXER



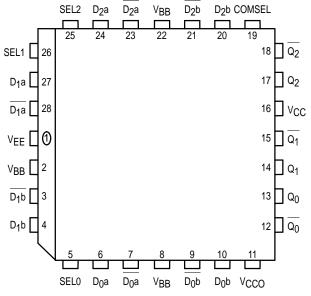
PIN NAMES

Pin	Function
Dn[0:2]; Dn[0:2]	Differential Data Inputs
SEL	Individual Select Input
COMSEL	Common Select Input
V _{BB} _	V _{BB} Reference Output
Q[0:2], Q[0:2]	Differential Data Outputs

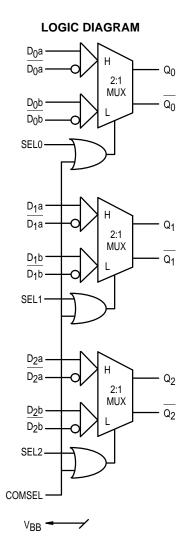
FUNCTION TABLE

SEL	Data				
Н	а				
L	b				

Pinout: 28-Lead PLCC (Top View)



* All V_{CC} and V_{CCO} pins are tied together on the die.



DC CHARACTERISTICS (VEE = VEE(min) to VEE(max); VCC = VCCO = GND)

		-40°C		0°C		25°C			85°C						
Symbol	Characteristic	Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	Unit	Cond
V _{BB}	Output Reference Voltage 10E 100E	-1.43 -1.38		-1.30 -1.26	-1.38 -1.38		-1.27 -1.26	-1.35 -1.38		-1.25 -1.26	-1.31 -1.38		-1.19 -1.26	V	
lіН	Input HIGH Current			150			150			150			150	μΑ	
lee	Power Supply Current 10E 100E		92 92	110 110		92 92	110 110		92 92	110 110		92 106	110 127	mA	
V _{PP} (DC)	Input Sensitivity	50			50			50			50			mV	1
VCMR	Commom Mode Range	-1.5		0	-1.5		0	-1.5		0	-1.5		0	V	2

Differential input voltage required to obtain a full ECL swing on the outputs.
 V_{CMR} is defined as the range within which the V_{IH} level may vary, with the device still meeting the propagation delay specification. The V_{IL} level must be such that the peak to peak voltage is less than 1.0 V and greater than or equal to V_{PP}(min).

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AC CHARACTERISTICS (VEE = VEE (min) to VEE (max); VCC = VCCO = GND)

		0°C			0	°C to 85°0			
Symbol	Characteristic	Min	Тур	Max	Min	Тур	Max	Unit	Condition
^t PLH ^t PHL	Propagation Delay to Output D (Differential) D (Single-Ended) SEL COMSEL	325 275 300 325	475 475 500 525	700 750 775 800	375 325 350 375	475 475 500 525	650 700 725 750	ps	
^t skew	Within-Device Skew		40			40		ps	1
^t skew	Duty Cycle Skew t _{PLH} - t _{PHL}		±10			±10		ps	2
V _{PP} (AC)	Minimum Input Swing	150			150			mV	3
t _r /t _f	Rise/Fall Time	125	275	500	150	275	450	ps	20–80%

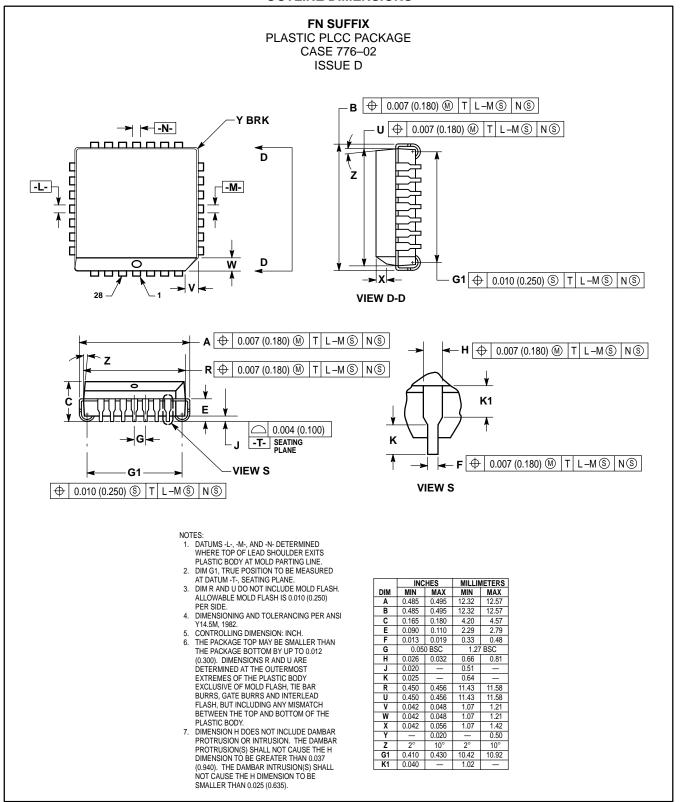
- 1. Within-device skew is defined as identical transitions on similar paths through a device.
- 2. Duty cycle skew is defined only for differential operation when the delays are measured from the cross point of the inputs to the cross point of the outputs.

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3. Minimum input swing for which AC parameters are guaranteed.

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OUTLINE DIMENSIONS



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