

MDLM117HV-H REV 0A0

MICROCIRCUIT DATA SHEET

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POSITIVE THREE TERMINAL HIGH VOLTAGE ADJUSTABLE REGULATOR

General Description

The LM117HV adjustable 3-terminal positive voltage regulator is capable of supplying in excess of 0.5A over a 1.2V to 57V output range. It is exceptionally easy to use and requires only two external resistors to set the output voltage. Further, both line and load regulation are are better than standard fixed regulators.

In addition to higher performance than fixed regulators, the LM117HV offers full overload protection available only in IC's. Included on the chip are current limit, thermal overload protection and safe area protection. All overload protection circuitry remains fully functional even if the adjustment terminal is disconnected.

Normally, no capacitors are needed unless the device is situated more than 6 inches from the input filter capacitors in which case an input bypass is needed. An optional output capacitor can be added to improve transient response. The adjustment terminal can be bypassed to achieve very high ripple rejection ratios which are difficult to achieve with standard 3-terminal regulators.

Besides replacing fixed regulators, the LM117HV is useful in a wide variety of other applications. Since the regulator is "floating" and sees only the input-to-output differential voltage, supplies of several hundred volts can be regulated as long as the maximum input to output differential is not exceeded, (i.e. do not short the output to ground).

Also, it makes an especially simple adjustable switching regulator, a programmable output regulator, or by connecting a fixed resistor between the adjustment and output, the LM17HV can be used as a precision current regulator. Supplies with electronic shutdown can be achieved by clamping the adjustment terminal to ground which programs the output to 1.2V where most loads draw little current.

Industry Part Number

NS Part Numbers

LM117HVH

LM117HVH-SMD

Prime Die

LM117HVH

Controlling Document

DESC.# 7703402XA

Processing

MIL-STD-883, Method 5004

Quality Conformance Inspection

 ${\tt MIL-STD-883,\ Method\ 5005}$

Subgrp	Description	Temp	(°C)
1 2 3 4 5 6 7 8A 8B 9 10	Static tests at Static tests at Static tests at Dynamic tests at Dynamic tests at Dynamic tests at Functional tests at Functional tests at Functional tests at Switching tests at Switching tests at Switching tests at	+25 +125 -55 +25 +125 -55 +125 -55 +125 -55 +25 +125 -55	

Features

- Adjustable output down to 1.2V.
- Guaranteed 0.5A output current.
- Line regulation typically 0.01%/V.
- Load regulation typically 0.1%.
- Current limit constant with temperature.
- Eliminates the need to stock many voltages.
- 80 dB ripple rejection.
- Output is short-circuit protected.

(Absolute Maximum Ratings)

(Note 1)

Power Dissipation Internally Limited

Maximum Junction Temperature 150 C

Storage Temperature Range

-65 C to +150 C

Lead Temperature (Soldering, 10 seconds) 300 C

Thermal Resistance

ThetaJA
(Still Air)
(500LF/Min Air flow)

186 C/W
64 C/W

ThetaJC 21 C/W

ESD Tolerance (Note 2) 2000V

Note 1: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is intended to be functional, but do not guarantee specific performance limits. For guaranteed specifications and test conditions, see the Electrical Characteristics. The

guaranteed specifications apply only for the test conditions listed. Note 2: Human body model, 1.5 K Ohms in series with 100 pF.

Recommended Operating Conditions

Operating Temperature Range

-55 C \leq TA \leq +125 C

Electrical Characteristics

DC PARAMETERS

(The following conditions apply to all the following parameters, unless otherwise specified.) DC: Il = 8mA

SYMBOL	PARAMETER	CONDITIONS	NOTES	PIN- NAME	MIN	MAX	UNIT	SUB- GROUPS
Vref Reference	Reference Voltage	Vdiff = 3V			1.2	1.3	V	1
		Vdiff = 40V			1.2	1.3	V	1, 2,
		Vdiff = 60V			1.2	1.3	V	1, 2,
		Vdiff = 3.3V			1.2	1.3	V	2, 3
Rline	Line Regulation	3V ≤ Vdiff ≤ 40V, Vout = Vref			-9	9	mV	1
		40V < Vdiff < 60V, Vout = Vref			-5	5	mV	1
		40V ≤ Vdiff ≤ 60V, Vout = Vref			-10	10	mV	2, 3
		3V ≤ Vdiff ≤ 60V			-14	14	mV	1
		3V ≤ Vdiff ≤ 60V			-33	33	mV	2, 3
		3.3V ≤ Vdiff ≤ 40V, Vout = Vref			-23	23	mV	2, 3
Rload	Load Regulation	Vdiff = 3V, 10mA ≤ I1 ≤ 500mA			-15	15	mV	1
		Vdiff = 40V, 10mA ≤ Il ≤ 150mA			-15	15	mV	1
		Vdiff = 60V, 10mA ≤ I1 ≤ 20mA			-15	15	mV	1, 2,
		Vdiff = 3.3V, 10mA ≤ I1 ≤ 500mA			-15	15	mV	2, 3
		Vdiff = 40V, 10mA ≤ Il ≤ 100mA			-15	15	mV	2, 3
Vrth	Thermal Regulation	Vin = 14.6V, I1 = 300mA, Pd = 4W, t = 20mS			-3.1	3.1	mV	1
Iadj Adju	Adjustment Pin	Vdiff = 3V				100	uA	1
	Current	Vdiff = 40V				100	uA	1, 2,
		Vdiff = 60V				100	uA	1, 2,
		Vdiff = 3.3V				100	uA	2, 3
Delta Iadj		Vdiff = 3V, 10mA ≤ I1 ≤ 500mA			-5	5	uA	1
	Current Change	Vdiff = 40V, 10mA ≤ Il ≤ 150mA			-5	5	uA	1
		3V ≤ Vdiff ≤ 40V			-5	5	uA	1
		3.3V ≤ Vdiff ≤ 60V			-5	5	uA	1, 2,
		Vdiff = 3.3V, 10mA ≤ I1 ≤ 500mA			-5	5	uA	2, 3
		Vdiff = 40V, 10mA ≤ I1 ≤ 100mA			-5	5	uA	2, 3
		3.3V ≤ Vdiff ≤ 40V			-5	5	uA	2, 3

Electrical Characteristics

DC PARAMETERS (Continued)

(The following conditions apply to all the following parameters, unless otherwise specified.) DC: Il = 8mA

SYMBOL	PARAMETER	CONDITIONS	NOTES	PIN- NAME	MIN	MAX	UNIT	SUB- GROUPS
Ilmin Minimum Load	Minimum Load Current	Vdiff = 3V, Vout = 1.4V (forced)				5	mA	1
	current	Vdiff = 40V, Vout = 1.4V (forced)				5	mA	1, 2,
		Vdiff = 60V, Vout = 1.4V (forced)				7	mA	1, 2,
		Vdiff = 3.3V, Vout = 1.4V (forced)				5	mA	2, 3
Icl Curre	Current Limit	Vdiff = 5V			.5	1.65	A	1, 2,
		Vdiff = 40V			.15	.65	A	1
		Vdiff = 60V			.02	. 28	A	1

AC PARAMETERS

(The following conditions apply to all the following parameters, unless otherwise specified.) AC: Il = 8mA

Rn	Ripple Rejection	f = 120Hz, Vout = Vref, Cadj = 10uF,	1	66	dВ	4, 5,
		Iout = 100mA				6

Note 1: Group "A" sample only, test at all temperature.

Graphics and Diagrams

GRAPHICS#	DESCRIPTION
9784HRB1	3LD .200 DIA P.C. METAL CAN PKG (B/I CKT)
H03ARD	3LD .200 DIA P.C. METAL CAN PKG (P/P DWG)

See attached graphics following this page.

