

# BIPOLAR ANALOG INTEGRATED CIRCUIT $\mu PC317$

#### 3-TERMINAL POSITIVE ADJUSTABLE REGULATOR

#### **DESCRIPTION**

The  $\mu$ PC317 is an adjustable 3-terminal positive voltage regulator, which has 1.5 A capable for the output current. The output voltage can be set any value between 1.3 V and 30 V by two external resistors.

#### **FEATURES**

- · Output current excess of 1.5 A
- On-chip some protection circuit (over current protection, SOA protection and thermal shut down).

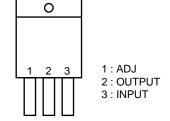
#### **PIN CONFIGURATION (Marking Side)**

3-pin plastic SIP (MP-45G)

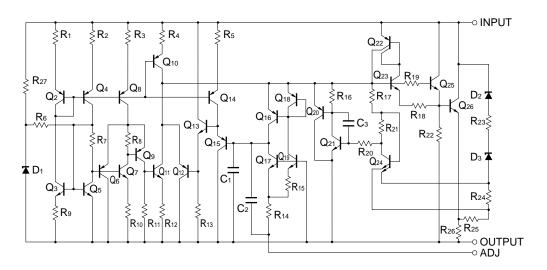
μPC317HF

#### ORDERING INFORMATION

Part NumberPackage $\mu$ PC317HF3-pin plastic SIP (MP-45G) (isolated TO-220)



#### **EQUIVALENT CIRCUIT**



The information in this document is subject to change without notice. Before using this document, please confirm that this is the latest version. Not all devices/types available in every country. Please check with local NEC representative for availability and additional information.



#### ABSOLUTE MAXIMUM RATINGS (TA = 25°C, unless otherwise specified.)

Parameter	Symbol	Rating	Unit
Input-Output Voltage Differential	Vin-Vo	-0.3 to +40	٧
Total Power Dissipation (Tc = 25°C)	Рт	15 <sup>Note</sup>	W
Operating Ambient Temperature	TA	-20 to +80	°C
Operating Junction Temperature	TJ	-20 to +150	°C
Storage Temperature	Tstg	-65 to +150	°C
Thermal Resistance (junction to case)	Rth (J-C)	5	°C/W
Thermal Resistance (junction to ambient)	Rth (J-A)	65	°C/W

Note Internally limited.

When operating junction temperature rise up to 150°C (≤200°C), the internal circuit shutdown output voltage.

Caution Product quality may suffer if the absolute maximum rating is exceeded even momentarily for any parameter. That is, the absolute maximum ratings are rated values at which the product is on the verge of suffering physical damage, and therefore the product must be used under conditions that ensure that the absolute maximum ratings are not exceeded.

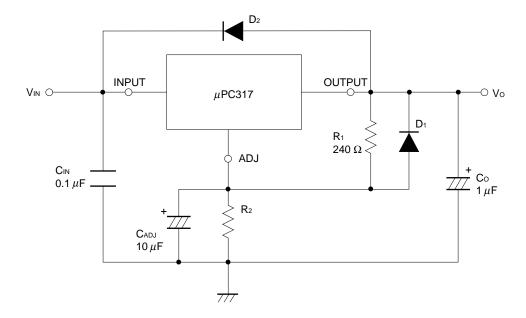
#### RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Input-Output Voltage Differential	Vin-Vo	3		38.7	V
Input Voltage	Vin	4.3		40	V
Output Voltage	Vo	1.3		30	V
Output Current	lo	0.01		1.5	Α
Operating Junction Temperature	TJ	-20		+125	°C

Caution The recommended operating range may be exceeded without causing any problems provided that the absolute maximum ratings are not exceeded. However, if the device is operated in a way that exceeds the recommended operating conditions, the margin between the actual conditions of use and the absolute maximum ratings is small, and therefore thorough evaluation is necessary. The recommended operating conditions do not imply that the device can be used with all values at their maximum values.



#### TYPICAL CONNECTION



Remark R<sub>1</sub>, R<sub>2</sub>: Resistor to set the output voltage.

$$Vo = (1 + \frac{R_2}{R_1}) \bullet Vref + Iadj \bullet R_2 = (1 + \frac{R_2}{R_1}) \bullet Vref$$

Vo (V)	R <sub>2</sub> (Ω : TYP.)
1.25	0
2.5	240
5.0	720
12	2064
24	4368
30	5520

C<sub>IN</sub>: Need to stop the oscillation for the long input wiring length.

Co : Need to stop the oscillation for the long output wiring length.

Improve the transient stability of the output voltage when the lord current is suddently changed.

CADJ: Improve the ripple rejection and the oscillate rejection.

D1 : Protect against CADJ from output short.

 $D_2$ : Need for  $V_{IN} < V_O$ .

Data Sheet G12826EJ3V0DS00



#### **ELECTRICAL CHARACTERISTICS**

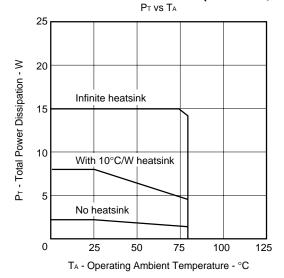
# (VIN – Vo = 5 V, lo = 0.5 A, $0^{\circ}$ C $\leq$ TJ $\leq$ +125 $^{\circ}$ C, unless otherwise specified.)

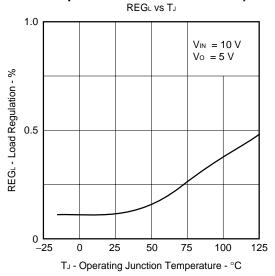
Parameter	Symbol	Conditions		MIN.	TYP.	MAX.	Unit
Line Regulation	REGIN	$T_{A} = 25^{\circ}C, \ 3 \ V \leq (V_{IN} - V_{O}) \leq 40 \ V, \ I_{O} = 0.1 \ A^{\text{Note}}$ $3 \ V \leq (V_{IN} - V_{O}) \leq 40 \ V, \ I_{O} = 0.1 \ A^{\text{Note}}$			0.01	0.04	%/V
					0.02	0.07	%/V
Load Regulation	REG∟	T <sub>J</sub> = 25°C	Vo≤5 V		5	25	mV
		10 mA ≤ lo ≤ 1.5 A <sup>Note</sup>	Vo ≥ 5 V		0.1	0.5	%
		10 mA ≤ lo ≤ 1.5 A <sup>Note</sup>	Vo≤5 V		20	70	mV
			Vo≥5 V		0.3	1.5	%
Thermal Regulation	REGTH	$T_A = 25^{\circ}C$ , 0.2 ms $\leq t \leq 20$ ms			0.01	0.07	%/W
ADJ pin Output Current	ladj				50	100	μΑ
IADJ Change	$\Delta I$ ADJ	10 mA ≤ Io ≤ 1.5 A, P <sub>T</sub> ≤ 15 W			0.4	5	μΑ
Reference Voltage	V <sub>REF</sub>	10 mA ≤ Io ≤ 1.5 A, P <sub>T</sub> ≤ 15 W		1.20	1.25	1.30	٧
Temperature Stability of VREF	$\Delta V_{REF}/\Delta T$				0.7		%
Minimum Load Current	Іомін.	V <sub>IN</sub> - V <sub>O</sub> = 40 V			4.7	10	mA
Peak Output Current	lOpeak	$5 \text{ V} \le (\text{V}_{\text{IN}} - \text{V}_{\text{O}}) \le 15 \text{ V}$		1.5	2.2	2.9	Α
		V <sub>IN</sub> - V <sub>O</sub> = 40 V		0.15	0.8		Α
Output Noise Voltage (RMS)	Vn	T <sub>A</sub> = 25°C, 10 Hz ≤ f ≤ 10 kHz			0.001		%
Ripple Rejection	R • R	$T_A = 25$ °C, $\Delta V_{IN} = 1 V_{r.m.s}$	Cadu = 0		48		dB
		f = 120 Hz, Vo = 10 V	C <sub>ADJ</sub> = 10 μF	56	65		dB

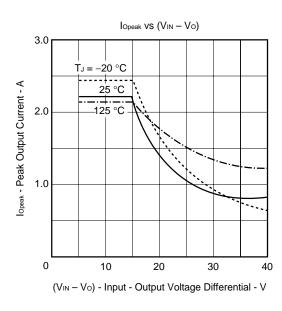
**Note** Measured at constant junction temperature, using pulse testing with a low duty cycle. PW = 10 ms, Duty Cycle  $\leq 2 \%$ 

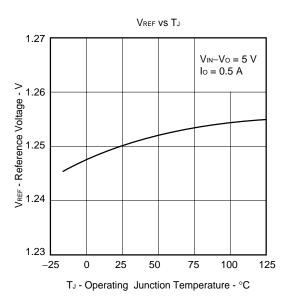


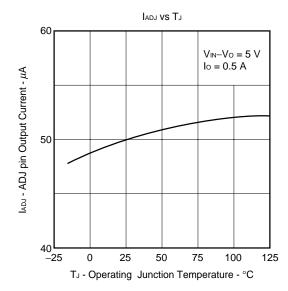
## TYPICAL CHARACTERISTICS (TA = 25°C, unless otherwise specified. Reference Values.)

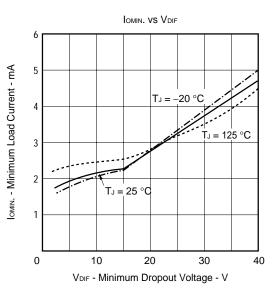


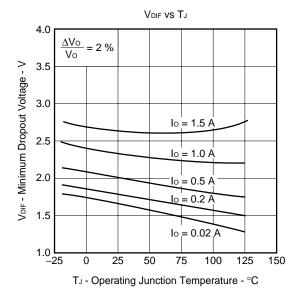


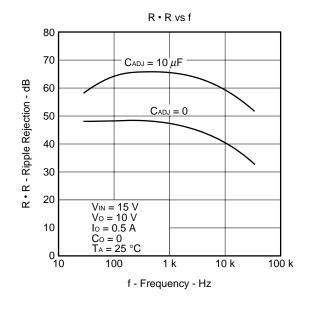


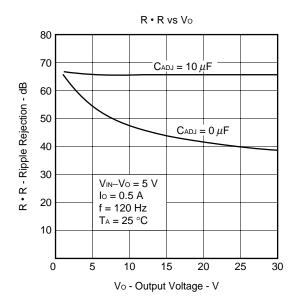


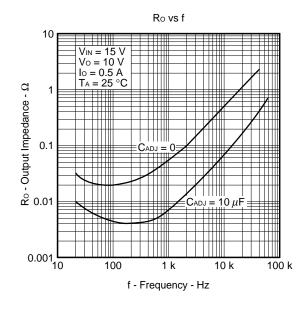


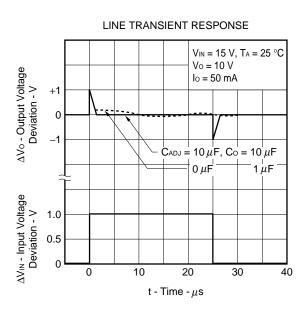


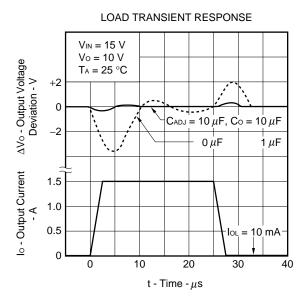








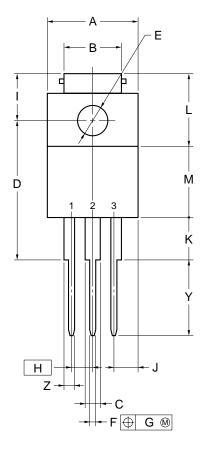


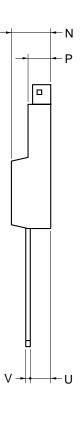




# **PACKAGE DRAWING**

# **3PIN PLASTIC SIP (MP-45G)**





### NOTE

Each lead centerline is located within 0.25 mm of its true position (T.P.) at maximum material condition.

ITEM	MILLIMETERS
Α	10.0±0.2
В	7.0±0.2
С	1.50±0.2
D	17.0±0.3
Е	$\phi$ 3.3±0.2
F	0.75±0.10
G	0.25
Н	2.54 (T.P.)
I	5.0±0.3
J	2.46±0.2
K	5.0±0.2
L	8.5±0.2
М	8.5±0.2
N	4.5±0.2
Р	2.8±0.2
U	2.4±0.5
V	0.65±0.10
Υ	8.9±0.7
Z	1.30±0.2

P3HF-254B-4



#### RECOMMENDED SOLDERING CONDITIONS

When soldering these products, it is highly recommended to observe the conditions as shown below. If other soldering processes are used, or if the soldering is performed under different conditions, please make sure to consult with our sales offices.

For more details, refer to our document "SEMICONDUCTOR DEVICE MOUNTING TECHNOLOGY MANUAL" (C10535E).

## **Type of Through-hole Devices**

 $\mu$ PC317HF: 3-pin plastic SIP (MP-45G)(isolated TO-220)

Process	Conditions
Wave soldering (only to leads)	Solder temperature: 260°C or below, Flow time: 10 seconds or less.
Partial heating method	Pin temperature: 300°C or below, Heat time: 3 seconds or less (per each lead).

Caution For through-hole device, the wave soldering process must be applied only to leads, and make sure that the package body does not get jet soldered.

#### REFERENCE DOCUMENTS

QUALITY GRADES ON NEC SEMICONDUCTOR DEVICES	C11531E
SEMICONDUCTOR DEVICE MOUNTING THCHNOLOGY MANUAL	C10535E
SEMICONDUCTORS SELECTION GUIDE – Products and Packages – (CD-ROM)	X13769X
SEMICONDUCTORS SELECTION GUIDE	X10679E
NEC SEMICONDUCTOR DEVICE RELIABILITY/QUALITY CONTROL SYSTEM	IEI-1212
-THREE TERMINAL REGULATOR	

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