TOSHIBA Bipolar Digital Integrated Circuit Silicon Monolithic

## TD62064APG, TD62064AFG, TD62074APG, TD62074AFG

### 4ch High-Current Darlington Sink Driver

The TD62064APG/AFG and TD62074APG/AFG are high-voltage, high-current darlington drivers comprised of four NPN darlington pairs.

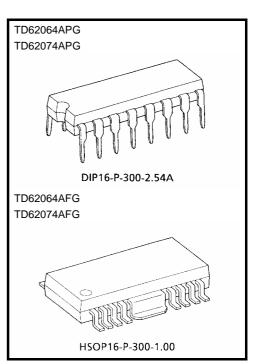
All units feature integral clamp diodes for switching inductive loads and all units of TD62074APG/AFG feature uncommitted collectors and emitters for isolated darlington applications.

For proper operation, the substrate (SUB) must be connected to the most negative voltage.

Applications include relay, hammer, lamp and stepping motor drivers.

### Features

- Output current (single output) 1.5 A (max)
- High sustaining voltage output
   50 V (min) (TD62064APG/AFG, 074APG/AFG)
- Output clamp diodes: TD62064APG/AFG
- Isolated darlington array: TD62074APG/AFG
- Input compatible with TTL and 5 V CMOS
- GND and SUB terminal = Heat sink
- Package type-APG: DIP-16 pin
- Package type-AFG: HSOP-16 pin

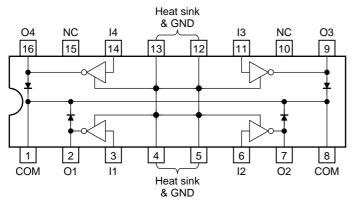


Weight DIP16-P-300-2.54A: 1.11 g (typ.) HSOP16-P-300-1.00: 0.50 g (typ.)

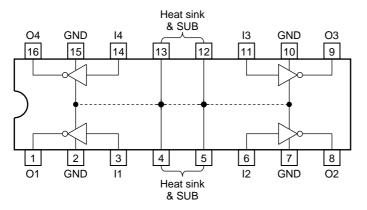
# <u>TOSHIBA</u>

### Pin Assignment (top view)

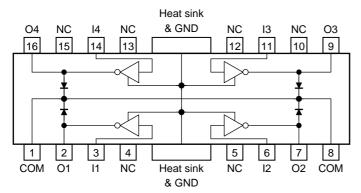
#### TD62064APG



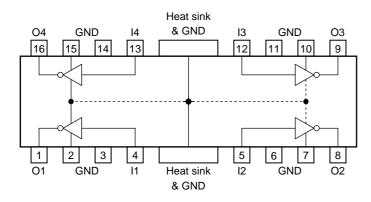
#### **TD62074APG**



#### TD62064AFG

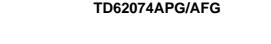


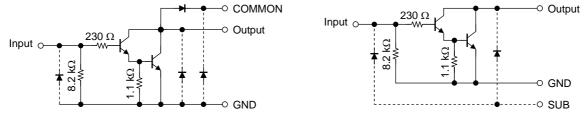
#### TD62074AFG



#### Schematics (each driver)

#### TD62064APG/AFG





Note: The input and output parasitic diodes cannot be used as clamp diodes.

### **Precautions for Using**

- (1) This IC does not include built-in protection circuits for excess current or overvoltage.
   If this IC is subjected to excess current or overvoltage, it may be destroyed.
   Hence, the utmost care must be taken when systems which incorporate this IC are designed.
   Utmost care is necessary in the design of the output line, COMMON and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.
- (2) If a TD62064APG/AFG is being used to drive an inductive load (such as a motor, solenoid or relay), Toshiba recommends that the diodes (pins 1 and 8) be connected to the secondary power supply pin so as to absorb the counter electromotive force generated by the load. Please adhere to the device's maximum ratings. Toshiba recommends that zener diodes be connected between the diodes (pins 1 and 8) and the secondary power supply pin (as the anode) so as to enable rapid absorption of the counter electromotive force. Again, please adhere to the device's maximum ratings.

If a TD62074APG/AFG is being used to drive an inductive load (such as a motor, solenoid or relay), Toshiba recommends that a diode be connected between the output pin (as the anode) and the secondary power supply pin. Please adhere to the device's maximum ratings.

| Characteristics             |                       | Symbol                       | Rating               | Unit |  |
|-----------------------------|-----------------------|------------------------------|----------------------|------|--|
| Output sustaining voltage   |                       | V <sub>CE (SUS)</sub>        | -0.5 to 50           | V    |  |
| Output current              |                       | lout                         | 1.5                  | A/ch |  |
| Input current               |                       | I <sub>IN</sub>              | 50                   | mA   |  |
| Input voltage               |                       | V <sub>IN</sub>              | -0.5 to 17           | V    |  |
| Clamp diode reverse voltage |                       | V <sub>R</sub> (Note 1)      | 50                   | V    |  |
| Clamp diode forward current |                       | I <sub>F</sub> (Note 1)      | 1.5                  | А    |  |
| Isolated voltage            |                       | V <sub>SUB</sub><br>(Note 2) | 50                   | V    |  |
| Dower dissinction           | APG                   | D-                           | 1.47/2.7<br>(Note 3) | W    |  |
| Power dissipation           | AFG                   | P <sub>D</sub>               | 0.9/1.4<br>(Note 4)  | vv   |  |
| Operating temperature       | Operating temperature |                              | -40 to 85            | °C   |  |
| Storage temperature         |                       | T <sub>stg</sub>             | -55 to 150           | °C   |  |

#### Maximum Ratings (Ta = 25°C)

Note 1: TD62064APG/AFG

Note 2: TD62074APG/AFG

Note 3: On glass epoxy PCB (50  $\times$  50  $\times$  1.6 mm Cu 50%)

Note 4: On glass epoxy PCB ( $60 \times 30 \times 1.6 \text{ mm Cu } 30\%$ )

## Recommended Operating Conditions (Ta = -40 to $85^{\circ}C$ )

| Characteristics             |              | Symbol                | Test Condition   |            | Min | Тур. | Max  | Unit  |
|-----------------------------|--------------|-----------------------|--|------------|-----|------|------|-------|
| Output sustaining voltage   |              | V <sub>CE</sub> (SUS) |  |            | 0   |      | 50   | V     |
|                             |              | lout                  | DC1 circuit, Ta = 25°C   |            | 0   |      | 1250 |       |
|                             | APG (Note 1) |                       | $T_{pw} = 25 ms$<br>4 circuits<br>$T_j = 120^{\circ}C$<br>$Ta = 85^{\circ}C$ | Duty = 10% | 0   | _    | 1250 | mA/ch |
| Output current              |              |                       |  | Duty = 50% | 0   | _    | 390  |       |
|                             | AFG (Note 2) |                       |  | Duty = 10% | 0   |      | 907  |       |
|                             |              |                       |  | Duty = 50% | 0   |      | 172  |       |
|                             | <u> </u>     |                       | —  |            | 0   |      | 8    |       |
| Input voltage               | Output ON    | V <sub>IN (ON)</sub>  | I <sub>OUT</sub> = 1.25 A  |            | 2.5 |      | 8    | V     |
|                             | Output OFF   | VIN (OFF)             | —  |            | 0   |      | 0.4  |       |
| Input current               |              | l <sub>IN</sub>       | —  |            | 0   |      | 20   | mA    |
| Clamp diode reverse voltage |              | VR                    | TD62064APG/AFG   |            | 0   |      | 50   | V     |
| Clamp diode forward current |              | ١ <sub>F</sub>        | —  |            | _   |      | 1.25 | А     |
| Isolation voltage           |              | V <sub>SUB</sub>      | TD62074APG/AFG   |            | _   |      | 50   | V     |
| Dower dissinction           | APG          | PD                    | Ta = 85°C  | (Note 1)   | _   | _    | 1.4  | W     |
| Power dissipation           | AFG          |                       | Ta = 85°C  | (Note 2)   | —   |      | 0.7  | vv    |

Note 1: On glass epoxy PCB ( $50 \times 50 \times 1.6$  mm Cu 50%)

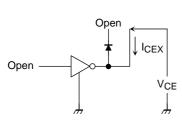
Note 2: On glass epoxy PCB (60  $\times$  30  $\times$  1.6 mm Cu 30%)

### **Electrical Characteristics (Ta = 25°C)**

| Characteristics             | Symbol                            | Test<br>Circuit | Test Condition   |                           | Min | Тур. | Max  | Unit |
|-----------------------------|-----------------------------------|-----------------|--|---------------------------|-----|------|------|------|
| Output leakage current      | V <sub>CE</sub> = 50 V, Ta = 25°C |                 | a = 25°C   | _                         | _   | 50   |      |      |
| Output leakage current      | ICEX                              | 1               | V <sub>CE</sub> = 50 V, Ta = 85°C  |                           |     | _    | 500  | μA   |
|                             | V <sub>CE</sub> (sat)             | 2               | I <sub>OUT</sub> = 1.25 A, I <sub>IN</sub> = 2 mA  |                           |     | _    | 1.6  | v    |
| Output saturation voltage   |                                   |                 | I <sub>OUT</sub> = 0.75 A, I <sub>IN</sub> = 935 μA  |                           | _   | _    | 1.25 |      |
| DC current transfer ratio   | h <sub>FE</sub>                   | 2               | $V_{CE} = 2 V$   | I <sub>OUT</sub> = 1.0 A  | _   | 800  | _    |      |
| DC current transfer fatio   |                                   |                 |  | I <sub>OUT</sub> = 0.25 A |     | 1500 | _    |      |
| Input voltage (output on)   | V <sub>IN (ON)</sub>              | 3               | I <sub>OUT</sub> = 1.25 A, I <sub>IN</sub> = 2 mA  |                           |     | _    | 2.4  | V    |
| Clama diada laskaga surrant | ۱ <sub>R</sub>                    | 4               | V <sub>R</sub> = 50 V, Ta = 25°C   |                           | _   | _    | 50   | μA   |
| Clamp diode leakage current |                                   |                 | V <sub>R</sub> = 50 V, Ta = 85°C   |                           |     | _    | 100  |      |
| Clamp diode forward voltage | VF                                | 5               | I <sub>F</sub> = 1.25 A  |                           |     | _    | 2.0  | V    |
| Input capacitance           | C <sub>IN</sub>                   | 6               | $V_{IN} = 0 V$ , f = 1 MHz   |                           |     | 15   | _    | pF   |
| Turn-ON delay               | t <sub>ON</sub>                   | 7               | $\begin{array}{l} C_L = 15 \text{ pF},  V_{OUT} = 50 \text{ V}, \\ R_L = 42  \Omega \end{array}$ |                           | _   | 0.1  |      | μs   |
| Turn-OFF delay              | tOFF                              | 7               | $\begin{array}{l} C_L = 15 \text{ pF},  V_{OUT} = 50  \text{V}, \\ R_L = 42  \Omega \end{array}$ |                           | _   | 1.0  |      | μS   |

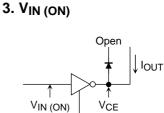
## **Test Circuit**

#### 1. ICEX



UIN VCE, VCE (sat)

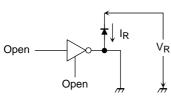
2. V<sub>CE (sat)</sub>, h<sub>FE</sub>

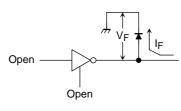


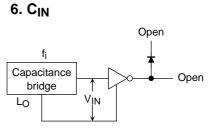
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4. I<sub>R</sub>



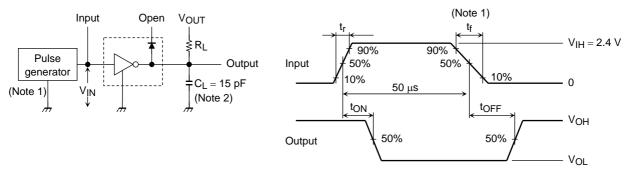






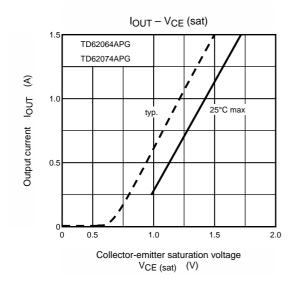
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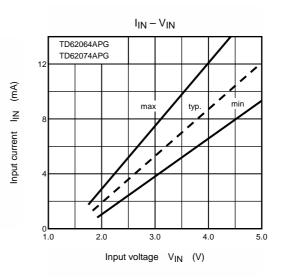
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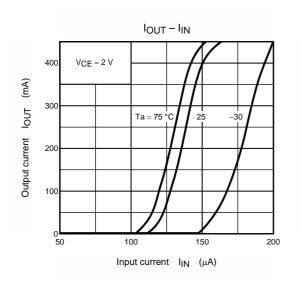


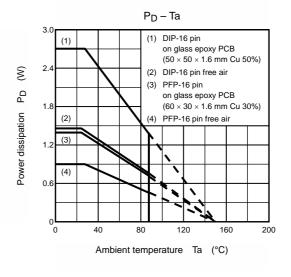
Note 1: Pulse Width 50  $\mu$ s, Duty Cycle 10% Output Impedance 50  $\Omega$ , t<sub>r</sub>  $\leq$  5 ns, t<sub>f</sub>  $\leq$  10 ns

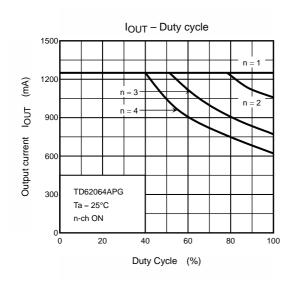
Note 2: CL includes probe and jig capacitance

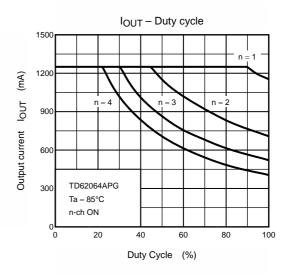


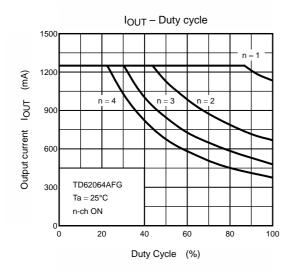


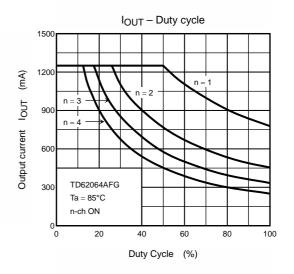




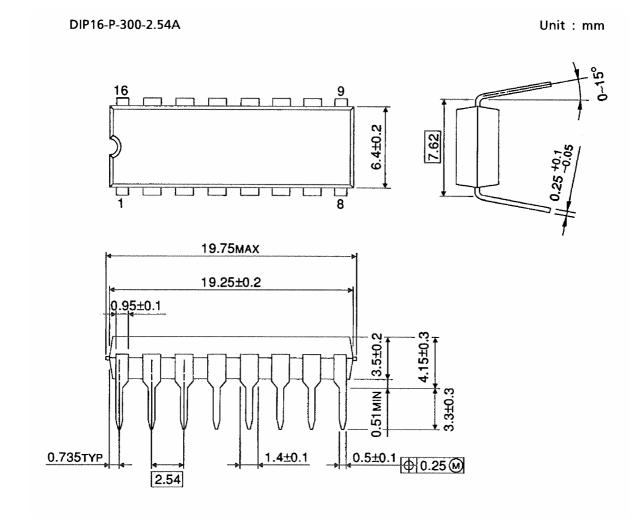






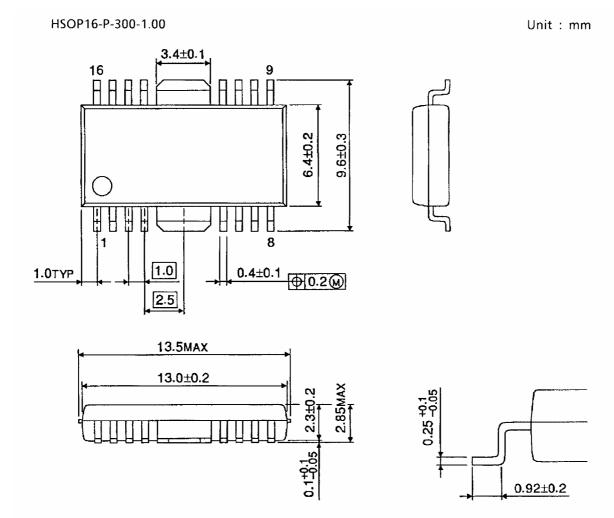


### **Package Dimensions**



Weight: 1.11 g (typ.)

## **Package Dimensions**



Weight: 0.50 g (typ.)

| About solderability, following conditions were confirmed |   |  |  |
|--|---|--|--|
|  | Solderability   |  |  |
|  | <ul> <li>(1) Use of Sn-63Pb solder Bath <ul> <li>solder bath temperature = 230°C</li> <li>dipping time = 5 seconds</li> <li>the number of times = once</li> <li>use of R-type flux</li> </ul> </li> </ul> |  |  |
|  | <ul> <li>(2) Use of Sn-3.0Ag-0.5Cu solder Bath</li> <li>solder bath temperature = 245°C</li> <li>dipping time = 5 seconds</li> <li>the number of times = once</li> </ul>                                  |  |  |

• use of R-type flux

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