Keypad Multiplexer

The NLSF2500 is a keyboard multiplexer fabricated in sub–micron silicon CMOS Technology. The NLSF2500 is designed to operate over wide operating voltage, with minimum power consumption and very low voltage drop from $V_{\rm CC}$. The device saves dozens of active and passive components and permits operating voltage far lower than the standard diode scheme.

Features

- Single Supply Operation
- \bullet Optimized for 1.8 V to 3.6 V V_{CC}
- Tiny 3 x 3 mm QFN-16 Package
- Conforms to: JEDEC MO-220, Issue H, Variation VEED-6
- Very Low Voltage Drop
- Permits Operation Down to 1.65 V
- Near Zero Static Power
- ESD Protection: Human Body Model (HBM); > 3000 V, Machine Model (MM); >300 V
- Latchup Maximum Rating: 200 mA
- Pin-to-Pin Compatible with CM2500
- This is a Pb-Free Device

Typical Applications

- Cell Phones
- PDAs
- MP3 players



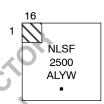
ON Semiconductor®

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MARKING DIAGRAM



QFN-16 CASE 485AE



XXXX = Specific Device Code

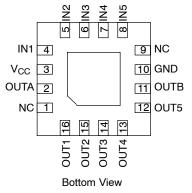
A = Assembly Location

= Wafer Lot

= Year = Work Week

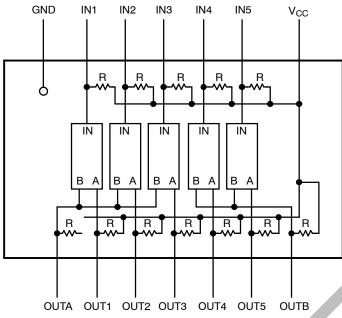
= Pb-Free Package

PIN CONNECTIONS



ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 4 of this data sheet.



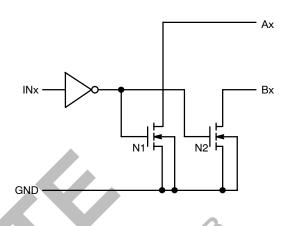


Figure 1. Device Block Diagram

Figure 2. Circuit Schematic (1 Channel Pair Only)

PIN DESCRIPTION

Pin	Name	Function Description	
1	NC	Not Internally Connected	
2	OUTA	Combined "Functional OR" Output of IN1, IN2, and IN3	
3	V _{CC}	Supply Pin	
4	IN1	Input 1 from Switch to be Multiplexed	
5	IN2	Input 2 from Switch to be Multiplexed	
6	IN3	Input 3 from Switch to be Multiplexed	
7	IN4	Input 4 from Switch to be Multiplexed	
8	IN5	Input 5 from Switch to be Multiplexed	
9	NC	Not Internally Connected	
10	GND	Ground	
11	OUTB	Combined "Functional OR" Output of IN4 and IN5	
12	OUT5	Output 5 for Keyboard Interface Lines	
13	OUT4	Output 4 for Keyboard Interface Lines	
14	ОИТЗ	Output 3 for Keyboard Interface Lines	
15	OUT2	Output 2 for Keyboard Interface Lines	
16	OUT1	Output 1 for Keyboard Interface Lines	

MAXIMUM RATINGS

Symbol	Rating		Value	Unit
V _{CC}	DC Supply Voltage		-0.5 to +7.0	V
VI	DC Input Voltage		$0 \le V_{CC} \le V_{CC} + 0.5$	V
Vo	DC Output Voltage		- 0.5 to + 7.0	V
I _{IK}	DC Input Diode Current	V _I < GND	±50	mA
lok	DC Output Diode Current	V _O = GND	- 50	mA
Ιο	DC Output Sink Current		± 50	mA
I _{CC}	DC Supply Current per Supply Pin		± 100	mA
I _{GND}	DC Ground Current per Ground Pin		± 100	mA
T _{STG}	Storage Temperature Range		- 65 to + 150	°C
T _L	Lead Temperature, 1 mm from Case for 10 S	Seconds	260	°C
T_J	Junction Temperature under bias		+ 150	°C
$\theta_{\sf JA}$	Thermal Resistance		80	°C/W
P_{D}	Power Dissipation in Still Air at 85°C		800	mW
MSL	Moisture Sensitivity		Level 1	
F _R	Flammability Rating	Oxygen Index: 28 to 34	UL 94 V-0 @ 0125 in	
V _{ESD}	ESD Test Voltage	ıman Body Model (Note 1) Machine Model (Note 2)	> 3000 > 300	V

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

- 1. Tested to EIA/JESD22-A114-A.
- 2. Tested to EIA/JESD22-A115-A.

RECOMMENDED OPERATING CONDITIONS

Symbol	Characteristics	Min	Max	Unit
V _{CC}	Positive DC Supply Voltage	1.5	5.5	V
V _{IN}	DC Input Voltage	GND	V _{CC} + 0.5	V
V _{OUT}	DC Output Voltage	GND	5.5	V
T _A	Operating Temperature Range	40	85	°C

DC CHARACTERISTICS

Symbol	Parameter	Condition	V _{CC}	Min	Max	Unit
V _{IL}	Input Logic Low Voltage		1.65 – 3.6	0.3 * V _{CC}		٧
V _{IH}	Input Logic High Voltage		1.65 – 3.6		0.7 * V _{CC}	٧
R _{OUT}	OUT _x Pullup Resistance		1.65 – 3.6	50	150	kΩ
R _{IN} 2.7	INx Pullup Resistance	PIN = GND	2.7	50	150	kΩ
R _{IN} 1.8	INx Pullup Resistance	PIN = GND	1.8	100	360	kΩ
V _D	Voltage Drop	INx = GND, I_{OUT} = 100 μ A			100	mV
Icc	Quiescent Current	All I/O Floating	1.65 – 3.6		10	μΑ
lμ	Output Leakage Current	INx = Floating			1.0	μΑ
C _P	I/O Pin Capacitance	1.0 MHz	2.5		15	pF

ORDERING INFORMATION

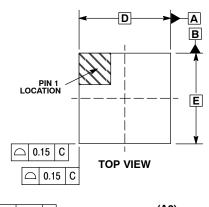
	Device Nomenclature						
Device Order Number	Circuit Indicator	Technology	Device Function	Package Suffix	Tape and Reel Suffix	Package Type	Tape & Reel Size [†]
NLSF2500MN1R2G	NL	SF	2500	MN1	R2	QFN-16 (Pb-Free)	3000

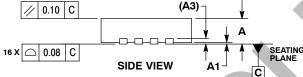
[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

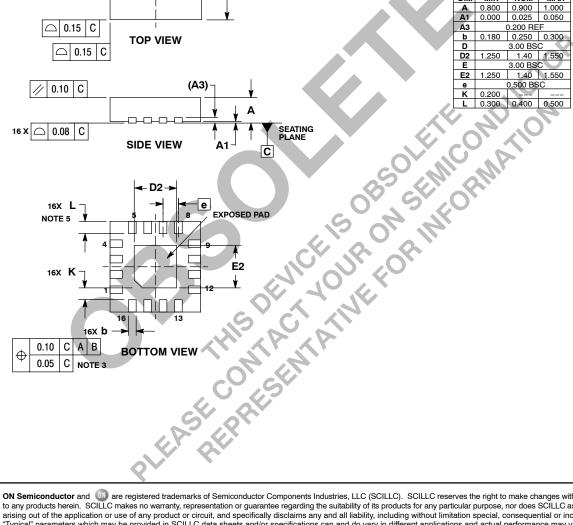


PACKAGE DIMENSIONS

QFN-16 3*3*0.85 MM, 0.5 P CASE 485AE-01 **ISSUE O**







NOTES:

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994. CONTROLLING DIMENSION: MILLIMETERS.
- 25 OMMINION BAPPLIES TO PLATED
 TERMINAL AND IS MEASURED BETWEEN
 0.25 AND 0.30 MM FROM TERMINAL
 4. COPLANARITY APPLIES TO THE EXPOSED
- PAD AS WELL AS THE TERMINALS.
 5. OUTLINE MEETS JEDEC DIMENSIONS PER
- MO-220, VARIATION VEED-6.

	MILLIMETERS					
DIM	MIN	NOM	MAX			
A	0.800	0.900	1.000			
A1	0.000	0.025	0.050			
АЗ		0.200 RE	F			
q	0.180	0.300				
D		3.00 BS	0			
D2	1.250	1.40	1.550			
Ε	3.00 BSC					
E2	1.250	1.40	1.550			
е	0.500 BSC					
Κ	0.200	1.2				
L	0.300	0.400	0.500			

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