Small Signal MOSFET

30 V, 250 mA, Dual N-Channel, SC-88

Features

- Low Gate Charge for Fast Switching
- Small Footprint 30% Smaller than TSOP-6
- ESD Protected Gate
- Pb-Free Package for Green Manufacturing (G Suffix)

Applications

- Low Side Load Switch
- Li-Ion Battery Supplied Devices Cell Phones, PDAs, DSC
- Buck Converters
- Level Shifts

MAXIMUM RATINGS (T_J = 25°C unless otherwise stated)

Parameter			Symbol	Value	Units
Drain-to-Source Voltage			V _{DSS}	30	V
Gate-to-Source Voltage			V_{GS}	±20	V
Continuous Drain	Steady State	T _A = 25 °C	I _D	250	mA
Current (Note 1)		T _A = 85 °C		180	
Power Dissipation (Note 1)	Steady State	T _A = 25 °C	P _D	272	mW
Pulsed Drain Current t =10 μs			I _{DM}	600	mA
Operating Junction and Storage Temperature			T _J , T _{STG}	–55 to 150	°C
Source Current (Body Diode)			I _S	250	mA
Lead Temperature for Soldering Purposes (1/8" from case for 10 s)			T _L	260	°C

1. Surface mounted on FR4 board using 1 in sq. pad size (Cu area = 1.127 in sq. [1 oz] including traces).

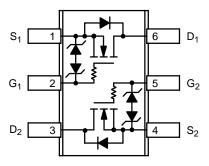


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V _{(BR)DSS}	R _{DS(on)} TYP	I _D Max
30 V	1.0 Ω @ 4.0 V	250 mA
	1.5 Ω @ 2.5 V	250 IIIA

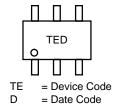
SOT-363 SC-88 (6 LEADS)



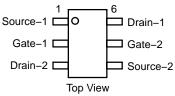
Top View



MARKING DIAGRAM



PIN ASSIGNMENT



ORDERING INFORMATION

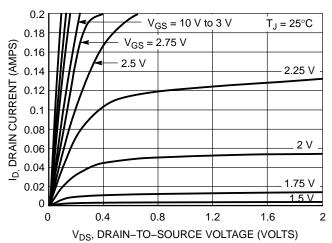
Device	Package	Shipping
NTJD4001NT1	SC-88	3000 Units/Reel
NTJD4001NT1G	SC-88 (Pb-Free)	3000 Units/Reel

ELECTRICAL CHARACTERISTICS (T₁ = 25°C unless otherwise stated)

Parameter	Symbol	Test Con	dition	Min	Тур	Max	Unit	
OFF CHARACTERISTICS								
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V}, I_D = 100 \mu\text{A}$		30			V	
Drain-to-Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} /T _J				56		mV/°C	
Zero Gate Voltage Drain Current	I _{DSS}	V _{GS} = 0 V, V	_{DS} = 30 V			1.0	μА	
Gate-to-Source Leakage Current	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 10 \text{ V}$				±1.0	μА	
ON CHARACTERISTICS (Note 2)								
Gate Threshold Voltage	V _{GS(TH)}	$V_{GS} = V_{DS}, I_{E}$	ο = 100 μΑ	0.8	1.2	1.5	V	
Gate Threshold Temperature Coefficient	V _{GS(TH)} /T _J				-3.2		mV/°C	
Drain-to-Source On Resistance	R _{DS(on)}	$R_{DS(on)}$ $V_{GS} = 4.0 \text{ V}, I_D = 10 \text{ mA}$			1.0	1.5	Ω	
		V _{GS} = 2.5 V,	_D = 10 mA		1.5	2.5	7	
Forward Transconductance	9 _{FS}	$V_{DS} = 3.0 \text{ V}, I_{D} = 10 \text{ mA}$			80		mS	
CHARGES AND CAPACITANCES								
Input Capacitance	C _{ISS}	$V_{GS} = 0 \text{ V, f} = 1.0 \text{ MHz,}$ $V_{DS} = 5.0 \text{ V}$			20	33	pF	
Output Capacitance	C _{OSS}				19	32		
Reverse Transfer Capacitance	C _{RSS}				7.25	12		
Total Gate Charge	Q _{G(TOT)}	$V_{GS} = 5.0 \text{ V}, V_{DS} = 24 \text{ V},$ $I_{D} = 0.1 \text{ A}$			0.9	1.3	nC	
Threshold Gate Charge	Q _{G(TH)}				0.2			
Gate-to-Source Charge	Q _{GS}				0.3			
Gate-to-Drain Charge	Q_{GD}				0.2			
SWITCHING CHARACTERISTICS (No	ote 3)							
Turn-On Delay Time	td _(ON)	V_{GS} = 4.5 V, V_{DD} = 5.0 V, I_{D} = 10 mA, R_{G} = 50 Ω			17		ns	
Rise Time	tr				23			
Turn-Off Delay Time	td _(OFF)				94			
Fall Time	tf				82			
DRAIN-SOURCE DIODE CHARACTE	RISTICS							
Forward Diode Voltage	V_{SD}	V _{GS} = 0 V,	T _J = 25°C		0.65	0.7	V	
		$I_S = 10 \text{ mA}$	T _J = 125°C		0.45			
Reverse Recovery Time	t _{RR}	$V_{GS} = 0 \text{ V, } dI_{S}/dt = 8.0 \text{ A/}\mu\text{s,}$ $I_{S} = 10 \text{ mA}$			12.4		ns	

Pulse Test: pulse width ≤ 300 μs, duty cycle ≤ 2%.
 Switching characteristics are independent of operating junction temperatures.

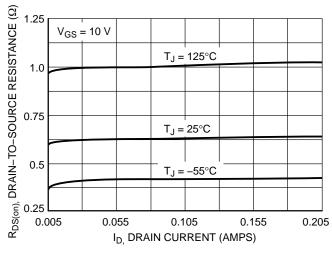
TYPICAL PERFORMANCE CURVES (T_J = 25°C unless otherwise noted)



0.1 V_{DS} = 5 V 0.08 V_{DS} = 5 V T_J = 125°C T_J = -55°C T_J = -55°C T_J = -55°C

Figure 1. On-Region Characteristics

Figure 2. Transfer Characteristics



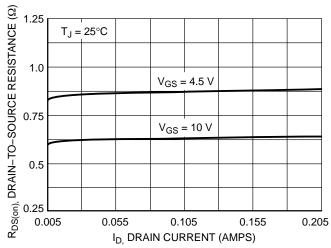
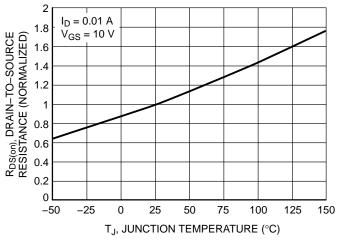


Figure 3. On–Resistance vs. Drain Current and Temperature

Figure 4. On–Resistance vs. Drain Current and Gate Voltage



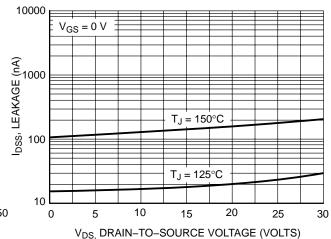
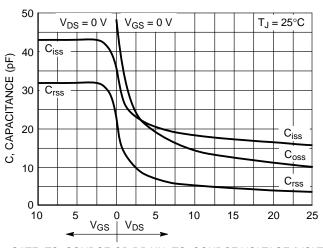
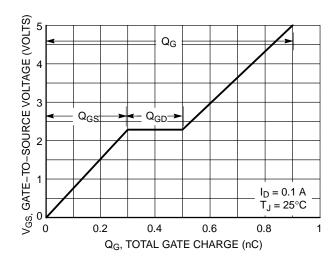


Figure 5. On–Resistance Variation with Temperature

Figure 6. Drain-to-Source Leakage Current vs. Voltage

TYPICAL PERFORMANCE CURVES (T_J = 25°C unless otherwise noted)





GATE-TO-SOURCE OR DRAIN-TO-SOURCE VOLTAGE (VOLTS)

Figure 7. Capacitance Variation

Figure 8. Gate-to-Source Voltage vs. Total Gate Charge

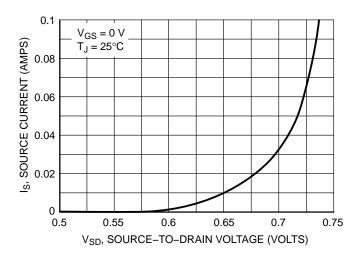
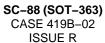
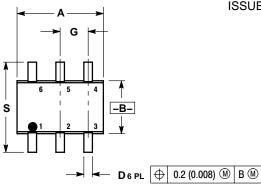
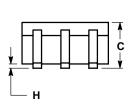


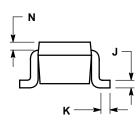
Figure 9. Diode Forward Voltage vs. Current

PACKAGE DIMENSIONS









- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. 419B-01 OBSOLETE, NEW STANDARD 419B-02.

	INC	HES	MILLIMETERS		
DIM	MIN	IIN MAX MIN		MAX	
Α	0.071	0.087	1.80	2.20	
В	0.045	0.053	1.15	1.35	
С	0.031	0.043	0.80	1.10	
D	0.004	0.012	0.10	0.30	
G	0.026	BSC	0.65 BSC		
Н		0.004		0.10	
J	0.004	0.010	0.10	0.25	
K	0.004	0.012	0.10	0.30	
N	0.008 REF		0.20 REF		
S	0.079	0.087	2.00	2.20	

- STYLE 26: PIN 1. SOURCE 1 2. GATE 1 3. DRAIN 2 4. SOURCE 2 5. GATE 2 6. DRAIN 1

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