

# BC487, BC487B

## High Current Transistors

### NPN Silicon

#### Features

- Pb-Free Packages are Available\*

#### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector – Emitter Voltage	$V_{CEO}$	60	Vdc
Collector – Base Voltage	$V_{CBO}$	60	Vdc
Emitter – Base Voltage	$V_{EBO}$	5.0	Vdc
Collector Current – Continuous	$I_C$	0.5	Adc
Total Device Dissipation @ $T_A = 25^\circ\text{C}$ Derate above $25^\circ\text{C}$	$P_D$	625 5.0	mW mW/ $^\circ\text{C}$
Total Device Dissipation @ $T_C = 25^\circ\text{C}$ Derate above $25^\circ\text{C}$	$P_D$	1.5 12	W mW/ $^\circ\text{C}$
Operating and Storage Junction Temperature Range	$T_J, T_{stg}$	-55 to +150	$^\circ\text{C}$

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

#### THERMAL CHARACTERISTICS

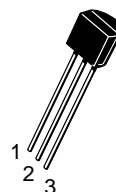
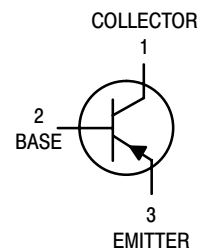
Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	200	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	83.3	$^\circ\text{C}/\text{W}$

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.



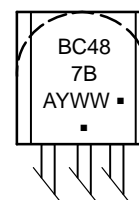
ON Semiconductor®

<http://onsemi.com>



TO-92  
CASE 29  
STYLE 17

#### MARKING DIAGRAM



BC487B = Device Code  
A = Assembly Location  
Y = Year  
WW = Work Week  
■ = Pb-Free Package

(Note: Microdot may be in either location)

#### ORDERING INFORMATION

Device	Package	Shipping†
BC487	TO-92	5000 Units / Box
BC487G	TO-92 (Pb-Free)	5000 Units / Box
BC487B	TO-92	5000 Units / Box
BC487BG	TO-92 (Pb-Free)	5000 Units / Box
BC487BRL1	TO-92	2000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

# BC487, BC487B

## ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
<b>OFF CHARACTERISTICS</b>					
Collector–Emitter Breakdown Voltage (Note 1) (I <sub>C</sub> = 10 mA, I <sub>B</sub> = 0)	V <sub>(BR)CEO</sub>	60	–	–	Vdc
Collector–Base Breakdown Voltage (I <sub>C</sub> = 100 µA, I <sub>E</sub> = 0)	V <sub>(BR)CBO</sub>	60	–	–	Vdc
Emitter–Base Breakdown Voltage (I <sub>E</sub> = 10 µA, I <sub>C</sub> = 0)	V <sub>(BR)EBO</sub>	5.0	–	–	Vdc
Collector Cutoff Current (V <sub>CB</sub> = 40 Vdc, I <sub>E</sub> = 0)	I <sub>CBO</sub>	–	–	100	nA

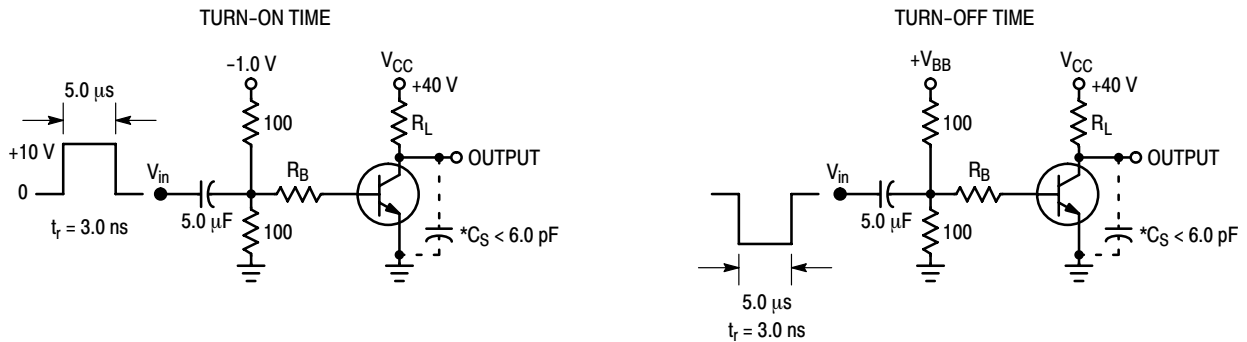
## ON CHARACTERISTICS\*

DC Current Gain (I <sub>C</sub> = 10 mA, V <sub>CE</sub> = 2.0 Vdc) (I <sub>C</sub> = 100 mA, V <sub>CE</sub> = 2.0 Vdc)  (I <sub>C</sub> = 1.0 A, V <sub>CE</sub> = 5.0 Vdc)*	h <sub>FE</sub>	40 60 160 15	– – 260 –	– 400 400 –	–
Collector–Emitter Saturation Voltage (I <sub>C</sub> = 500 mA, I <sub>B</sub> = 50 mA) (I <sub>C</sub> = 1.0 A, I <sub>B</sub> = 100 mA)	V <sub>CE(sat)</sub>	– –	0.2 0.3	0.5 –	Vdc
Base–Emitter Saturation Voltage (I <sub>C</sub> = 500 mA, I <sub>B</sub> = 50 mA) (I <sub>C</sub> = 1.0 A, I <sub>B</sub> = 100 mA) <sup>(1)</sup>	V <sub>BE(sat)</sub>	– –	0.85 0.9	1.2 –	Vdc

## DYNAMIC CHARACTERISTICS

Current–Gain – Bandwidth Product (I <sub>C</sub> = 50 mA, V <sub>CE</sub> = 2.0 Vdc, f = 100 MHz)	f <sub>T</sub>	–	200	–	MHz
Output Capacitance (V <sub>CB</sub> = 10 Vdc, I <sub>E</sub> = 0, f = 1.0 MHz)	C <sub>ob</sub>	–	7.0	–	pF
Input Capacitance (V <sub>EB</sub> = 0.5 Vdc, I <sub>C</sub> = 0, f = 1.0 MHz)	C <sub>ib</sub>	–	50	–	pF

1. Pulse Test: Pulse Width = 300 µs, Duty Cycle 2.0%.



\*Total Shunt Capacitance of Test Jig and Connectors  
For PNP Test Circuits, Reverse All Voltage Polarities

**Figure 1. Switching Time Test Circuits**

# BC487, BC487B

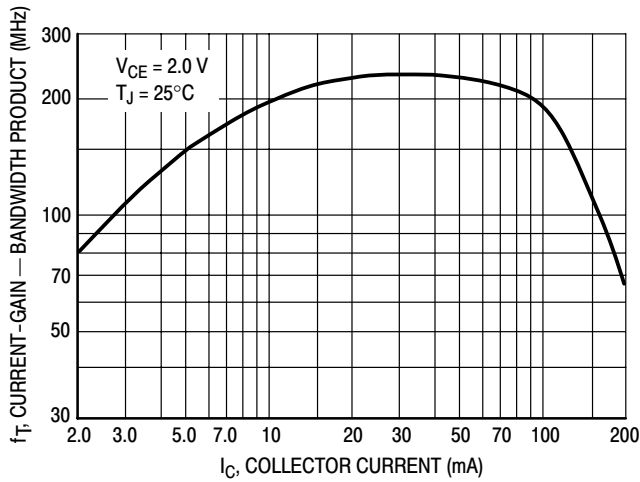


Figure 2. Current-Gain – Bandwidth Product

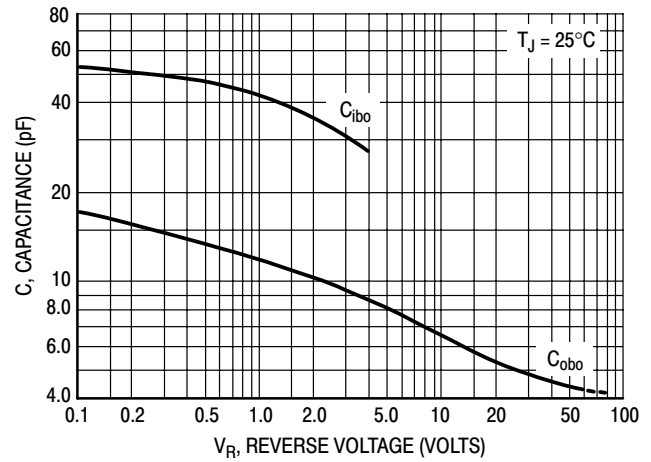


Figure 3. Capacitance

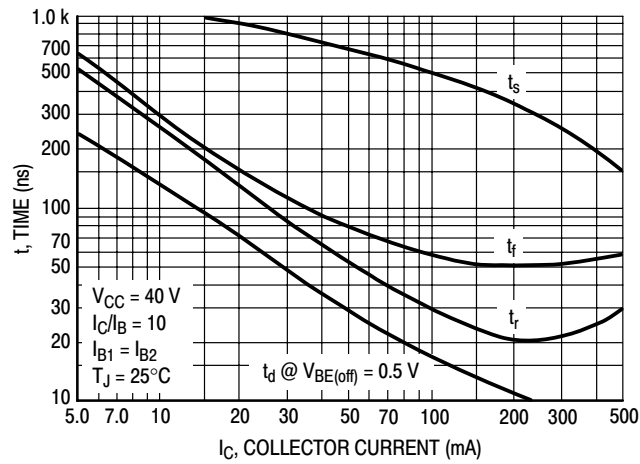


Figure 4. Switching Time

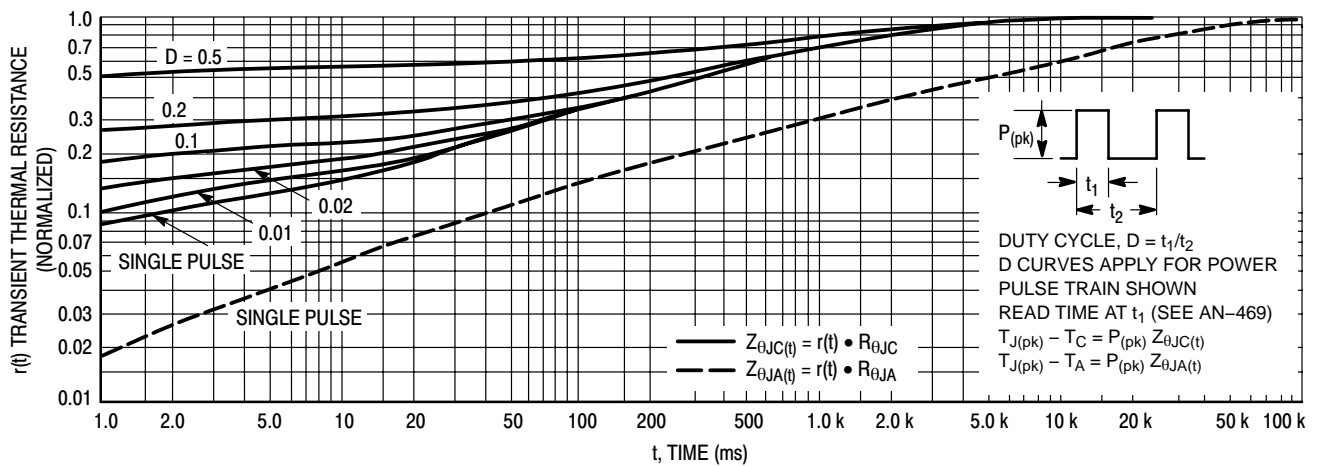


Figure 5. Thermal Response

# BC487, BC487B

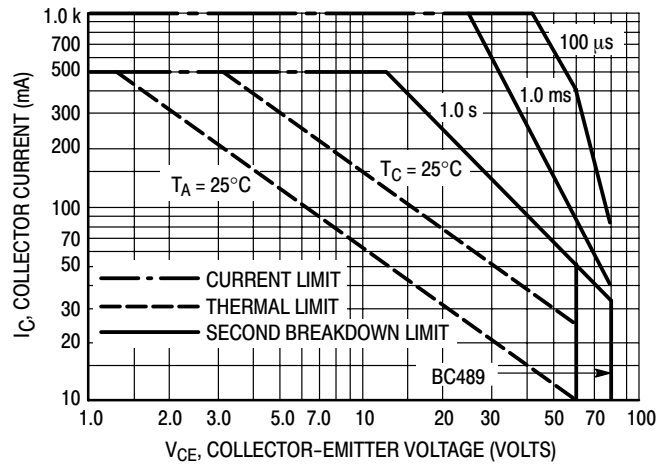


Figure 6. Active Region – Safe Operating Area

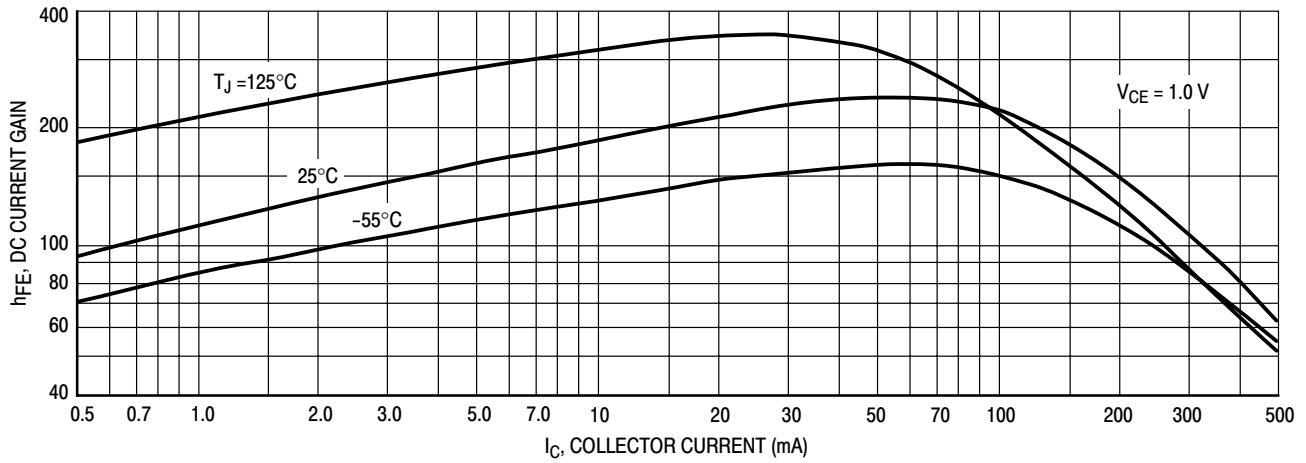


Figure 7. DC Current Gain

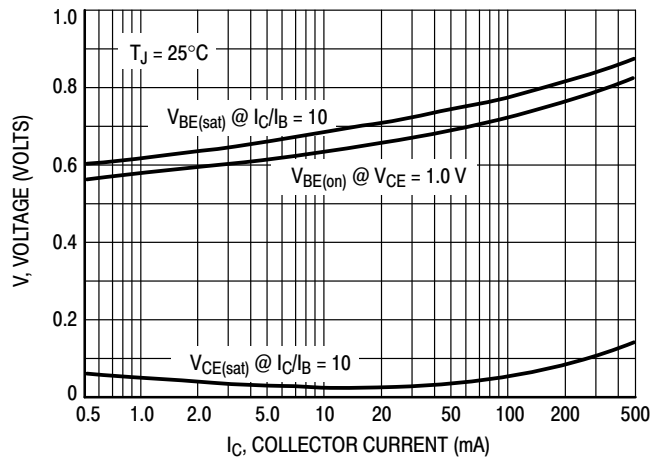


Figure 8. "On" Voltages

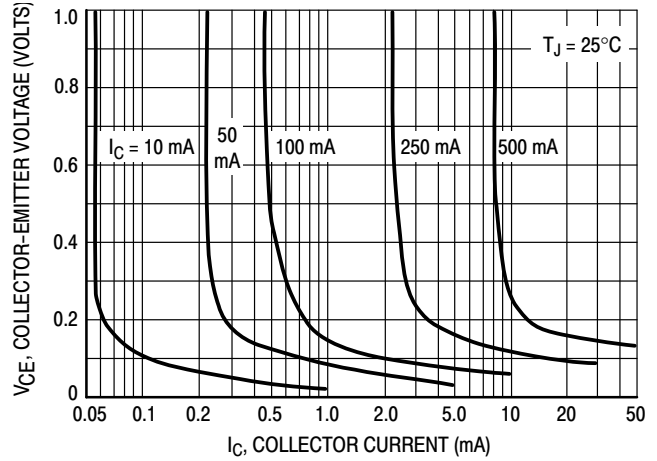


Figure 9. Collector Saturation Region

# BC487, BC487B

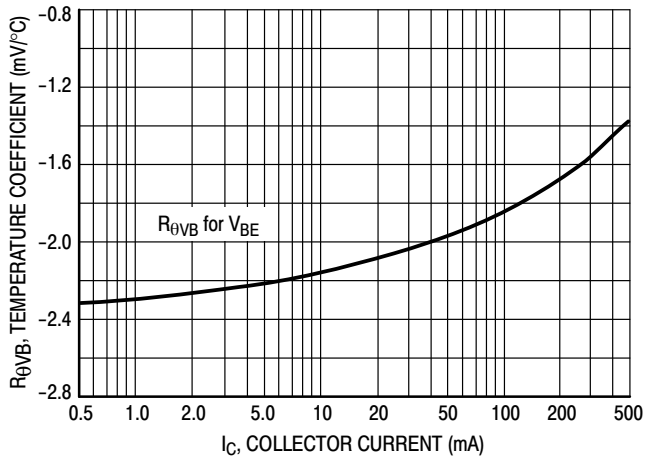


Figure 10. Base-Emitter Temperature Coefficient

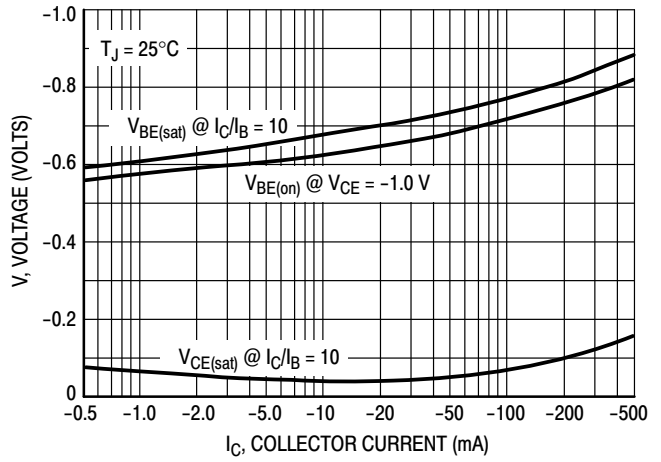


Figure 11. "On" Voltages

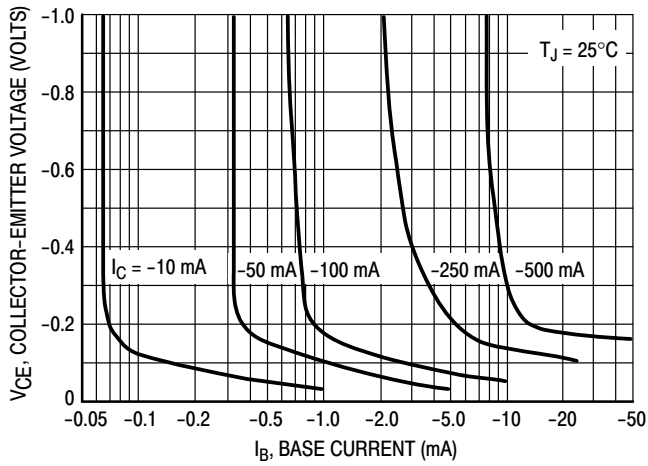


Figure 12. Collector Saturation Region

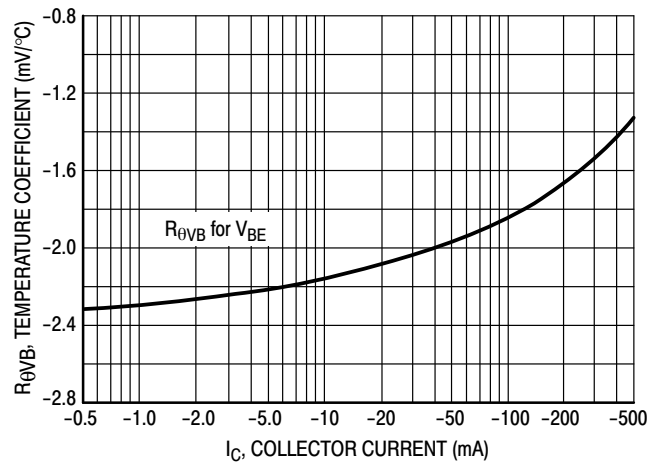
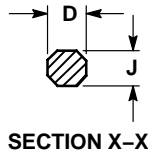
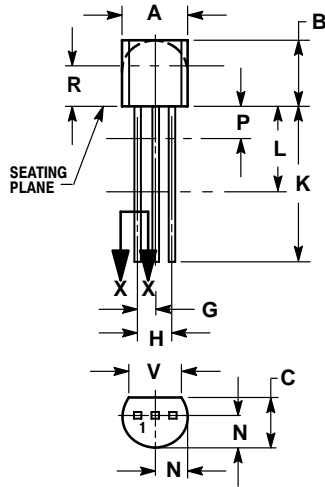


Figure 13. Base-Emitter Temperature Coefficient

# BC487, BC487B

## PACKAGE DIMENSIONS

TO-92  
(TO-226)  
CASE 29-11  
ISSUE AL




### NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
4. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.175	0.205	4.45	5.20
B	0.170	0.210	4.32	5.33
C	0.125	0.165	3.18	4.19
D	0.016	0.021	0.407	0.533
E	0.045	0.055	1.15	1.39
F	0.095	0.105	2.42	2.66
G	0.015	0.020	0.39	0.50
H	0.500	---	12.70	---
I	0.250	---	6.35	---
J	0.080	0.105	2.04	2.66
K	---	0.100	---	2.54
L	0.115	---	2.93	---
M	0.135	---	3.43	---

### STYLE 17:

1. COLLECTOR
2. BASE
3. EMITTER

ON Semiconductor and  are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

## PUBLICATION ORDERING INFORMATION

**LITERATURE FULFILLMENT:**  
Literature Distribution Center for ON Semiconductor  
P.O. Box 61312, Phoenix, Arizona 85082-1312 USA  
**Phone:** 480-829-7710 or 800-344-3860 Toll Free USA/Canada  
**Fax:** 480-829-7709 or 800-344-3867 Toll Free USA/Canada  
**Email:** orderlit@onsemi.com

**N. American Technical Support:** 800-282-9855 Toll Free  
USA/Canada

**Japan:** ON Semiconductor, Japan Customer Focus Center  
2-9-1 Kamimeguro, Meguro-ku, Tokyo, Japan 153-0051  
**Phone:** 81-3-5773-3850

**ON Semiconductor Website:** <http://onsemi.com>

**Order Literature:** <http://www.onsemi.com/litorder>

For additional information, please contact your local Sales Representative.