5-Bit Magnitude Comparator

Description

The MC10H166 is a 5-Bit Magnitude Comparator and is a functional/pinout duplication of the standard MECL $10K^{TM}$ part with 100% improvement in propagation delay and no increase in power-supply current.

The MC10H166 is a high-speed expandable 5-bit comparator for comparing the magnitude of two binary words. Two outputs are provided: A < B and A > B. The A = B function can be obtained by wire-ORing these outputs (a low level indicates A = B) or by wire-NORing the outputs (a high level indicates A = B). A high level on the enable function forces both outputs low.

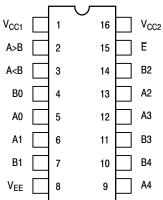
Features

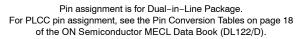
- Propagation Delay, Data-to-Output, 2.0 ns Typical
- Power Dissipation 440 mW Typical
- Improved Noise Margin 150 mV (Over Operating Voltage and Temperature Range)
- Voltage Compensated
- MECL 10K Compatible
- Pb-Free Packages are Available*

TRUTH TABLE

	Inputs	Outputs		
μ	А	В	A < B	A > B
Н	Х	Х	L	L
L	WORD A	= WORD B	L	L
L	WORD A	L	Н	
L	WORD A	< WORD B	Н	L





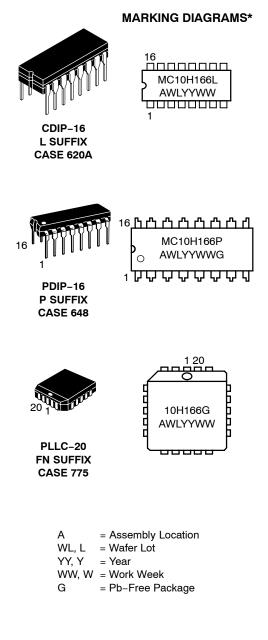


*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.



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*For additional marking information, refer to Application Note AND8002/D.

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 5 of this data sheet.

Table 1. MAXIMUM RATINGS

Symbol	Characteristic	Rating	Unit
V_{EE}	Power Supply (V _{CC} = 0)	-8.0 to 0	Vdc
VI	Input Voltage (V _{CC} = 0)	0 to V _{EE}	Vdc
l _{out}	Output Current – Continuous – Surge	50 100	mA
T _A	Operating Temperature Range	0 to +75	°C
T _{stg}	Storage Temperature Range – Plastic – Ceramic	−55 to +150 −55 to +165	°C ℃

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

Table 2. ELECTRICAL CHARACTERISTICS ($V_{EE} = -5.2 \text{ V} \pm 5\%$) (Note 1)

		0 °		25 °		75 °		
Symbol	Characteristic	Min	Max	Min	Max	Min	Max	Unit
١ _E	Power Supply Current	-	117	-	106	-	117	mA
I _{inH}	Input Current High	-	350	-	220	-	220	μΑ
I _{inL}	Input Current Low	0.5	-	0.5	-	0.3	-	μΑ
V _{OH}	High Output Voltage	-1.02	-0.84	-0.98	-0.81	-0.92	-0.735	Vdc
V _{OL}	Low Output Voltage	-1.95	-1.63	-1.95	-1.63	-1.95	-1.60	Vdc
V _{IH}	High Input Voltage	-1.17	-0.84	-1.13	-0.81	-1.07	-0.735	Vdc
VIL	Low Input Voltage	-1.95	-1.48	-1.95	-1.48	-1.95	-1.45	Vdc

 Each MECL 10H[™] series circuit has been designed to meet the dc specifications shown in the test table, after thermal equilibrium has been established. The circuit is in a test socket or mounted on a printed circuit board and transverse air flow greater than 500 lfpm is maintained. Outputs are terminated through a 50 Ω resistor to −2.0 V.

Table 3. AC PARAMETERS

		0° 25°		75 °				
Symbol	Characteristic	Min	Max	Min	Max	Min	Max	Unit
t _{pd}	Propagation Delay Data-to-Output Enable-to-Output	1.1 0.6	3.5 1.7	1.1 0.7	3.7 1.7	1.2 0.7	4.1 1.8	ns
t _r	Rise Time	0.6	1.5	0.6	1.6	0.6	1.7	ns
t _f	Fall Time	0.6	1.5	0.6	1.6	0.6	1.7	ns

NOTE: Device will meet the specifications after thermal equilibrium has been established when mounted in a test socket or printed circuit board with maintained transverse airflow greater than 500 lfpm. Electrical parameters are guaranteed only over the declared operating temperature range. Functional operation of the device exceeding these conditions is not implied. Device specification limit values are applied individually under normal operating conditions and not valid simultaneously.



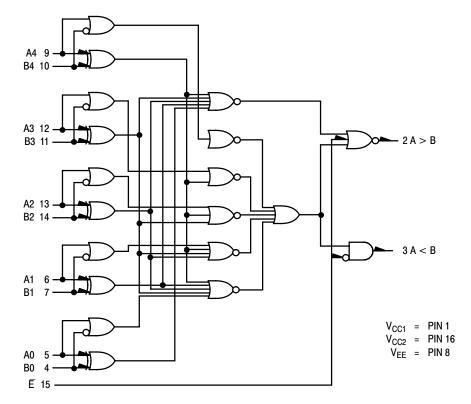
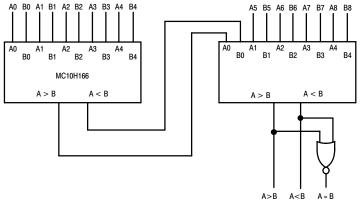
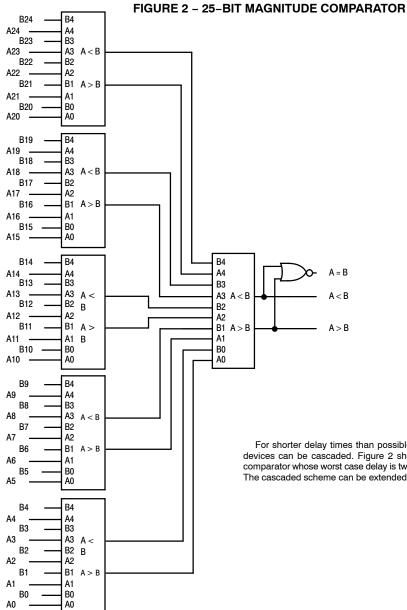


FIGURE 1 – 9-BIT MAGNITUDE COMPARATOR



For 9-Bit Word

For longer word lengths, the MC10H166 can be serially expanded or cascaded. Figure 1 shows two devices in a serial expansion for a 9-bit word length. The A > B and A < B outputs are fed to the A0 and B0 inputs respectively of the next device. The connection for an A = B output is also shown. The worst case delay time of serial expansion is equal to the number of comparators times the data-to-output delay.



For shorter delay times than possible with serial expansion, devices can be cascaded. Figure 2 shows a 25-bit cascaded comparator whose worst case delay is two data-to-output delays. The cascaded scheme can be extended to longer word lengths.

ORDERING INFORMATION

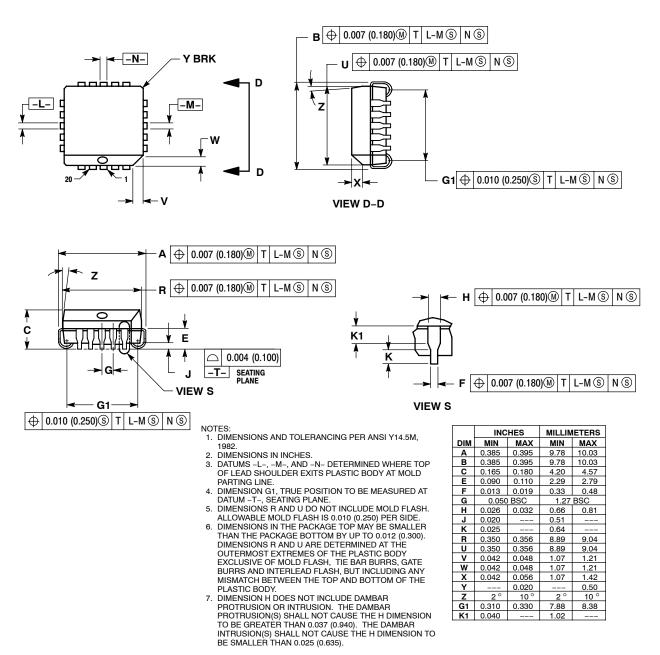
Device	Package	Shipping [†]
MC10H166FN	PLLC-20	46 Units / Rail
MC10H166FNG	PLLC-20 (Pb-Free)	46 Units / Rail
MC10H166FNR2	PLLC-20	500 / Tape & Reel
MC10H166FNR2G	PLLC-20 (Pb-Free)	500 / Tape & Reel
MC10H166L	CDIP-16	25 Unit / Rail
MC10H166P	PDIP-16	25 Unit / Rail
MC10H166PG	PDIP-16 (Pb-Free)	25 Unit / Rail

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

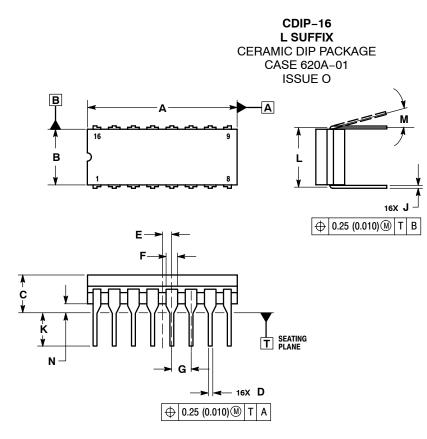
PACKAGE DIMENSIONS



CASE 775-02 ISSUE E



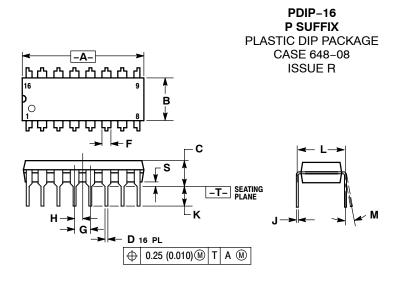
PACKAGE DIMENSIONS



NOTES: 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994. 2. CONTROLLING DIMENSION: INCH. 3. DIMENSION L TO CENTER OF LEAD WHEN FORMED PARALLEL. 4. DIMENSION F MAY NARROW TO 0.76 (0.030) WHERE THE LEAD ENTERS THE CERAMIC BODY. 5. THIS DRAWING REPLACES OBSOLETE CASE OUTLINE 620-10.

	INC	HES	MILLIMETERS		
DIM	MIN	MIN MAX		MAX	
Α	0.750	0.785	19.05	19.93	
В	0.240 0.295		6.10	7.49	
С	0.200			5.08	
D	0.015 0.020		0.39	0.50	
Е	0.050 BSC		1.27 BSC		
F	0.055	0.065	1.40	1.65	
G	0.100	BSC	2.54 BSC		
Η	0.008	0.015	0.21	0.38	
K	0.125 0.170		3.18	4.31	
L	0.300 BSC		7.62	BSC	
Μ	0 °	15 °	0 °	15°	
N	0.020	0.040	0.51	1.01	

PACKAGE DIMENSIONS



NOTES:

DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

CONTROLLING DIMENSION: INCH.

DIMENSION L TO CENTER OF LEADS WHEN 3

FORMED PARALLEL DIMENSION B DOES NOT INCLUDE MOLD FLASH. ROUNDED CORNERS OPTIONAL. 5.

	INC	HES	MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.740	0.770	18.80	19.55	
В	0.250	0.270	6.35	6.85	
С	0.145	0.175	3.69	4.44	
D	0.015	0.021	0.39	0.53	
F	0.040	0.70	1.02	1.77	
G	0.100 BSC		2.54 BSC		
Н	0.050	BSC	1.27 BSC		
J	0.008	0.015	0.21	0.38	
K	0.110	0.130	2.80	3.30	
L	0.295	0.305	7.50	7.74	
Μ	0 °	10 °	0 °	10 °	
S	0.020	0.040	0.51	1.01	

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