# **Differential Receiver**

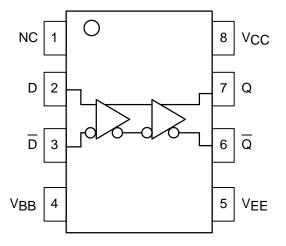
The MC100LVEL16 is a differential receiver. The device is functionally equivalent to the EL16 device, operating from a low voltage supply. The LVEL16 exhibits a wider CMR range than its EL16 counterpart. With output transition times and propagation delays comparable to the EL16 the LVEL16 is ideally suited for interfacing with high frequency sources at 3.3V supplies.

The LVEL16 provides a VBB output for either single-ended use or as a DC bias for AC coupling to the device. The VBB pin should be used only as a bias for the LVEL16 as its current sink/source capability is limited. Whenever used, the VBB pin should be bypassed to ground via a  $0.01\mu f$  capacitor.

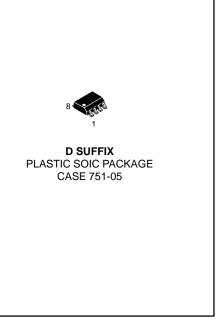
 $\underline{\text{Un}}\text{der}$  open input conditions, the Q input will be pulled down to  $V_{\mbox{\footnotesize{ee}}}$  and the Q input will be biased to  $V_{\mbox{\footnotesize{CC}}}/2.$  This condition will force the Q output low.

- 300ps Propagation Delay
- High Bandwidth Output Transitions
- 75kΩ Internal Input Pulldown Resistors
- >2000V ESD Protection

### LOGIC DIAGRAM AND PINOUT ASSIGNMENT



# MC100LVEL16



#### **PIN DESCRIPTION**

PIN	FUNCTION
D	Data Inputs
Q	Data Outputs
V <sub>BB</sub>	Ref. Voltage Output

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## **DC CHARACTERISTICS** (VEE = VEE(min) to VEE(max); VCC = GND)

		-40°C		0°C			25°C			85°C				
Symbol	Characteristic	Min	Тур	Max	Unit									
IEE	Power Supply Current		17	23		17	23		17	23		18	24	mA
V <sub>BB</sub>	Output Reference Voltage	-1.38		-1.26	-1.38		-1.26	-1.38		-1.26	-1.38		-1.26	٧
VEE	Power Supply Voltage	-3.0	-3.3	-3.8	-3.0	-3.3	-3.8	-3.0	-3.3	-3.8	-3.0	-3.3	-3.8	٧
ΙΗ	Input HIGH Current			150			150			150			150	μΑ
IIL	Input LOW Current DDD	0.5 -600			0.5 -600			0.5 -600			0.5 -600			μА

# **AC CHARACTERISTICS** ( $V_{EE} = V_{EE}(min)$ to $V_{EE}(max)$ ; $V_{CC} = GND$ )

		-40°C		0°C			25°C			85°C				
Symbol	Characteristic	Min	Тур	Max	Unit									
<sup>t</sup> PLH <sup>t</sup> PHL	Propagation Delay to Output (Diff) (SE)	150 100	275 275	400 450	215 165	290 290	365 415	225 175	300 300	375 425	240 190	315 315	390 440	ps
tSKEW	Duty Cycle Skew <sup>1</sup> (Diff)		5	30		5	20		5	20		5	20	ps
VPP	Minimum Input Swing2	150			150			150			150			mV
VCMR	Common Mode Range <sup>3</sup> Vpp < 500mV Vpp ≥ 500mV	-2.0 -1.8		-0.4 -0.4	-2.1 -1.9		-0.4 -0.4	-2.1 -1.9		-0.4 -0.4	-2.1 -1.9		-0.4 -0.4	V
t <sub>r</sub> t <sub>f</sub>	Output Rise/Fall Times Q (20% – 80%)	120	220	320	120	220	320	120	220	320	120	220	320	ps

Duty cycle skew is the difference between a TPLH and TPHL propagation delay through a device.
 Minimum input swing for which AC parameters guaranteed. The device has a DC gain of ≈40.

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<sup>3.</sup> The CMR range is referenced to the most positive side of the differential input signal. Normal operation is obtained if the HIGH level falls within the specified range and the peak-to-peak voltage lies between Vppmin and 1V. The lower end of the CMR range varies 1:1 with VEE. The numbers in the spec table assume a nominal VEE = -3.3V. Note for PECL operation, the VCMR(min) will be fixed at 3.3V – |VCMR(min)|.

#### **OUTLINE DIMENSIONS**

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#### NOTES

- DIMENSIONING AND TOLERANCING PER

  ANSI V14 FM 1082
- ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: MILLIMETER.
- DIMENSIONS A AND B DO NOT INCLUDE MOLD PROTRUSION.
- MAXIMUM MOLD PROTRUSION 0.15 (0.006)
   PER SIDE.
- PER SIDE.

  5. DIMENSION D DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.127 (0.005) TOTAL IN EXCESS OF THE D DIMENSION AT MAXIMUM MATERIAL CONDITION.

	MILLIN	IETERS	INCHES				
DIM	MIN	MIN MAX		MAX			
Α	4.80	4.80   5.00		0.196			
В	3.80	4.00	0.150	0.157			
С	1.35	1.75	0.054	0.068			
D	0.35	0.49	0.014	0.019			
F	0.40	1.25	0.016	0.049			
G	1.27	BSC	0.050	BSC			
J	0.18	0.25	0.007	0.009			
K	0.10	0.25	0.004	0.009			
M	0°	7°	0 °	7 °			
Р	5.80	6.20	0.229	0.244			
R	0.25	0.50	0.010	0.019			

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#### How to reach us:

**USA/EUROPE/Locations Not Listed**: Motorola Literature Distribution; P.O. Box 20912; Phoenix, Arizona 85036. 1–800–441–2447 or 602–303–5454

MFAX: RMFAX0@email.sps.mot.com - TOUCHTONE 602-244-6609 INTERNET: http://Design-NET.com

JAPAN: Nippon Motorola Ltd.; Tatsumi–SPD–JLDC, 6F Seibu–Butsuryu–Center, 3–14–2 Tatsumi Koto–Ku, Tokyo 135, Japan. 03–81–3521–8315

ASIA/PACIFIC: Motorola Semiconductors H.K. Ltd.; 8B Tai Ping Industrial Park, 51 Ting Kok Road, Tai Po, N.T., Hong Kong. 852–26629298



