# M1MA141KT1, M1MA142KT1

Preferred Device

# Single Silicon Switching Diode

This Silicon Epitaxial Planar Diode is designed for use in ultra high speed switching applications. This device is housed in the SC-70 package which is designed for low power surface mount applications.

#### **Features**

- Fast  $t_{rr}$ , < 3.0 ns
- Low  $C_D$ , < 2.0 pF
- Pb-Free Packages are Available

# **MAXIMUM RATINGS** $(T_A = 25^{\circ}C)$

Rating		Symbol	Value	Unit
	M1MA141KT1 M1MA142KT1	$V_R$	40 80	Vdc
	M1MA141KT1 M1MA142KT1	$V_{RM}$	40 80	Vdc
Forward Current		IF	100	mAdc
Peak Forward Current		I <sub>FM</sub>	225	mAdc
Peak Forward Surge Curre	ent	I <sub>FSM</sub> (Note 1)	500	mAdc

#### THERMAL CHARACTERISTICS

Rating	Symbol	Max	Unit
Power Dissipation	P <sub>D</sub>	150	mW
Junction Temperature	T <sub>J</sub>	150	°C
Storage Temperature	T <sub>stg</sub>	-55 ~ <b>+</b> 150	°C

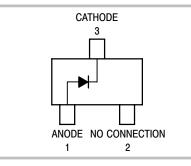
Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

1. t = 1 sec



# ON Semiconductor®

#### http://onsemi.com





SC-70 (SOT-323) CASE 419 STYLE 2

#### **MARKING DIAGRAM**



 $\begin{array}{ll} \text{Mx} &= \text{Device Code} \\ & \text{x} = \text{H for 141} \\ & \text{I for 142} \\ \text{M} &= \text{Date Code}^* \\ \end{array}$ 

= Date Code= Pb-Free Package

(Note: Microdot may be in either location)
\*Date Code orientation may vary depending upon manufacturing location.

#### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
M1MA141KT1	SC-70	3000/Tape & Reel
M1MA141KT1G	SC-70 (Pb-Free)	3000/Tape & Reel
M1MA142KT1	SC-70	3000/Tape & Reel
M1MA142KT1G	SC-70 (Pb-Free)	3000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

**Preferred** devices are recommended choices for future use and best overall value.

# M1MA141KT1, M1MA142KT1

# **ELECTRICAL CHARACTERISTICS** $(T_A = 25^{\circ}C)$

Characteristic		Condition	Symbol	Min	Max	Unit
Reverse Voltage Leakage Current	M1MA141KT1 M1MA142KT1	V <sub>R</sub> = 35 V V <sub>R</sub> = 75 V	I <sub>R</sub>	-	0.1	μAdc
Forward Voltage		I <sub>F</sub> = 100 mA	V <sub>F</sub>	-	1.2	Vdc
Reverse Breakdown Voltage	M1MA141KT1 M1MA142KT1	I <sub>R</sub> = 100 μA	V <sub>R</sub>	40 80	-	Vdc
Diode Capacitance		V <sub>R</sub> = 0, f = 1.0 MHz	C <sub>D</sub>	-	2.0	pF
Reverse Recovery Time (Figure 1)		$I_F = 10 \text{ mA}, V_R = 6.0 \text{ V},$ $R_L = 100 \Omega, I_{rr} = 0.1 I_R$	t <sub>rr</sub> (Note 2)	-	3.0	ns

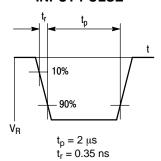
<sup>2.</sup> t<sub>rr</sub> Test Circuit

# M1MA141KT1, M1MA142KT1

# RECOVERY TIME EQUIVALENT TEST CIRCUIT

# A R<sub>L</sub>

# **INPUT PULSE**



#### **OUTPUT PULSE**

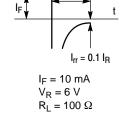
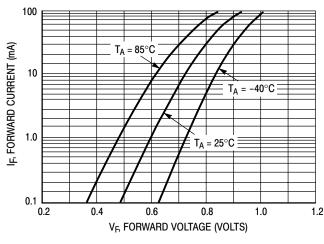


Figure 1. Recovery Time Equivalent Test Circuit



T<sub>A</sub> = 150°C

T<sub>A</sub> = 125°C

T<sub>A</sub> = 85°C

T<sub>A</sub> = 85°C

T<sub>A</sub> = 55°C

T<sub>A</sub> = 25°C

V<sub>B</sub>, REVERSE VOLTAGE (VOLTS)

Figure 2. Forward Voltage

Figure 3. Reverse Current

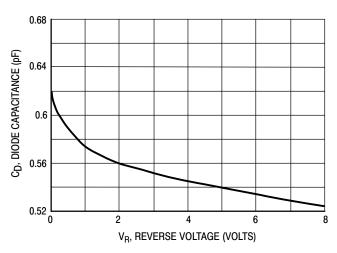
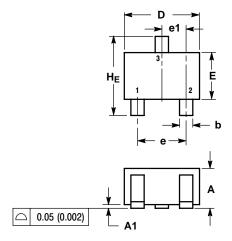


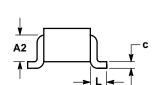
Figure 4. Diode Capacitance

#### M1MA141KT1, M1MA142KT1

#### PACKAGE DIMENSIONS

SC-70 (SOT-323) CASE 419-04 ISSUE M





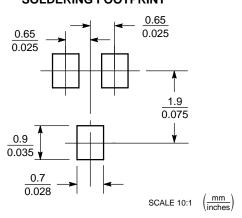
#### NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: INCH.

	MILLIMETERS			INCHES			
DIM	MIN	NOM	MAX	MIN	NOM	MAX	
Α	0.80	0.90	1.00	0.032	0.035	0.040	
A1	0.00	0.05	0.10	0.000	0.002	0.004	
A2		0.7 REF			0.028 REI	-	
b	0.30	0.35	0.40	0.012	0.014	0.016	
С	0.10	0.18	0.25	0.004	0.007	0.010	
D	1.80	2.10	2.20	0.071	0.083	0.087	
E	1.15	1.24	1.35	0.045	0.049	0.053	
е	1.20	1.30	1.40	0.047	0.051	0.055	
e1		0.65 BSC			0.026 BSC	;	
L	0.425 REF			0.017 REF			
HE	2.00	2.10	2.40	0.079 0.083 0.095			

STYLE 2: PIN 1. ANODE 2. N.C. 3. CATHODE

#### **SOLDERING FOOTPRINT\***



\*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ON Semiconductor and the registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

#### **PUBLICATION ORDERING INFORMATION**

#### LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor
P.O. Box 61312, Phoenix, Arizona 85082–1312 USA
Phone: 480–829–7710 or 800–344–3860 Toll Free USA/Canada
Fax: 480–829–7709 or 800–344–3867 Toll Free USA/Canada
Email: orderlit@onsemi.com

N. American Technical Support: 800–282–9855 Toll Free USA/Canada

Japan: ON Semiconductor, Japan Customer Focus Center 2–9–1 Kamimeguro, Meguro–ku, Tokyo, Japan 153–0051 Phone: 81–3–5773–3850

ON Semiconductor Website: http://onsemi.com

Order Literature: http://www.onsemi.com/litorder

For additional information, please contact your local Sales Representative.