

# MKP9V160

Preferred Device

## Sidac High Voltage

### Bidirectional Triggers

Bidirectional devices designed for direct interface with the ac power line. Upon reaching the breakover voltage in each direction, the device switches from a blocking state to a low voltage on-state. Conduction will continue like a Triac until the main terminal current drops below the holding current. The plastic axial lead package provides high pulse current capability at low cost. Glass passivation insures reliable operation.

#### Features

- High Pressure Sodium Vapor Lighting
- Strobes and Flashers
- Igniters
- High Voltage Regulators
- Pulse Generators
- Used to Trigger Gates of SCR's and Triacs
- $\mathcal{N}$  Indicates UL Registered – File #E116110
- Pb-Free Package is Available

#### MAXIMUM RATINGS ( $T_J = 25^\circ\text{C}$ unless otherwise noted)

Rating	Symbol	Value	Unit
Peak Repetitive Off-State Voltage (Sine Wave, 50 to 60 Hz, $T_J = -40$ to $125^\circ\text{C}$ )	$V_{\text{DRM}}$ , $V_{\text{RRM}}$	$\pm 90$	V
On-State Current RMS ( $T_L = 80^\circ\text{C}$ , Lead Length = $3/8''$ All Conduction Angles)	$I_{\text{T(RMS)}}$	$\pm 0.9$	A
Peak Non-repetitive Surge Current (60 Hz One Cycle Sine Wave, $T_J = 125^\circ\text{C}$ )	$I_{\text{TSM}}$	$\pm 4.0$	A
Operating Junction Temperature Range	$T_J$	$-40$ to $+125$	$^\circ\text{C}$
Storage Temperature Range	$T_{\text{stg}}$	$-40$ to $+150$	$^\circ\text{C}$

#### THERMAL CHARACTERISTICS

Rating	Symbol	Max	Unit
Thermal Resistance, Junction-to-Lead Lead Length = $3/8''$	$R_{\theta\text{JL}}$	40	$^\circ\text{C}/\text{W}$
Lead Solder Temperature (Lead Length $\geq 1/16''$ from Case, 10 s Max)	$T_L$	260	$^\circ\text{C}$

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

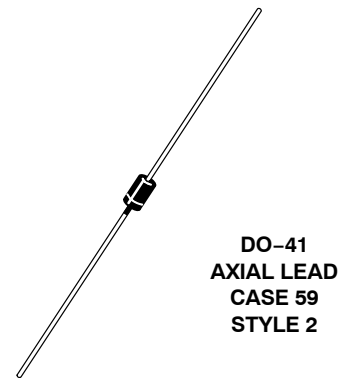
\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.



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**SIDACS ( $\mathcal{N}$ )**  
**0.9 AMPS RMS, 160 VOLTS**



DO-41  
AXIAL LEAD  
CASE 59  
STYLE 2

#### MARKING DIAGRAM



A = Assembly Location  
Y = Year  
WW = Work Week  
▪ = Pb-Free Package  
(Note: Microdot may be in either location)

#### ORDERING INFORMATION

Device	Package	Shipping†
MKP9V160RL	Axial Lead*	5000 Tape & Reel
MKP9V160RLG	Axial Lead*	5000 Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Preferred devices are recommended choices for future use and best overall value.

# MKP9V160

## ELECTRICAL CHARACTERISTICS ( $T_C = 25^\circ\text{C}$ unless otherwise noted; Electricals apply in both directions)

Characteristic	Symbol	Min	Typ	Max	Unit
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### OFF CHARACTERISTICS

Repetitive Peak Off-State Current (50 to 60 Hz Sine Wave)	$T_J = 25^\circ\text{C}$ $V_{\text{DRM}} = 90\text{ V}$	$I_{\text{DRM}}$	-	-	5.0	$\mu\text{A}$
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### ON CHARACTERISTICS

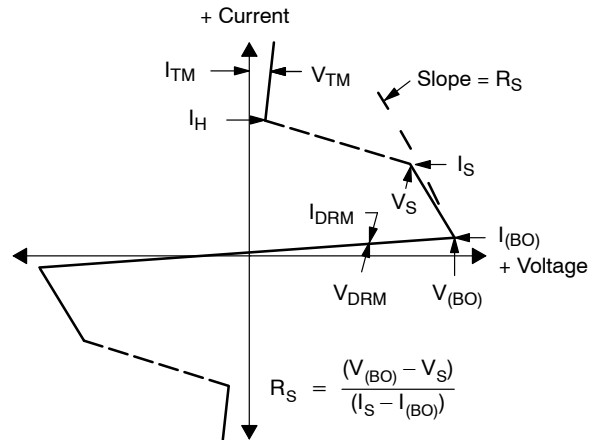
Breakover Voltage $I_{\text{BO}} = 200\ \mu\text{A}$	$V_{\text{BO}}$	150	-	170	V
Peak On-State Voltage ( $I_{\text{TM}} = 1\text{ A Peak}$ , Pulse Width $\leq 300\ \mu\text{s}$ , Duty Cycle $\leq 2\%$ )	$V_{\text{TM}}$	-	1.3	1.5	V
Dynamic Holding Current (Sine Wave, 50 to 60 Hz, $R_L = 100\ \Omega$ )	$I_{\text{H}}$	-	-	100	mA
Switching Resistance (Sine Wave, 50 to 60 Hz)	$R_S$	0.1	-	-	k $\Omega$

### DYNAMIC CHARACTERISTICS

Critical Rate-of-Rise of On-State Current, Critical Damped Waveform Circuit ( $I_{\text{PK}} = 130\text{ A}$ , Pulse Width = $10\ \mu\text{sec}$ )	$di/dt$	-	120	-	A/ $\mu\text{s}$
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## Voltage Current Characteristic of SIDAC (Bidirectional Device)

Symbol	Parameter
$I_{\text{DRM}}$	Off State Leakage Current
$V_{\text{DRM}}$	Off State Repetitive Blocking Voltage
$V_{\text{BO}}$	Breakover Voltage
$I_{\text{BO}}$	Breakover Current
$I_{\text{H}}$	Holding Current
$V_{\text{TM}}$	On State Voltage
$I_{\text{TM}}$	Peak on State Current



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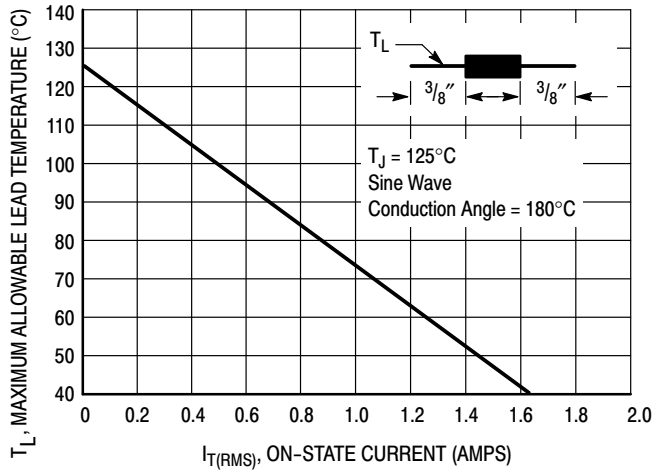


Figure 1. Maximum Lead Temperature

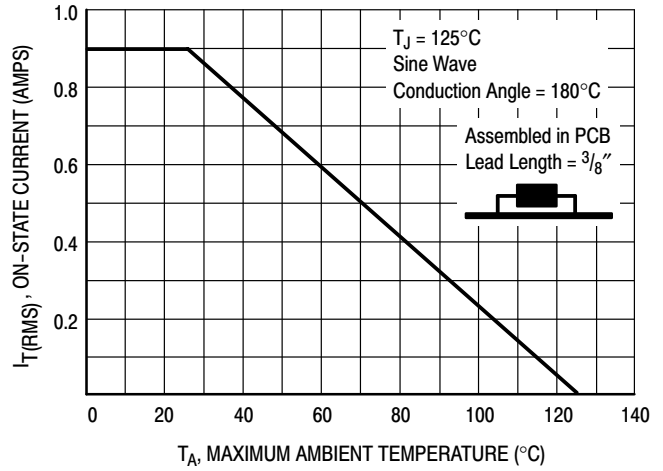


Figure 2. Maximum Ambient Temperature

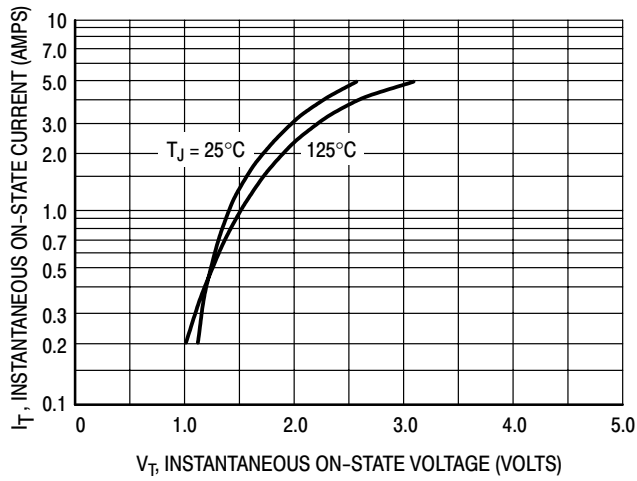


Figure 3. Typical On-State Voltage

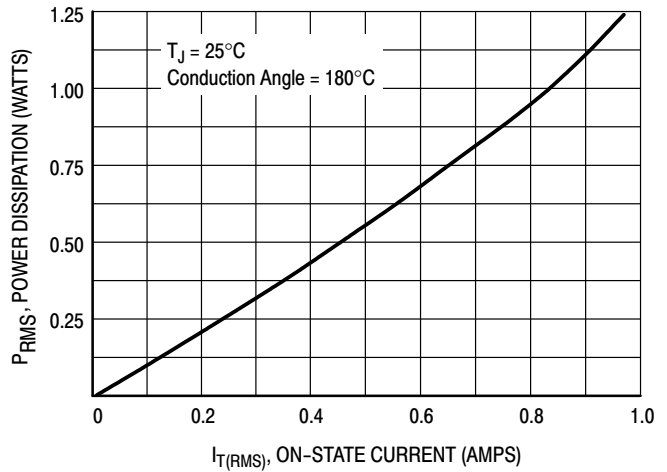


Figure 4. Typical Power Dissipation

## THERMAL CHARACTERISTICS

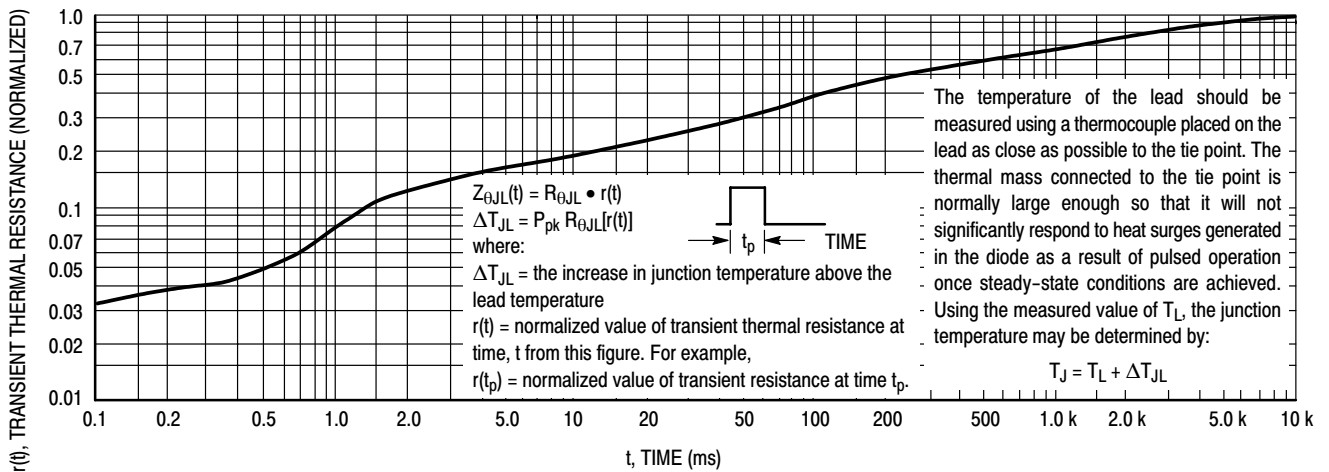


Figure 5. Thermal Response

# MKP9V160

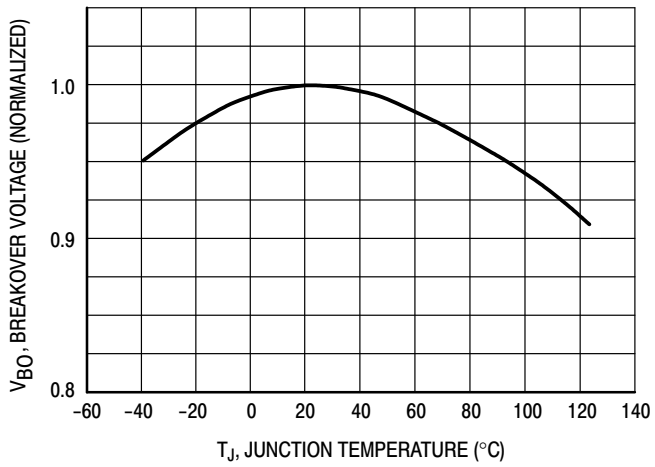


Figure 6. Typical Breakover Voltage

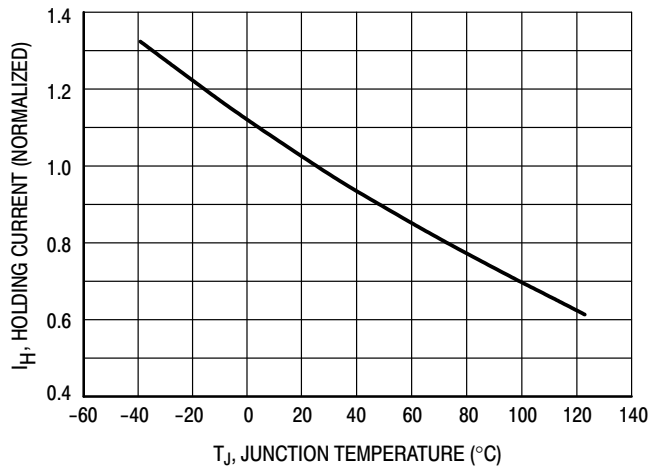


Figure 7. Typical Holding Current

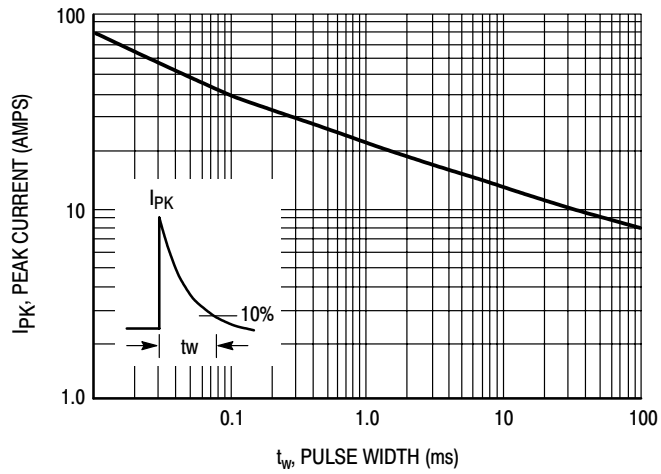
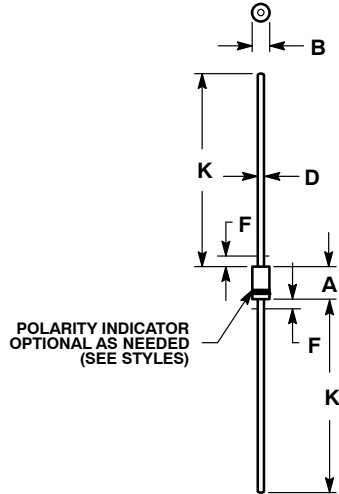


Figure 8. Pulse Rating Curve

# MKP9V160

## PACKAGE DIMENSIONS

### AXIAL LEAD CASE 59-10 ISSUE U




#### NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. ALL RULES AND NOTES ASSOCIATED WITH JEDEC DO-41 OUTLINE SHALL APPLY
4. POLARITY DENOTED BY CATHODE BAND.
5. LEAD DIAMETER NOT CONTROLLED WITHIN F DIMENSION.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.161	0.205	4.10	5.20
B	0.079	0.106	2.00	2.70
D	0.028	0.034	0.71	0.86
F	---	0.050	---	1.27
K	1.000	---	25.40	---

#### STYLE 2:

NO POLARITY

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