JFET Input Operational Amplifiers

These low cost JFET input operational amplifiers combine two state–of–the–art analog technologies on a single monolithic integrated circuit. Each internally compensated operational amplifier has well matched high voltage JFET input devices for low input offset voltage. The JFET technology provides wide bandwidths and fast slew rates with low input bias currents, input offset currents, and supply currents.

These devices are available in single, dual and quad operational amplifiers which are pin–compatible with the industry standard MC1741, MC1458, and the MC3403/LM324 bipolar devices.

- Input Offset Voltage of 5.0 mV Max (LF347B)
- Low Input Bias Current: 50 pA
- Low Input Noise Voltage: 16 nV/√Hz
- Wide Gain Bandwidth: 4.0 MHz
- High Slew Rate: 13V/μs
- Low Supply Current: 1.8 mA per Amplifier
- High Input Impedance: $10^{12} \Omega$
- High Common Mode and Supply Voltage Rejection Ratios: 100 dB

FAMILY OF JFET

LF347, B

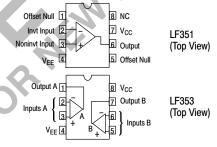
LF351

LF353

OPERATIONAL AMPLIFIERS



PIN CONNECTIONS



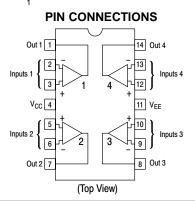
N SUFFIX PLASTIC PACKAGE CASE 646

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Supply Voltage	V _{CC} V _{EE}	+18 -18	V
Differential Input Voltage	V _{ID}	±30	V
Input Voltage Range (Note 1)	V _{IDR}	±15	V
Output Short Circuit Duration (Note 2)	tsc	Continuous	
Power Dissipation at $T_A = +25^{\circ}C$ Derate above $T_A = +25^{\circ}C$	P _D 1/ _{θJA}	900 10	mW mW/°C
Operating Ambient Temperature Range	T _A	0 to +70	°C
Operating Junction Temperature Range	Т _Ј	115	°C
Storage Temperature Range	T _{stg}	– 65 to +150	°C

NOTES: 1. Unless otherwise specified, the absolute maximum negative input voltage is limited to the negative power supply.

2. Any amplifier output can be shorted to ground indefinitely. However, if more than one amplifier output is shorted simultaneously, maximum junction temperature rating may be exceeded.



ORDERING INFORMATION

Device	Function	Operating Temperature Range	Package	
LF351D	Single		SO–8	
LF351N	Single		Plastic DIP	
LF353D	Dual	$T_A = 0^\circ$ to +70°C	SO–8	
LF353N	Dual		Plastic DIP	
LF347BN	Quad		Plastic DIP	
LF347N	Quad		Plastic DIP	

ON

LF347, B LF351 LF353

Characteristic		LF347B			LF347, LF351, LF353			
	Symbol	Min	Тур	Max	Min	Тур	Max	Unit
Input Offset Voltage ($R_S \le 10 \text{ k}, V_{CM} = 0$)	V _{IO}						4.0	mV
$\begin{array}{l} T_A = +25^{\circ}C\\ 0^{\circ}C \leq T_A \leq +70^{\circ}C \end{array}$		_	1.0 _	5.0 8.0	_	5.0 -	10 13	
Avg. Temperature Coefficient of Input Offset Voltage $R_S \leq 10~k,~0^\circ C \leq T_A \leq +70^\circ C$	$\Delta V_{IO} / \Delta T$	_	10	_	-	10	_	μV/°C
Input Offset Current (V _{CM} = 0, Note 3) T _A = +25°C	Ι _{ΙΟ}	_	25	100	-	25	100	pА
$0^{\circ}C \le T_A \le +70^{\circ}C$		-	-	4.0	-	-	4.0	nA
Input Bias Current ($V_{CM} = 0$, Note 3)	I _{IB}		50	200		50	200	
$ \begin{aligned} T_{A} &= +25^{\circ}C \\ 0^{\circ}C &\leq T_{A} \leq +70^{\circ}C \end{aligned} $		_	50 -	200 8.0	_	50 -	200 8.0	pA nA
Input Resistance	r _i	_	10 ¹²	-	_	10 ¹²		Ω
Common Mode Input Voltage Range	V _{ICR}	±11	+15 -12	-	±11	+15 -12	7	V
Large–Signal Voltage Gain (V _O = ± 10 V, R _L = 2.0 k) T _A = +25°C	A _{VOL}	50	100		25	100	_	V/mV
$0^{\circ}C \leq T_A \leq +70^{\circ}C$		25	-	-	15	-	-	
Output Voltage Swing ($R_L = 10 \text{ k}$)	Vo	±12	±14	-	±12	±14	-	V
Common Mode Rejection ($R_S \le 10 \text{ k}$)	CMR	80	100	-	70	100	-	dB
Supply Voltage Rejection ($R_S \le 10 \text{ k}$)	PSRR	80	100	e	70	100	-	dB
Supply Current	ID					7.0		mA
LF347 LF351			7.2	11	_	7.2 1.8	11 3.4	
LF353		-	\frown	-	-	3.6	6.5	
Short Circuit Current	I _{SC}		25	-	-	25	-	mA
Slew Rate (A _V = +1)	SR	\mathbf{G}	13	-	-	13	_	V/µs
Gain-Bandwidth Product	BWp	<u> </u>	4.0	-	-	4.0	-	MHz
Equivalent Input Noise Voltage $(R_S = 100 \Omega, f = 1000 Hz)$	en	-	24	-	-	24	-	nV/√H
Equivalent Input Noise Current (f = 1000 Hz)	in	-	0.01	-	-	0.01	-	pA/√H
Channel Separation (LF347, LF353) 1.0 Hz \leq f \leq 20 kHz (Input Referred)	-	-	-120	-	-	-120	-	dB

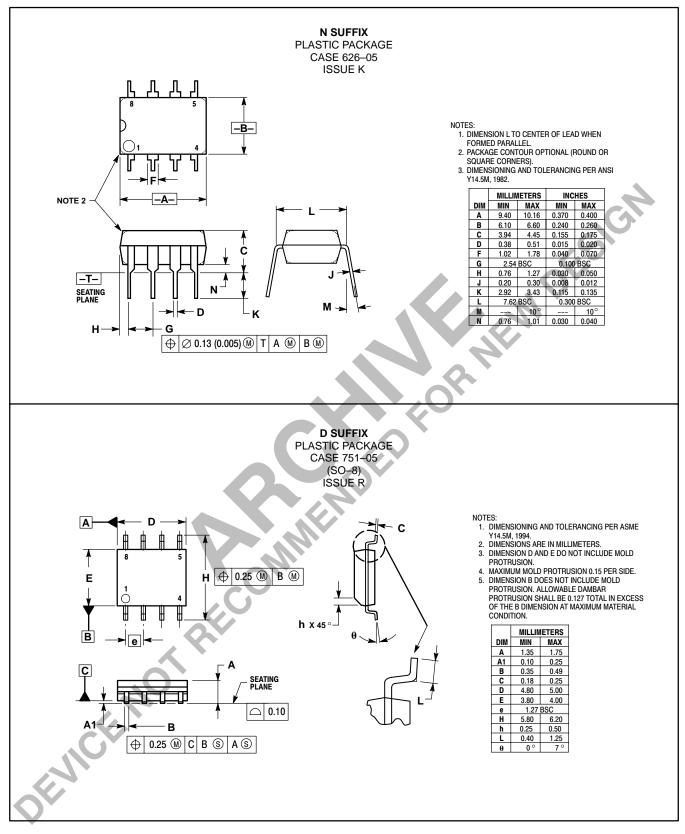
ELECTRICAL CHARACTERISTICS ($V_{CC} = +15 V_{EE} = -15 V$, $T_A = 25^{\circ}C$, unless otherwise noted.)

For Typical Characteristic Performance Curves, refer to MC34001, 34002, 34004 data sheet.

NOTE: 3. Input bias currents of JFET input op amps approximately double for every 10°C rise in junction temperature. To maintain junction temperatures as close to ambient as is possible, pulse techniques are utilized during test.

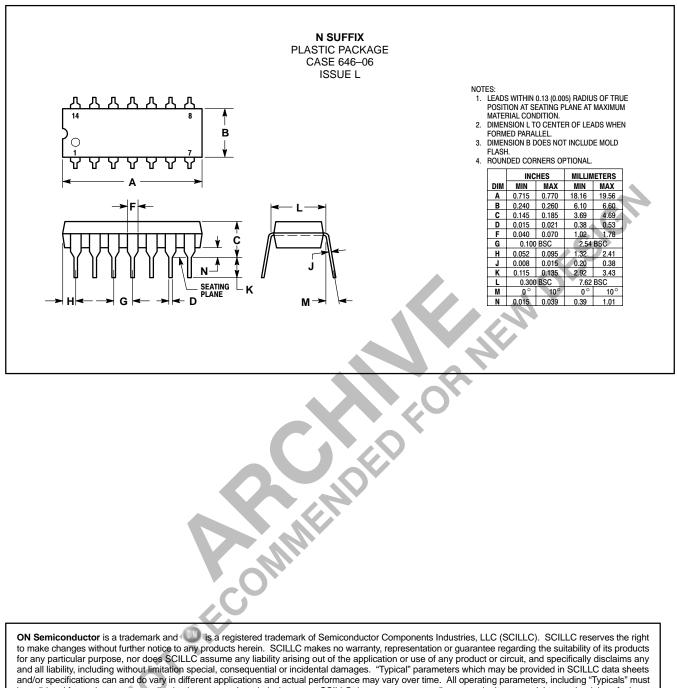
LF347, B LF351 LF353

OUTLINE DIMENSIONS



LF347, B LF351 LF353

OUTLINE DIMENSIONS



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