25 Ω Octal Bidirectional Transceiver With 3-State Inputs and Outputs

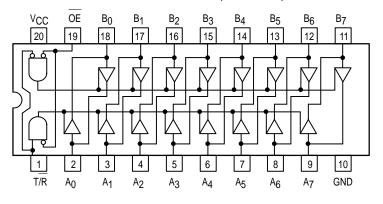
The MC74F2245 is designed for asynchronous communication between data buses. The device transmits data from the A bus to the B bus or from the B bus to the A bus depending upon the logic level at the direction control (DIR) input. The output enable (OE) input disables the device so the buses are effectively isolated.

Both A and B outputs can sink up to 12mA; 25Ω resistors are included in the lower output circuit to reduce overshoot and undershoot.

The MC74F2245 is characterized for operation from 0°C to 70°C.

- 3-State Outputs Drive Bus Lines or Buffer Memory Address Registers
- Package Options Include Plastic SOIC (DW–Suffix) and Plastic SSOP (SD–Suffix)

CONNECTION DIAGRAM (TOP VIEW)



MC74F2245

25 Ω OCTAL BIDIRECTIONAL TRANSCEIVER WITH 3-STATE INPUTS AND OUTPUTS

FAST™ SCHOTTKY TTL



DW SUFFIX PLASTIC SOIC CASE 751D-04



SD SUFFIX* PLASTIC SSOP CASE 940C-03

*Thermal Mounting Techniques are Recommended. Please refer to Motorola Application Note AN1567/D.

GUARANTEED OPERATING RANGES

Symbol	Parameter			Тур	Max	Unit
VCC	Supply Voltage			5.0	5.5	V
T _A	Operating Ambient Temperature Range		0	25	70	°C
loн	Output Current — High Outputs				-3.0	mA
loL	Output Current — Low Outputs				12	mA



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REV 0

DC CHARACTERISTICS OVER OPERATING TEMPERATURE RANGE (unless otherwise specified)

			Limits					
Symbol	Parame	ter	Min	Тур	Max	Unit	Test Conditions	
VIH	Input HIGH Voltage		2.0			V	Guaranteed Input HIGH Voltage	
V _{IL}	Input LOW Voltage				0.8	V	Guaranteed Input L	OW Voltage
VIK	Input Clamp Diode Volta	ge			-1.2	V	I _{IN} = -18 mA	V _{CC} = MIN
Vон	Output HIGH Voltage, Outputs		2.5			V	I _{OH} = -1.0 mA	V _{CC} = 4.50 V
			2.4	3.0		V	$I_{OH} = -3.0 \text{ mA}$	V _{CC} = 4.50 V
			2.7	3.2		V	$I_{OH} = -3.0 \text{ mA}$	V _{CC} = 4.75 V
VOL	Output LOW Voltage, Ou	itputs		0.2	0.5	V	I _{OL} = 1 mA	V _{CC} = MIN
VOL	Output LOW Voltage, Outputs			0.5	0.75	V	I _{OL} = 12 mA	V _{CC} = MIN
lozh + lih	Output Off Current HIGH				70	μΑ	V _{OUT} = 2.7 V	V _{CC} = MAX
lozL + lIL	Output Off Current LOW				-650	mA	V _{OUT} = 0.5 V	V _{CC} = MAX
	Input HIGH Current	OE, T/R Inputs			20	μΑ	V _{IN} = 2.7 V	
l _{IH}		OE, T/R Inputs			100	μΑ	V _{IN} = 7.0 V	V _{CC} = MAX
		A _n , B _n Inputs			1.0	mA	V _{IN} = 5.5 V	1
		T/R Input			-0.8	mA		
IIL	Input LOW Current	OE Input			-1.2	mA	V _{IN} = 0.5 V	V _{CC} = MAX
los	Output Short Circuit	A _n Outputs	-60		-150	mA	V _{OUT} = GND	V _{CC} = MAX
	Current (Note 2)	B _n Outputs	-100		-225	mA	V _{OUT} = GND	V _{CC} = MAX
ІССН	Power Supply Current HIGH				90	mA	V _{CC} = MAX, Outputs HIGH	
ICCL	Power Supply Current Lo	OW			120	mA	V _{CC} = MAX, Outputs LOW	
lccz	Power Supply Current O	FF			110	mA	V _{CC} = MAX, Outputs OFF	

NOTES:

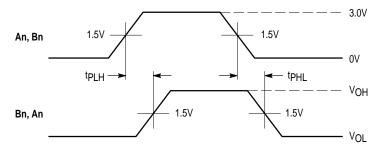
AC CHARACTERISTICS

		$T_A = +25^{\circ}C$ $V_{CC} = +5.0 \text{ V}$ $C_L = 50 \text{ pF}$		$T_A = 0^{\circ}\text{C to } +70^{\circ}\text{C}$ $V_{CC} = 5.0 \text{ V} \pm 10\%$ $C_L = 50 \text{ pF}$		
Symbol	Parameter	Min	Max	Min	Max	Unit
tPLH tPHL	Propagation Delay A _n to B _n or B _n to A _n	2.5 2.5	6.0 6.6	2.5 2.5	7.0 7.1	ns
^t PZH ^t PZL	Output Enable Time	3.0 3.5	7.3 10.6	3.0 3.5	8.5 12.0	ns
^t PHZ ^t PLZ	Output Disable Time	2.5 2.0	6.5 6.5	2.5 2.0	7.5 7.5	ns

MOTOROLA 2

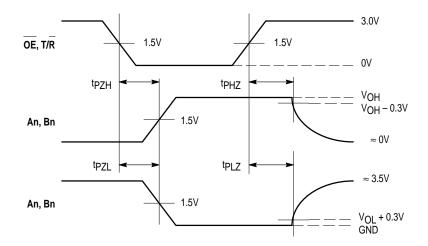
^{1.} For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions for the applicable device type.

^{2.} Not more than one output should be shorted at a time.



WAVEFORM 1 - PROPAGATION DELAYS

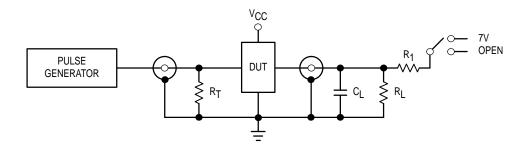
 t_R = t_F = 2.5ns, 10% to 90%; f = 1MHz; t_W = 500ns



WAVEFORM 2 - OUTPUT ENABLE AND DISABLE TIMES

 $t_R = t_F = 2.5$ ns, 10% to 90%; f = 1MHz; $t_W = 500$ ns

Figure 1. AC Waveforms



TEST	SWITCH
tPLH, tPHL	Open
tPZL, tPLZ	7V
tPZH, tPHZ	Open

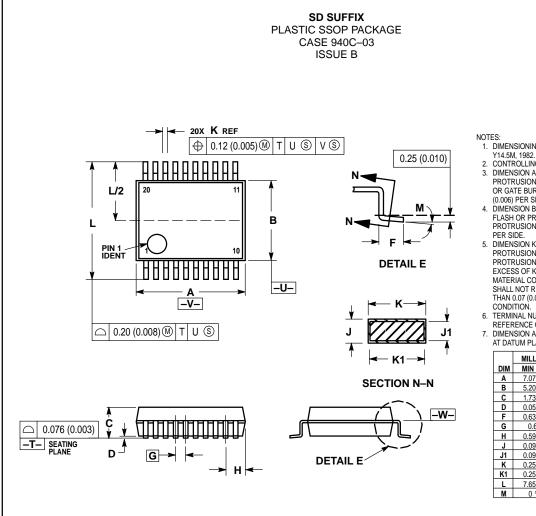
 C_L = 50pF or equivalent (Includes jig and probe capacitance) R_L = R_1 = 500 Ω or equivalent R_T = Z_{OUT} of pulse generator (typically 50 Ω)

Figure 2. Test Circuit

3

MOTOROLA

OUTLINE DIMENSIONS



- IOTES:
 1 DIMENSIONING AND TOLERANCING PER ANSI
 Y14.5M, 1982.
 2. CONTROLLING DIMENSION: MILLIMETER.
 3 DIMENSION A DOES NOT INCLUDE MOLD FLASH,
 PROTRUSIONS OR GATE BURRS. MOLD FLASH
 OR CATE BURDE GLAUL NOT EXCEED 4.6. OR GATE BURRS SHALL NOT EXCEED 0.15 (0.006) PER SIDE.
- 4. DIMENSION B DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSION. INTERLEAD FLASH OR PROTRUSION SHALL NOT EXCEED 0.15 (0.006) PER SIDE.
- 5. DIMENSION K DOES NOT INCLUDE DAMBAR PROTRUSION/INTRUSION. ALLOWABLE DAMBAR PROTRUSIONINI RUSION. ALLOWABLE DAMBA PROTRUSION SHALL BE 0.13 (0.005) TOTAL IN EXCESS OF K DIMENSION AT MAXIMUM MATERIAL CONDITION. DAMBAR INTRUSION SHALL NOT REDUCE DIMENSION K BY MORE THAN 0.07 (0.002) AT LEAST MATERIAL CONDITION.

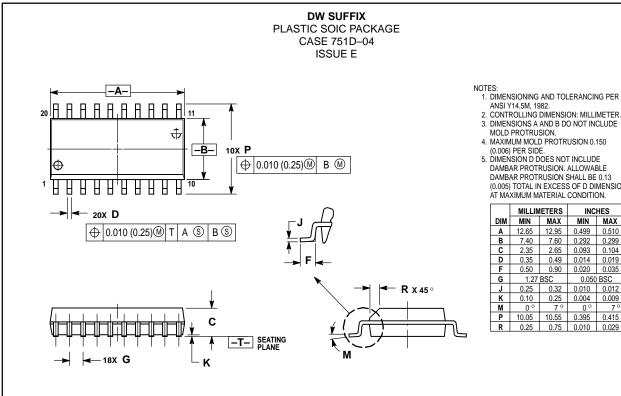
 6. TERMINAL NUMBERS ARE SHOWN FOR
- REFERENCE ONLY.

 7. DIMENSION A AND B ARE TO BE DETERMINED AT DATUM PLANE –W–.

	MILLIMETERS		INCHES		
DIM	MIN	MAX	MIN	MAX	
Α	7.07	7.33	0.278	0.288	
В	5.20	5.38	0.205	0.212	
С	1.73	1.99	0.068	0.078	
D	0.05	0.21	0.002	0.008	
F	0.63	0.95	0.024	0.037	
G	0.65 BSC		0.026 BSC		
Н	0.59	0.75	0.023	0.030	
J	0.09	0.20	0.003	0.008	
J1	0.09	0.16	0.003	0.006	
K	0.25	0.38	0.010	0.015	
K1	0.25	0.33	0.010	0.013	
L	7.65	7.90	0.301	0.311	
М	0 °	8 °	0 °	8 °	

MOTOROLA 4

OUTLINE DIMENSIONS



- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

- 4. MAXIMUM MOLD PROTRUSION 0.150
- DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.13 (0.005) TOTAL IN EXCESS OF D DIMENSION AT MAXIMUM MATERIAL CONDITION.

	MILLIN	METERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
Α	12.65	12.95	0.499	0.510	
В	7.40	7.60	0.292	0.299	
С	2.35	2.65	0.093	0.104	
D	0.35	0.49	0.014	0.019	
F	0.50	0.90	0.020	0.035	
G	1.27	BSC	0.050 BSC		
J	0.25	0.32	0.010	0.012	
K	0.10	0.25	0.004	0.009	
M	0 °	7°	0 °	7°	
Р	10.05	10.55	0.395	0.415	
P	0.25	0.75	0.010	0.020	

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