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TOSHIBA Field Effect Transistor Silicon N Channel MOS Type

2SK2823

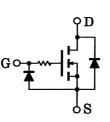
For Portable Equipment High Speed Switch Applications Analog Switch Applications

- High input impedance
- 1.5 V gate drive
- Low gate threshold voltage: V_{th} = 0.5~1.0 V
- Small package

Marking

Equivalent Circuit



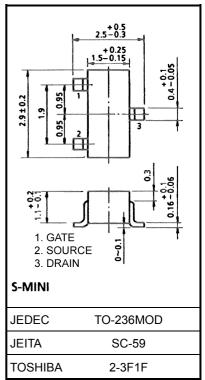


Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Drain-source voltage	V _{DS}	20	V
Gate-source voltage	V _{GSS}	10	V
DC drain current	I _D	100	mA
Drain power dissipation	PD	200	mW
Channel temperature	T _{ch}	150	°C
Storage temperature range	T _{stg}	-55~150	°C

Note: This transistor is electrostatic sensitive device.

Please handle with caution.



Weight: 0.012 g (typ.)

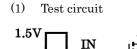
Unit: mm

Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	$V_{GS} = 10 \text{ V}, \text{ V}_{DS} = 0$			1	μA
Drain-source breakdown voltage		V (BR) DSS	$I_D = 100 \ \mu A, \ V_{GS} = 0$	20		_	V
Drain cut-off curre	ent	I _{DSS}	$V_{DS} = 20 V, V_{GS} = 0$	_		1	μA
Gate threshold vo	ltage	V _{th}	$V_{DS} = 1.5 \text{ V}, \text{ I}_{D} = 0.1 \text{ mA}$	0.5		1.0	V
Forward transfer	admittance	Y _{fs}	$V_{DS} = 1.5 \text{ V}, \text{ I}_{D} = 10 \text{ mA}$	35	70		mS
Drain-source ON resistance 1		R _{DS (ON) 1}	$I_D = 1 \text{ mA}, V_{GS} = 1.2 \text{ V}$	_	15	50	Ω
Drain-source ON resistance 2		R _{DS (ON) 2}	$I_D = 10 \text{ mA}, V_{GS} = 1.5 \text{ V}$	_	10	40	Ω
Drain-source ON resistance 3		R _{DS (ON) 3}	$I_D = 10 \text{ mA}, V_{GS} = 2.5 \text{ V}$	_	7	28	Ω
Input capacitance		C _{iss}	$V_{DS} = 1.5 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$	_	12	_	pF
Reverse transfer capacitance		C _{rss}	$V_{DS} = 1.5 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$	_	3.4	_	pF
Output capacitance		C _{oss}	$V_{DS} = 1.5 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$	_	12	_	pF
Switching time	Turn-on time	t _{on}	$V_{DD} = 1.5 \text{ V}, \text{ I}_{D} = 10 \text{ mA}, V_{GS} = 0 \text{~} 1.5 \text{ V}$	_	0.35		
	Turn-off time	t _{off}		_	0.2		μS

Switching Time Test Circuit

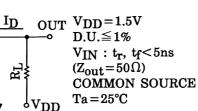
 50Ω



 $10 \mu s$

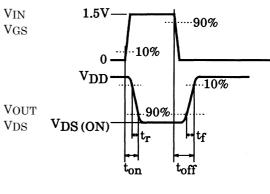
VIN

0



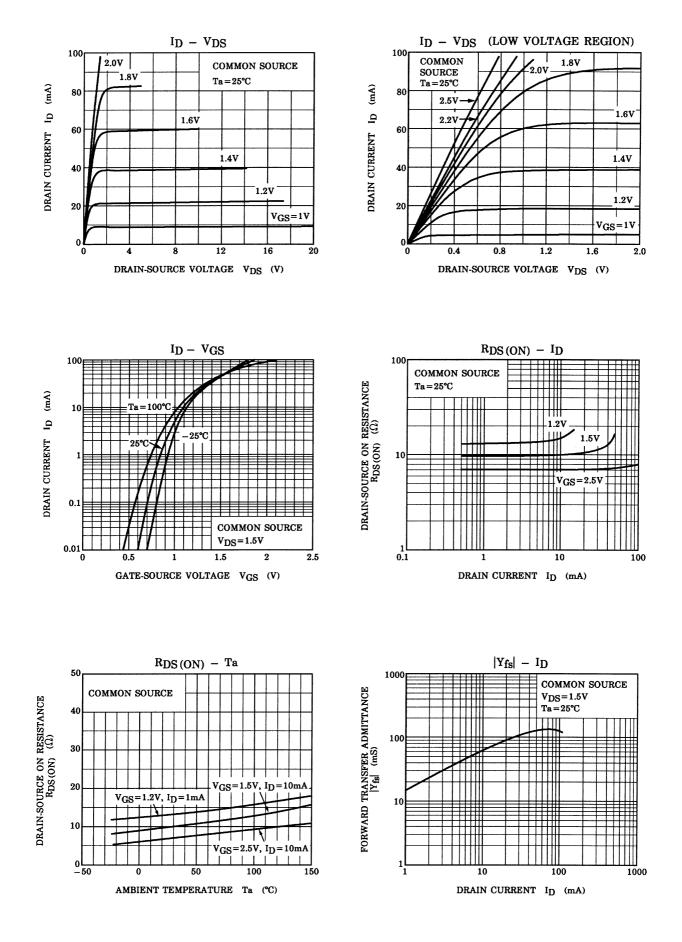


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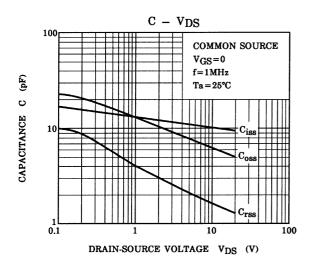


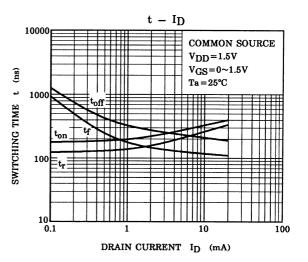
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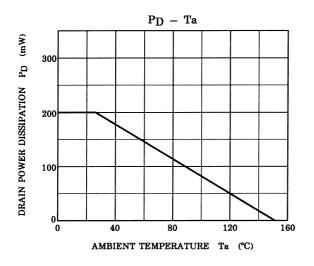
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