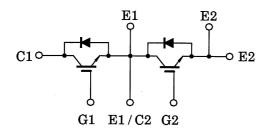
TOSHIBA GTR Module Silicon N Channel IGBT

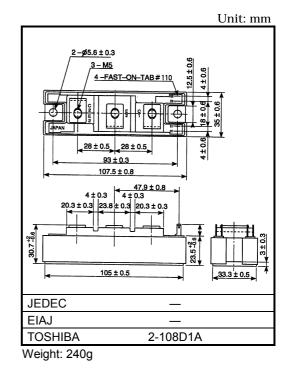
# MG75Q2YS51

High Power Switching Applications Motor Control Applications

- High input impedance
- High speed :  $t_f = 0.3 \ \mu s \ (Max)$ @Inductive load
- Low saturation voltage
- : CE(sat) = 3.6 V(Max)
- Enhancement-mode
- Includes a complete half bridge in one package
- The electrodes are isolated from case

#### **Equivalent Circuit**





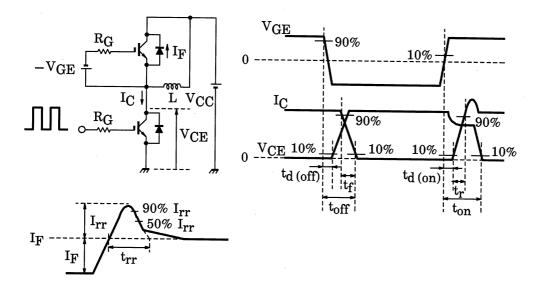
#### Maximum Ratings (Ta = 25°C)

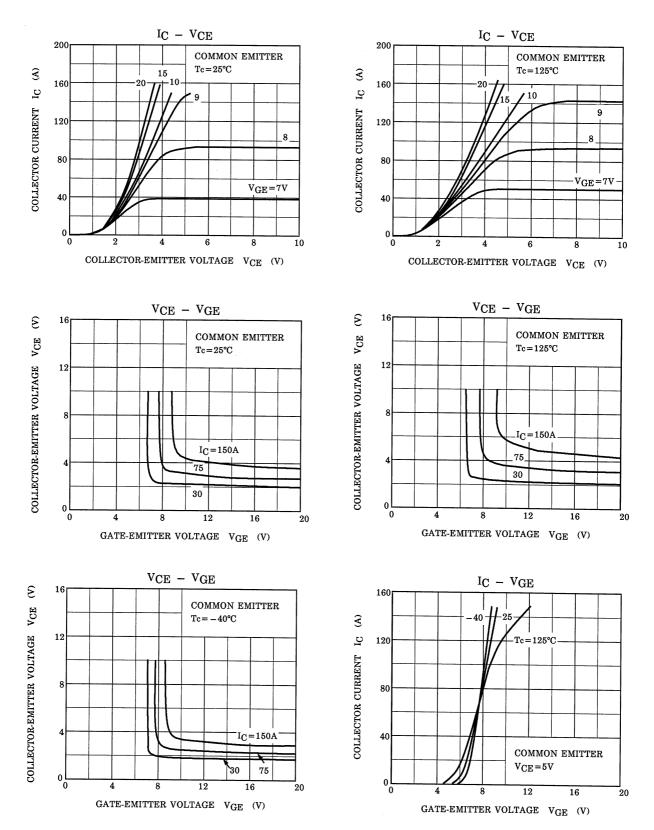
Characteristic		Symbol	Rating	Unit	
Collector-emitter voltage		V <sub>CES</sub>	1200	V	
Gate-emitter voltage		V <sub>GES</sub>	±20	V	
Collector current	DC	I <sub>C</sub> (25°C / 80°C)	100 / 75	А	
	1ms	I <sub>CP</sub> (25°C / 80°C)	200 / 150	~	
Forward current	DC	١ <sub>F</sub>	75	А	
	1ms	I <sub>FM</sub>	150	~	
Collector power dissipation (Tc = 25°C)		P <sub>C</sub>	600	W	
Junction temperature		Tj	150	°C	
Storage temperature range		T <sub>stg</sub>	-40 ~ 125	°C	
Isolation voltage		V <sub>Isol</sub>	2500 (AC 1minute)	V	
Screw torque (Terminal / mounting)		—	3/3	N∙m	

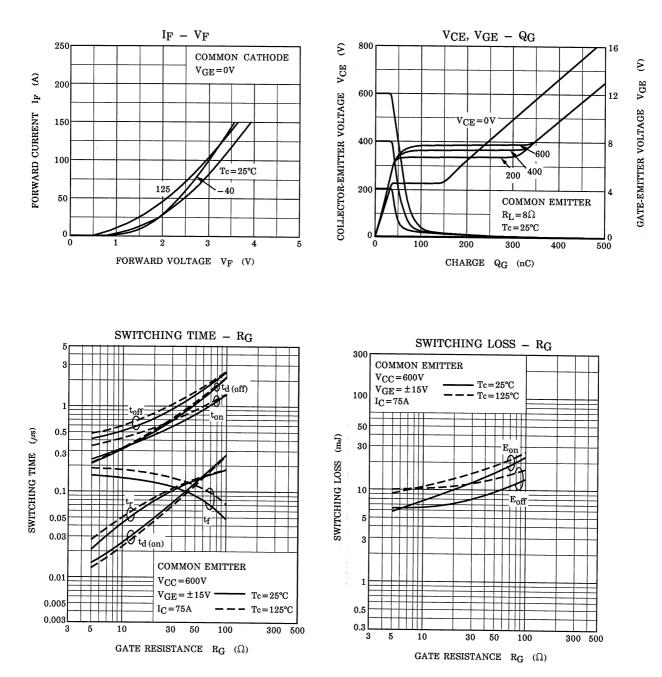
**Electrical Characteristics (Ta = 25°C)** 

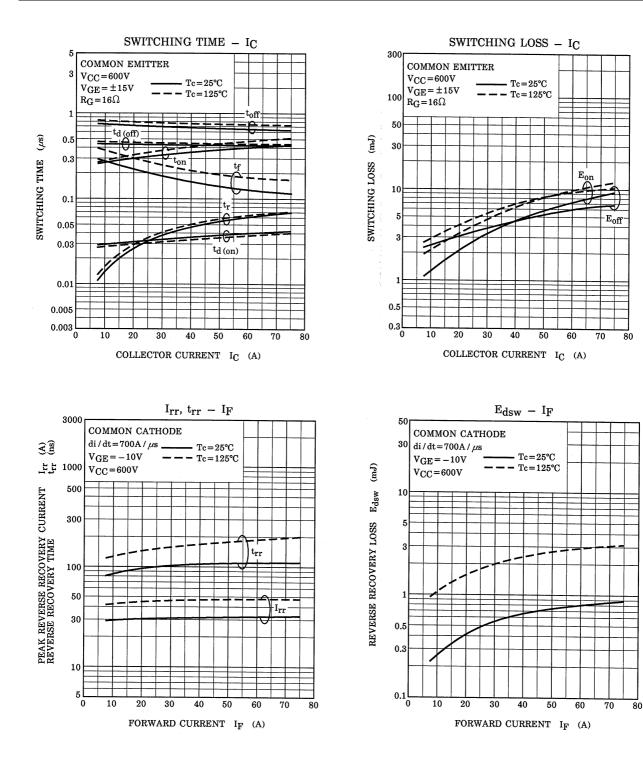
Ch	Characteristic Symbol Test Condition		on	Min	Тур.	Max	Unit	
Gate leakage current		I <sub>GES</sub>	V <sub>GE</sub> = ±20 V, V <sub>CE</sub> = 0		_	_	±500	nA
Collector cut-off current		ICES	V <sub>CE</sub> = 1200 V, V <sub>GE</sub> = 0		_	—	1.0	mA
Gate-emitter cut-off voltage		V <sub>GE (off)</sub>	I <sub>C</sub> = 75 mA, V <sub>CE</sub> = 5 V		3.0	_	6.0	V
Collector-emitter saturation voltage		V <sub>CE (sat)</sub>	I <sub>C</sub> = 75 A,V <sub>GE</sub> = 15 V	T <sub>j</sub> = 25°C	—	2.8	3.6	v
				T <sub>j</sub> = 125°C	_	3.1	4.0	
Input capacitance	9	Cies	V <sub>CE</sub> = 10 V, V <sub>GE</sub> = 0,f = 1 MHz		_	8.5	_	nF
Switching time	Turn-on delay time	t <sub>d (on)</sub>	Inductive load $V_{CC} = 600 V$ $I_C = 75 A$ $V_{GE} = \pm 15 V$ $R_G = 16 \Omega$		_	0.05	_	μs
	Rise-time	tr			_	0.05	_	
	Turn-on time	t <sub>on</sub>			_	0.2	_	
	Turn-off delay time	t <sub>d (off)</sub>			_	0.5	_	
	Fall time	t <sub>f</sub>		(Note 1)	_	0.1	_	
	Turn-off time	t <sub>off</sub>			_	0.6	_	
Forward voltage	orward voltage $V_F$ $I_F$ = 75 A, $V_{GE}$ = 0			_	2.4	3.5	V	
Reverse recovery time		t <sub>rr</sub>	I <sub>F</sub> = 75 A, V <sub>GE</sub> = -10 V di / dt = 700 A / μs (Note 1)		_	0.1	0.25	μs
Thermal resistance		R <sub>th (j-c)</sub>	Transistor stage		_	—	0.2	°C/W
			Diode stage		—	—	0.47	

Note 1: Switching time and reverse recovery time test circuit & timing chart



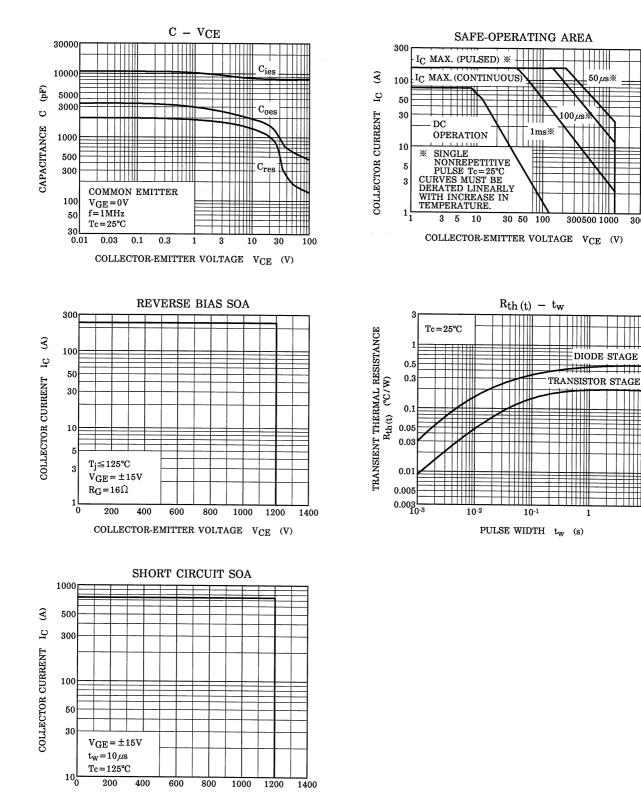






3000

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COLLECTOR-EMITTER VOLTAGE VCE (V)

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