

HVD376B

Variable Capacitance Diode for VCO

REJ03G0540-0200 Rev.2.00 Apr 26, 2005

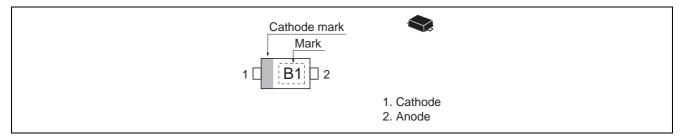
Features

- High capacitance ratio (n = 4.3min) and good C-V linearity.
- High Q circuit can be composed due to low series resistance. (rs = $0.8 \Omega \text{ max}$)
- To be usable at low voltage.
- Super small Flat Lead Package (SFP) is suitable for surface mount design.

Ordering Information

Type No.	Laser Mark	Package Name	Package Code (Previous Code)
HVD376B	B1	SFP	PUSF0002ZB-A
			(SFP)

Pin Arrangement





Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Value	Unit
Reverse voltage	V _R	15	V
Junction temperature	Tj	125	°C
Storage temperature	Tstg	–55 to +125	℃

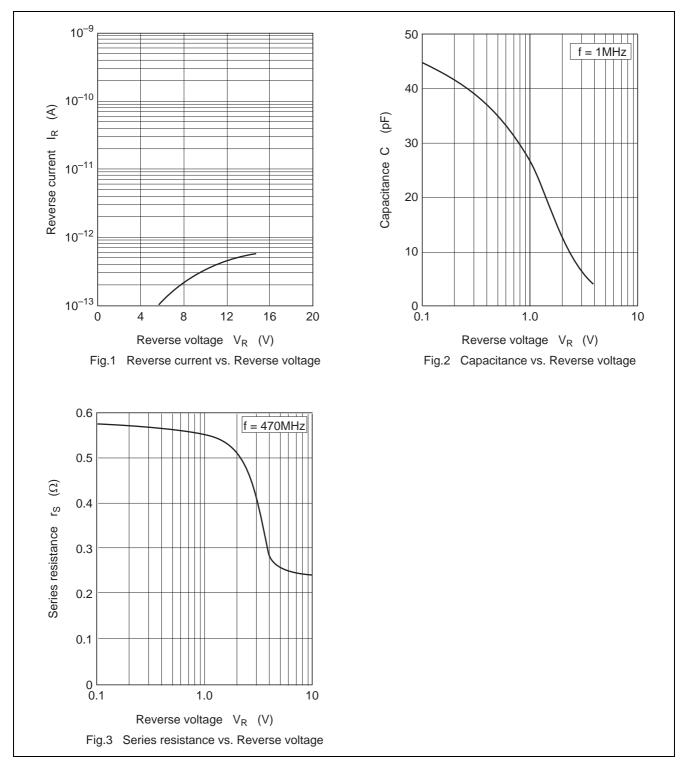
Electrical Characteristics

						$(Ta = 25^{\circ}C)$
Item	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse current	I _{R1}	_	—	10	nA	V _R =10 V
	I _{R2}	-	_	100		V _R = 10 V, Ta = 60°C
Capacitance	C _{0.2}	39.5	—	44.5	pF	V _R = 0.2V, f = 1MHz
	C ₁	25.0	—	28.5		$V_R = 1V$, f = 1MHz
	C _{2.3}	8.75	—	12.05		V _R = 2.3V, f = 1MHz
	C ₄	4.80	_	6.80		$V_R = 4V$, f = 1MHz
Capacitance ratio	n ₁	4.30	—	_	—	C ₁ /C ₄
	n ₂	3.55	_	_	_	C _{0.2} /C _{2.3}
Series resistance	r _S	_	_	0.8	Ω	V _R = 1 V, f = 470 MHz

Note: Please do not use the soldering iron due to avoid high stress to the SFP package.

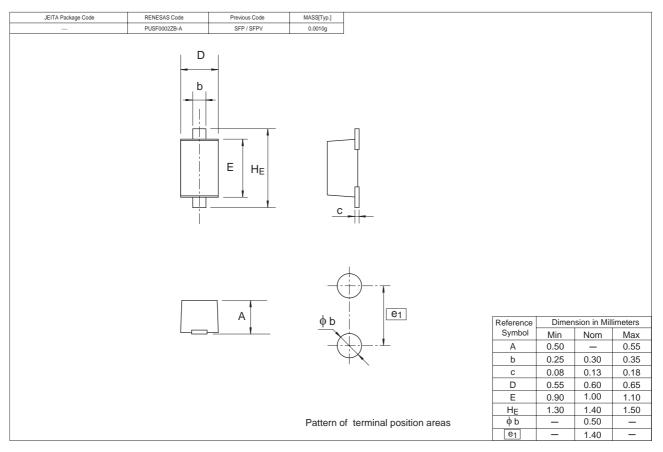


Main Characteristic





Package Dimensions





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