

# HVD376B

Variable Capacitance Diode for VCO

REJ03G0540-0200 Rev.2.00 Apr 26, 2005

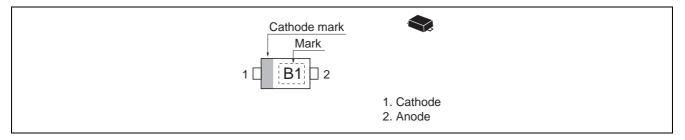
### Features

- High capacitance ratio (n = 4.3min) and good C-V linearity.
- High Q circuit can be composed due to low series resistance. (rs =  $0.8 \Omega \text{ max}$ )
- To be usable at low voltage.
- Super small Flat Lead Package (SFP) is suitable for surface mount design.

### **Ordering Information**

Type No.	Laser Mark	Package Name	Package Code (Previous Code)
HVD376B	B1	SFP	PUSF0002ZB-A
			(SFP)

### **Pin Arrangement**





# **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Value	Unit
Reverse voltage	V <sub>R</sub>	15	V
Junction temperature	Tj	125	°C
Storage temperature	Tstg	–55 to +125	℃

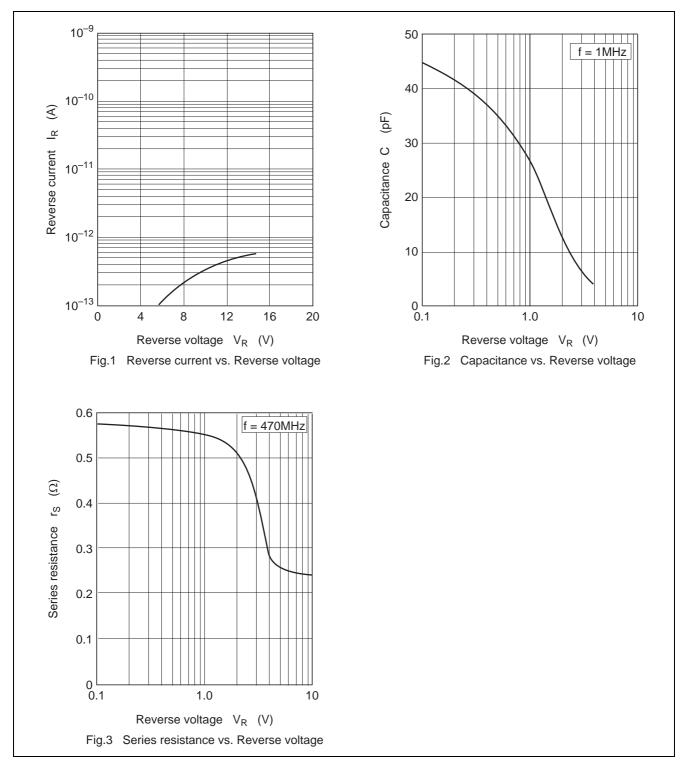
### **Electrical Characteristics**

						$(Ta = 25^{\circ}C)$
Item	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse current	I <sub>R1</sub>	_	—	10	nA	V <sub>R</sub> =10 V
	I <sub>R2</sub>	-	_	100		V <sub>R</sub> = 10 V, Ta = 60°C
Capacitance	C <sub>0.2</sub>	39.5	—	44.5	pF	V <sub>R</sub> = 0.2V, f = 1MHz
	C <sub>1</sub>	25.0	—	28.5		$V_R = 1V$ , f = 1MHz
	C <sub>2.3</sub>	8.75	—	12.05		V <sub>R</sub> = 2.3V, f = 1MHz
	C <sub>4</sub>	4.80	_	6.80		$V_R = 4V$ , f = 1MHz
Capacitance ratio	n <sub>1</sub>	4.30	—	_	—	C <sub>1</sub> /C <sub>4</sub>
	n <sub>2</sub>	3.55	_	_	_	C <sub>0.2</sub> /C <sub>2.3</sub>
Series resistance	r <sub>S</sub>	_	_	0.8	Ω	V <sub>R</sub> = 1 V, f = 470 MHz

Note: Please do not use the soldering iron due to avoid high stress to the SFP package.

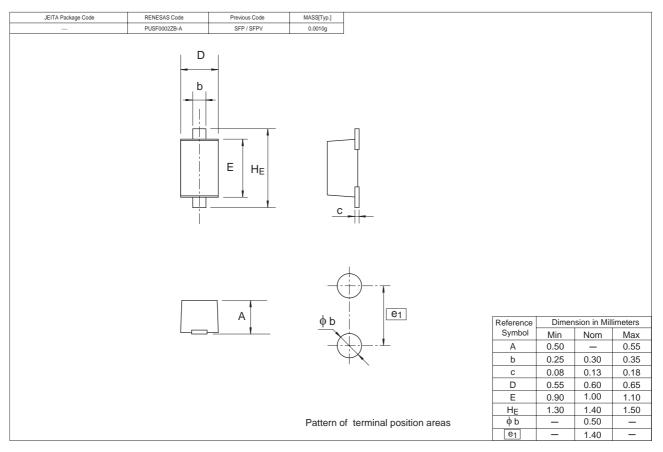


### **Main Characteristic**





# Package Dimensions





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