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Quad. 2-input OR Gates



ADE-205-417 (Z) 1st. Edition Sep. 2000

Features

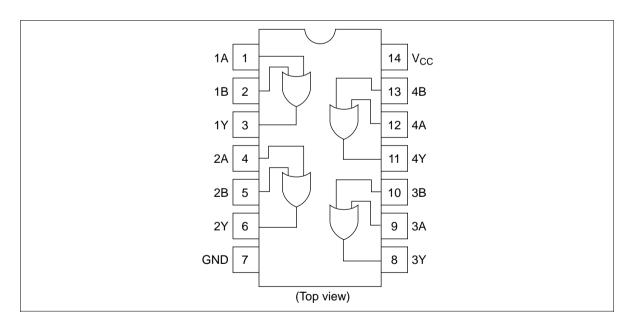
High Speed Operation: t_{pd} = 10 ns typ (C_L = 50 pF)
 High Output Current: Fanout of 10 LSTTL Loads

• Wide Operating Voltage: $V_{CC} = 2 \text{ to } 6 \text{ V}$

• Low Input Current: 1 μA max

• Low Quiescent Supply Current: I_{CC} (static) = 1 μ A max (Ta = 25°C)

Pin Arrangement



DC Characteristics

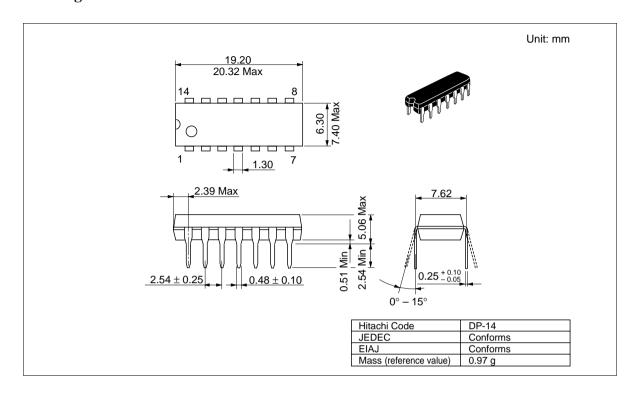
			Ta = 25°C		Ta = −40 to +85°C		_			
Item	Symbol	V _{cc} (V)	Min	Тур	Max	Min	Max	Unit	Test Condition	ns
Input voltage	V _{IH}	2.0	1.5	_	_	1.5	_	V		
		4.5	3.15	i —		3.15	_	_		
		6.0	4.2	_	_	4.2	_	=		
	V _{IL}	2.0	_	_	0.5	_	0.5	V		
		4.5	_	_	1.35	_	1.35	_		
		6.0	_	_	1.8	_	1.8	=		
Output voltage	V _{OH}	2.0	1.9	2.0	_	1.9	_	V	Vin = V _{IH} or V _{IL}	$I_{OH} = -20 \mu A$
		4.5	4.4	4.5	_	4.4	_	=		
		6.0	5.9	6.0	_	5.9	_	=		
		4.5	4.18	s —	_	4.13	_	=		$I_{OH} = -4 \text{ mA}$
		6.0	5.68	3 —	_	5.63	_	=		$I_{OH} = -5.2 \text{ mA}$
	V _{OL}	2.0	_	0.0	0.1	_	0.1	V	Vin = V _{IH} or V _{IL}	I _{OL} = 20 μA
		4.5	_	0.0	0.1	_	0.1	=		
		6.0	_	0.0	0.1	_	0.1	=		
		4.5	_	_	0.26	_	0.33	_		I _{OL} = 4 mA
		6.0	_	_	0.26	_	0.33	_		I _{OL} = 5.2 mA
Input current	lin	6.0	_	_	±0.1	_	±1.0	μΑ	Vin = V _{CC} or GND	
Quiescent supply current	I _{cc}	6.0	_	_	1.0	_	10	μΑ	Vin = V_{CC} or GND, lout = 0 μ A	

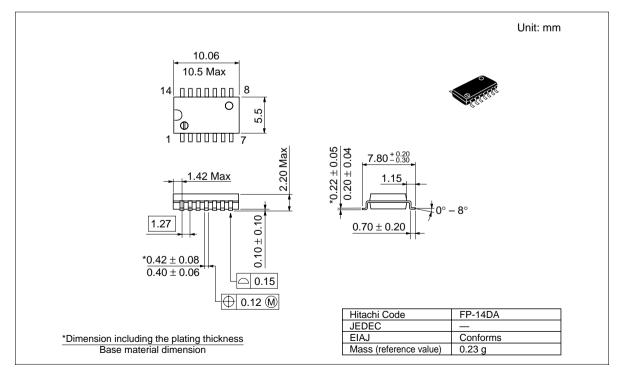
AC Characteristics ($C_L = 50 \text{ pF}$, Input $t_r = t_f = 6 \text{ ns}$)

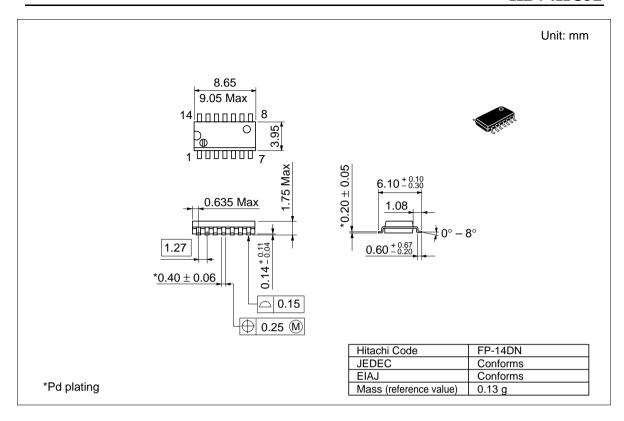
Ta = -40 to Ta = 25°C +85°C

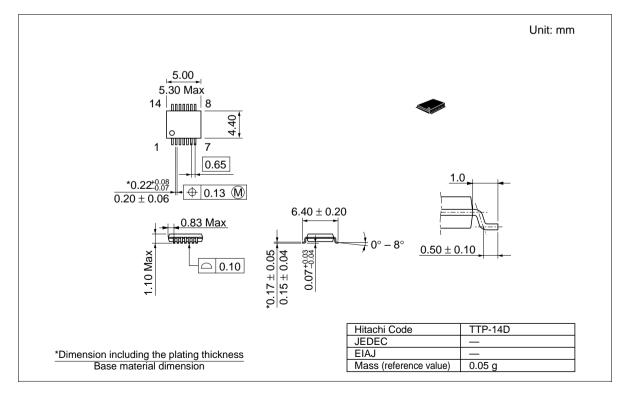
Item	Symbol	V _{cc} (V)	Min	Тур	Max	Min	Max	Unit	Test Conditions
Propagation delay	t _{PLH}	2.0	_	_	100	_	125	ns	
time		4.5	_	10	20	_	25	_	
		6.0	_	_	17	_	21	_	
	t _{PHL}	2.0	_	_	100	_	125	ns	
		4.5	_	10	20	_	25	_	
		6.0	_	_	17	_	21	_	
Output rise time	t _{TLH}	2.0	_	_	75	_	95	ns	
		4.5	_	5	15	_	19	_	
		6.0	_	_	13	_	16	_	
Output fall time	t _{THL}	2.0	_	_	75	_	95	ns	
		4.5	_	5	15	_	19	_	
		6.0	_	_	13	_	16	=	
Input capacitance	Cin	_	_	5	10	_	10	pF	

Package Dimensions









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