

# **HA13165H**

## Multiple Voltage Regulator for Car Audio

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#### **Description**

The HA13165H is a compact multiple voltage regulator for car audio system. This IC has seven output system, these are 5.7 V output for a microcontroller, 7 V output for CD driver, 8.5 V output for audio control, 10 V output for illuminations, 5.0 V output for independent from microcontroller line, and high side switch for remote-ANT and remote-external AMP.

#### **Functions**

#### General

- ACC power monitor circuit is built-in as to detect low voltage.
- Low saturation output (PNP output) used for audio output.
- Adjustable voltage for illumination output by changing an external resistor.

#### **Protections**

- Output current limit circuit to avoid device destruction caused by shorted output, etc.
- High surge input protector against VCC and ACC.
- Built in a thermal shutdown circuit to prevent against the thermal destruction.

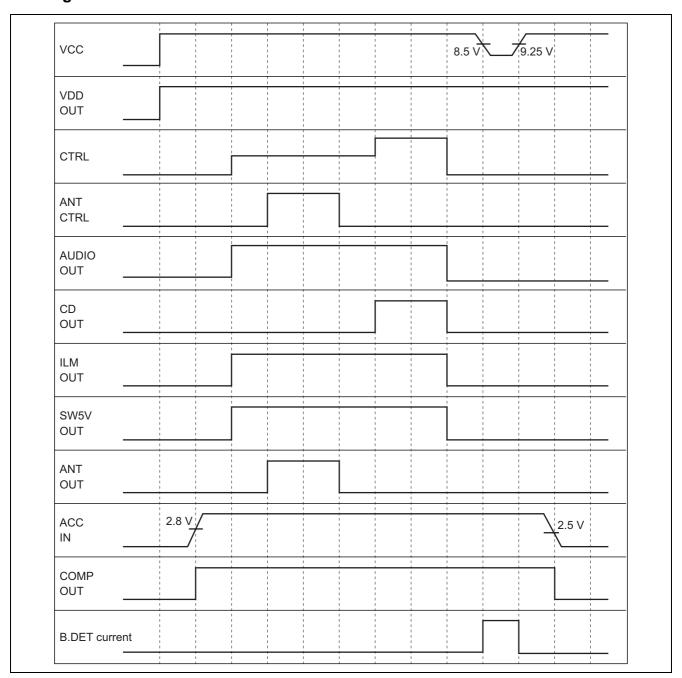
## Pin Description and Equivalent Circuit

				Function			
Pin							Surge
No.	Pin Name	Specification	Equivalent Circuit	Normal Operation	TSD	24 V	Input
1	_	NC		_	_	_	_
2	ANT OUT	VCC-1 V/500 mA min	Vcc γ00 kΩ 10 kΩ	Output voltage is VCC-1 V when M or H level to CTRL pin and H level to ANT- CTRL.	0 V	0 V	0 V
3	ACC IN	_	45 kΩ 	Connected to ACC.		_	_
4	VDD OUT	5.7 V/100 mA min	Vcc Vcc 175 kΩ 50 kΩ	Regular 5.7 V.	5.7 V	5.7 V	0 V
5	SW5V OUT	5.0 V/100 mA min	-VDD -Vcc	Output voltage is 5 V when M or H level applied to CTRL pin.	0 V	0 V	0 V
6	COMP OUT	5.0 V/100 mA min	≤50 kΩ	Output for ACC detector	0 V	5 V (ACC Hi)	0 V
7	ANT CTRL	_	51 kΩ 49 kΩ ≶	L: ANT output OFF H: ANT output ON	_	_	_
8	VCC	_		Connected to VCC		_	

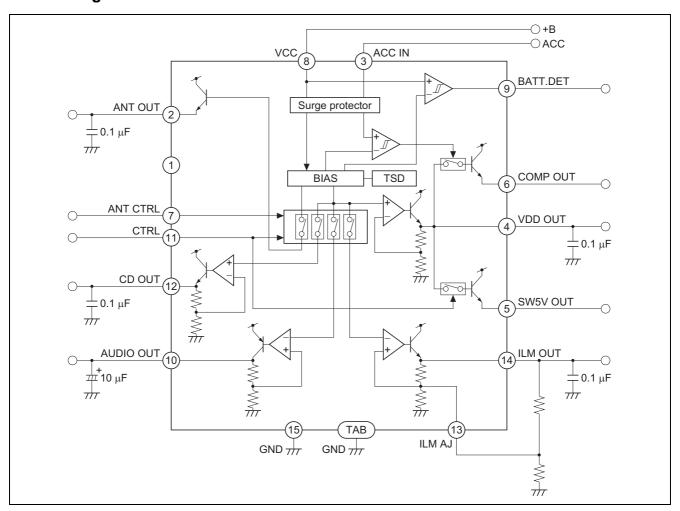
# Pin Description and Equivalent Circuit (cont.)

1 1				Function			
Pin	Pin Name	Specification	Favringlant Cinavit	Name of Operation	TOD	04.1/	Surge
No.	BATT DET	Specification	Equivalent Circuit	Normal Operation	TSD Detect	24 V	Input Not
		_	250 kΩ \$\frac{10 kΩ}{10 m}\$	Low battery detect.		Detect	detect
10	AUDIO OUT	8.5 V/500 mA min	Vcc Vcc 77.3 kΩ \$12.3 kΩ	Output voltage is 8.5 V when M or H level applied to CTRL pin.	0 V	0 V	0 V
11	CTRL	_	65 kΩ 35 kΩ 7777	L: BIAS OFF M: BIAS ON H: CD ON		_	_
12	CD OUT	7.0 V/1.3 A min	Vcc Vcc 64.7 kΩ 12.4 kΩ	Output voltage is 7 V when H level applied to CTRL pin.	0 V	0 V	0 V
13	ILM AJ	_	→ Vcc → Vcc	Adjustment pin for ILM output voltage.	_	_	_
14	ILM OUT	10.0 V/500 mA min	33.4 kΩ \$5 kΩ	Output voltage is 10 V when M or H level applied to CTRL pin	0 V	0 V	0 V
15 (	GND	_	,,,	Connected to GND	_	_	_

## **Timing Chart**



## **Block Diagram**



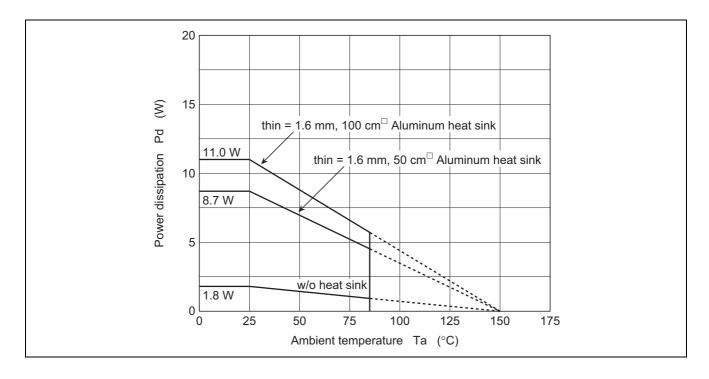
### **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Rating	Unit	Note
Operating power supply voltage	Vcc	18	V	
DC supply voltage	Vcc(DC)	24	V	1
Peak voltage	Vcc(PEAK)	50	V	2
Power dissipation	Pd	36	W	3
Junction temperature	Tj	150	°C	
Operating temperature	Topr	-40 to +85	°C	
Storage temperature	Tstg	-55 to +125	°C	

Notes: Recommended power supply voltage range 10 to 16 V.

- 1. Applied time is less than 60 s.
- 2. Surge pulse as input.
- 3. Ta = 25°C.: Permissible power dissipation when using a heat sink of infinite area. Refer to the derating curves below.

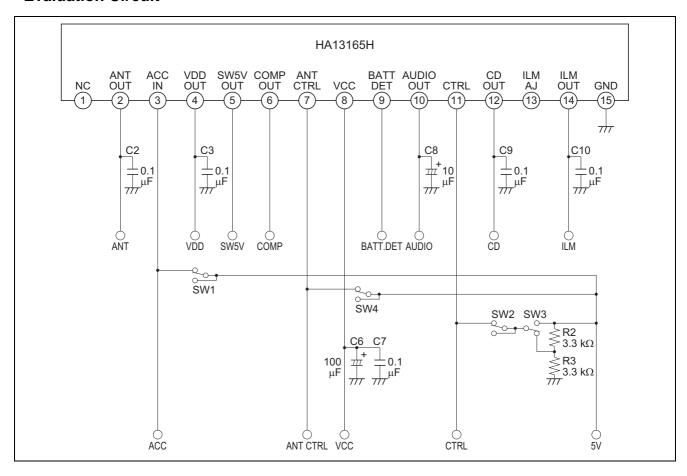


### **Electrical Characteristics**

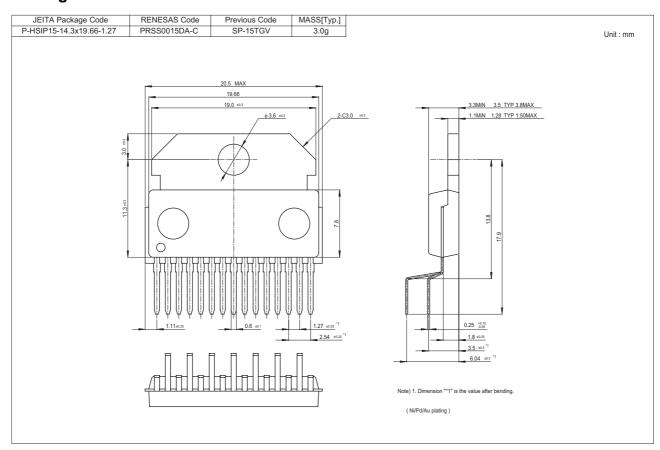
(unless otherwise noted, Vcc = 13.2 V,  $Ta = 25^{\circ}\text{C}$ )

Item		Symbol	Min	Тур	Max	Unit	Test Condition
Standby current		IST	_	460	700	μА	ACC = 0 V, CTRL = 0 V
CTRL L level (STBY mode)		VCL	0	_	1.0	V	
CTRL M level (CD OFF mode)		VCM	2.0	_	3.0	V	
CTRL H level (CD ON mode)		VCH	4.0	_	_	V	
ANT CT	RL L level (ANT OFF mode)	VACL	0	_	2.0	V	
ANT CT	RL H level (ANT ON mode)	VACH	3.0	_	_	V	
VDD	Output voltage	Vo1	5.45	5.7	5.95	V	Io1 = 80 mA
OUT	Voltage regulation	∆Vo11	_	10	50	mV	Vcc = 10 to 16 V, lo1 = 80 mA
	Load regulation	∆Vo12	_	50	100	mV	Io1 = 0 to 80 mA
	Minimum I/O voltage differential	ΔVo13	1	1.0	1.5	V	Io1 = 80 mA
	Output current capacity	lo1	100	250	_	mA	Vo1 ≥ 5.45 V
	Ripple rejection ratio	SVR1	50	60	_	dB	f = 100 Hz, Io1 = 80 mA
CD	Output voltage	Vo2	6.7	7.0	7.3	V	lo2 = 1.0 A
OUT	Voltage regulation	∆Vo21	_	40	100	mV	Vcc = 10 to 16V, lo2 = 1.0 A
	Load regulation	ΔVo22	_	70	150	mV	lo2 = 10m to 1.0 A
	Minimum I/O voltage differential	ΔVo23	_	1.0	1.5	V	lo2 = 1.0 A
	Output current capacity	lo2	1.3	2.0	_	Α	Vo2 ≥ 6.7 V
	Ripple rejection ratio	SVR2	45	50		dB	f = 100 Hz, lo2 = 1.0 A
AUDIO	Output voltage	Vo3	8.0	8.5	9.0	V	Io3 = 400 mA
OUT	Voltage regulation	∆Vo31		30	90	mV	Vcc = 10 to 16 V, lo3 = 400 mA
	Load regulation	ΔVo32		100	200	mV	Io3 = 10 to 400 mA
	Minimum I/O voltage differential	ΔVo33	_	0.4	0.9	V	lo3 = 400 mA
	Output current capacity	lo3	500	850	_	mA	Vo3 ≥ 8.0 V
	Ripple rejection ratio	SVR3	40	50		dB	f = 100 Hz, Io3 = 400 mA
ILM	Output voltage	Vo4	9.35	9.85	10.35	V	Io4 = 400 mA
OUT	Voltage regulation	∆Vo41	_	40	100	mV	Vcc = 12.5 to 16 V, lo4 = 400 mA
	Load regulation	∆Vo42	_	50	100	mV	lo4 = 10 to 400 mA
	Minimum I/O voltage differential	ΔV043	_	1.0	1.5	V	Io4 = 400 mA
	Output current capacity	lo4	500	900	_	mA	Vo4 ≥ 9.35 V
	Ripple rejection ratio	SVR4	32	40	_	dB	f = 100 Hz, Io4 = 400 mA
ANT	Differential I/O voltage	∆Vo51		1.0	1.5	V	Io5 = 500 mA
OUT	Load regulation	ΔVo52		350	600	mV	Io5 = 10 to 500 mA
	Output current capacity	lo5	500	900	—	mA	Vo5 ≥ 11.7 V
SW5V	Output voltage	Vo6	4.6	5.0	5.4	V	lo6 = 80 mA, VDD = no load
OUT	Output current capacity	lo6	100	300	_	mA	Vo6 ≥ 4.6 V
ACC	Output voltage	Vo7	4.6	5.0	5.4	V	Io7 = 40 mA, VDD = no load
OUT	Output current capacity	lo7	100	300	_	mA	Vo7 ≥ 4.6 V
	Rise threshold voltage	VTHH7	2.6	2.8	3.0	V	
	Hysteresis range	ΔVTH7	0.2	0.3	0.4	V	
BATT.	Threshold voltage	VTHH8	8.3	8.6	8.9	V	
DET	Hysteresis range	ΔVTH8	0.55	0.75	0.95	V	
	Output current capacity	lo8	200	_	—	μΑ	Vo = 0.3 V

#### **Evaluation Circuit**



## **Package Dimensions**



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