

HVD316

Variable Capacitance Diode for BS/CS tuner

REJ03G1467-0100 Rev.1.00 Jul 27, 2006

Features

- High capacitance ratio (n = 9.0 min)
- Low series resistance. ($r_s = 2.2 \Omega max$)
- Super small Flat Lead Package (SFP) is suitable for surface mount design.

Ordering Information

Type No.	Laser Mark	Package Name	Package Code
HVD316	B3	SFP	PUSF0002ZB-A

Pin Arrangement

1 [Cathode mark Mark B3 2	A state1. Cathode
		2. Anode



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Value	Unit
Reverse voltage	V _R	30	V
Junction temperature	Tj	125	°C
Storage temperature	Tstg	–55 to +125	۵°C

Electrical Characteristics

 $(Ta = 25^{\circ}C)$ Item Symbol Unit **Test Condition** Min Тур Max Reverse current I_{R1} 10 nA $V_{R} = 30 V$ — — _ $V_R = 30 V$, Ta = $60^{\circ}C$ I_{R2} — 100 Capacitance C_1 5.16 7.22 $V_R = 1 V, f = 1 MHz$ ____ pF C₂₅ 0.48 0.76 $V_R = 25V, f = 1 MHz$ _ Capacitance ratio n 9.0 _ C_1 / C_{25} _ 2.20 $V_{R} = 1 V, f = 470 MHz$ Series resistance Ω rs — _ $\Delta C/C *^1$ ___ 6.00 $V_R = 1$ to 25 V, f = 1 MHz Matching error ___ %

Notes: 1. C.C system (Continuous Connected taping system) enable to make any 10 pcs of ∆C/C continuous in a reel, expect extention to another group. Calculate Matching Error,

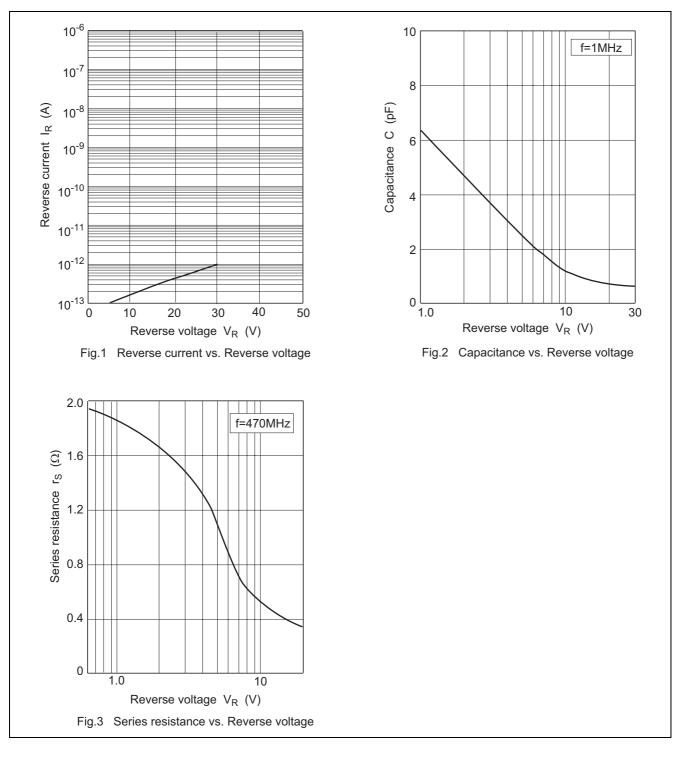
Calculate Matching Err

 $\Delta C/C = \frac{(Cmax - Cmin)}{Cmin} \times 100 \ (\%)$

2. For SFP package, the material of lead is exposed for cutting plane. There for, soldering nature of lead tip part is considered as unquestioned. Please kindly consider soldering nature.

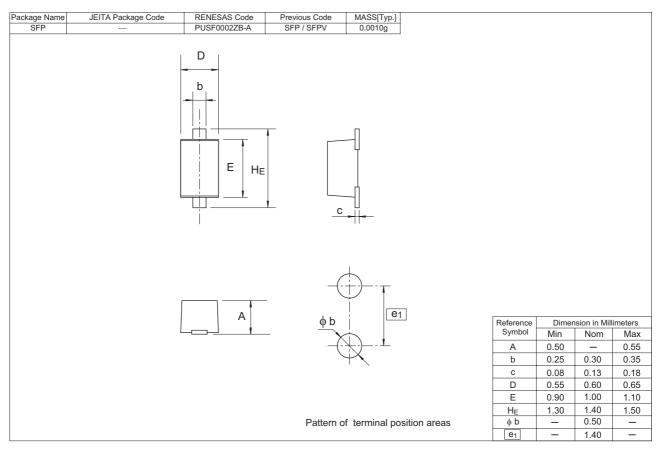


Main Characteristic





Package Dimensions





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