

H5N2007FN

Silicon N Channel MOS FET
High Speed Power Switching

REJ03G0370-0100Z

Rev.1.00

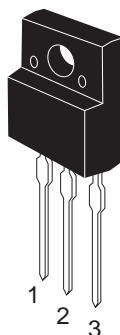
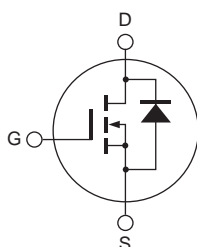
May.28.2004

Features

- Low on-resistance
- Low leakage current
- High speed switching

Outline

TO-220FN



1. Gate
2. Drain
3. Source

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to Source voltage	V_{DSS}	200	V
Gate to Source voltage	V_{GSS}	±30	V
Drain current	I_D	25	A
Drain peak current	$I_{D(pulse)}$ ^{Note1}	100	A
Body-Drain diode reverse Drain current	I_{DR}	25	A
Body-Drain diode reverse Drain peak current	$I_{DR(pulse)}$ ^{Note1}	100	A
Avalanche current	I_{AP} ^{Note3}	9	A
Avalanche energy	E_{AR} ^{Note3}	5.4	mJ
Channel dissipation	P_{ch} ^{Note2}	30	W
Channel to case thermal impedance	θ_{ch-c}	4.17	°C/W
Channel temperature	T_{ch}	150	°C
Storage temperature	T_{stg}	−55 to +150	°C

Notes: 1. $PW \leq 10 \mu s$, duty cycle $\leq 1\%$

2. Value at $T_c = 25^\circ C$

3. $STch = 25^\circ C$, $T_{ch} \leq 150^\circ C$

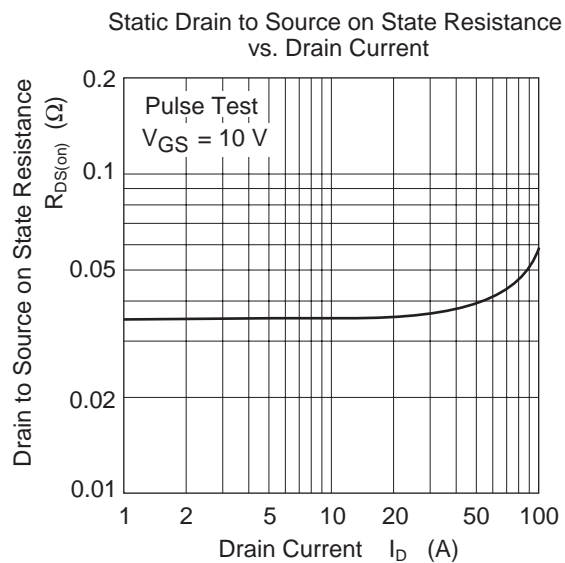
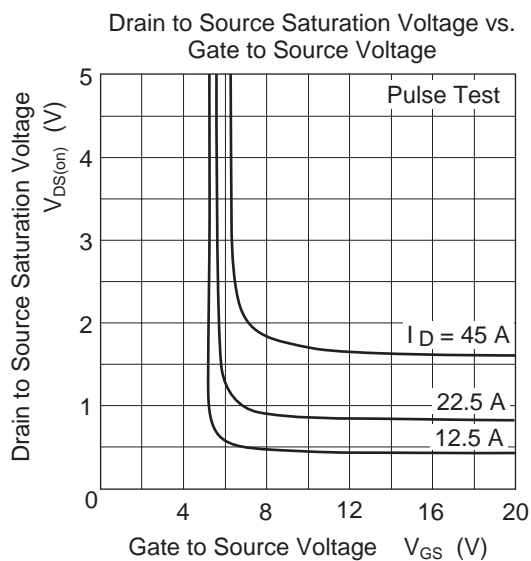
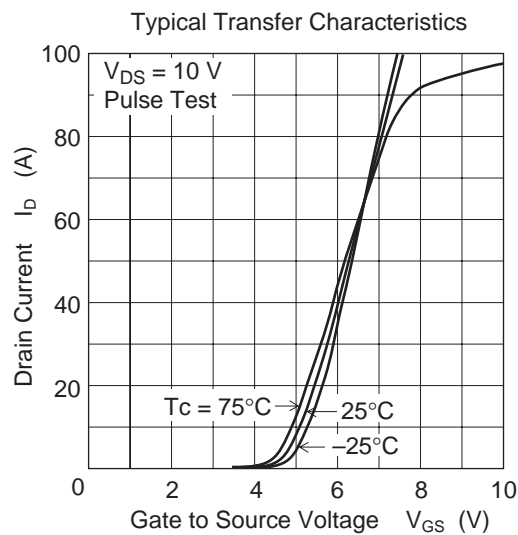
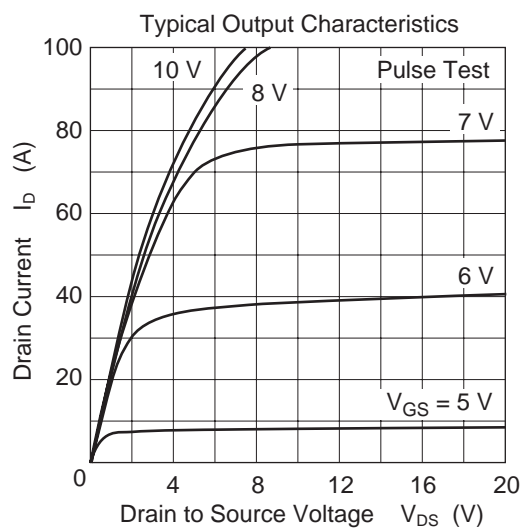
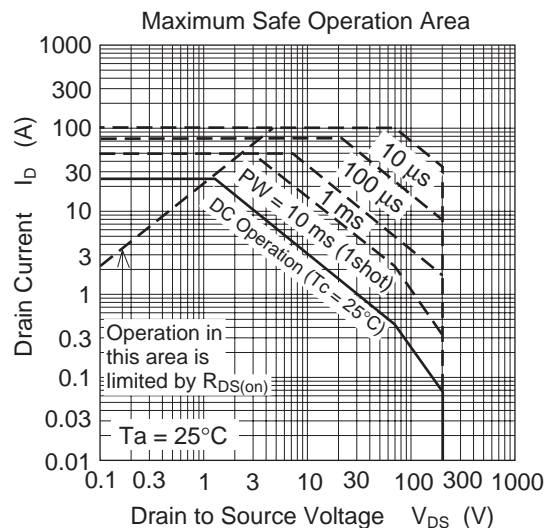
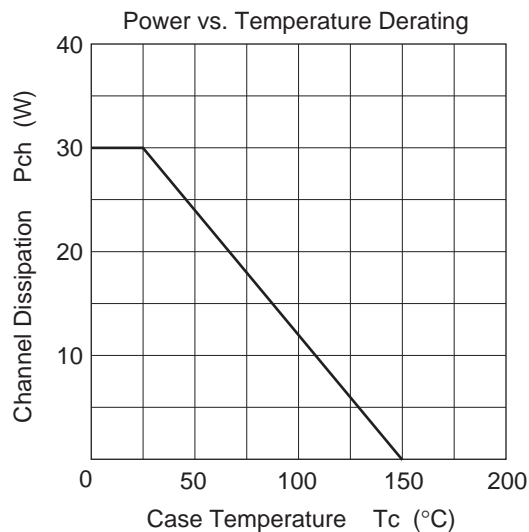
Electrical Characteristics

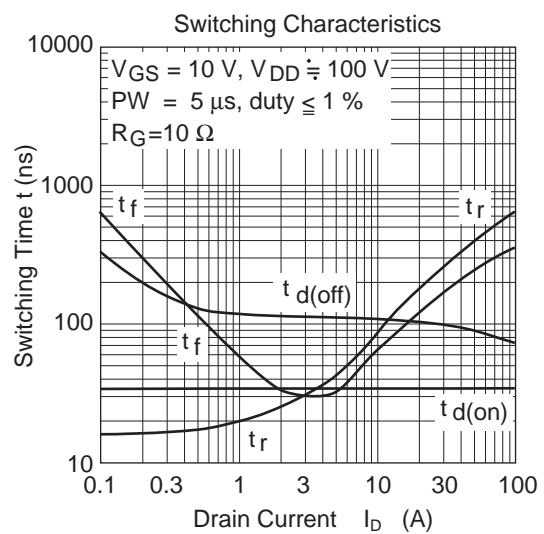
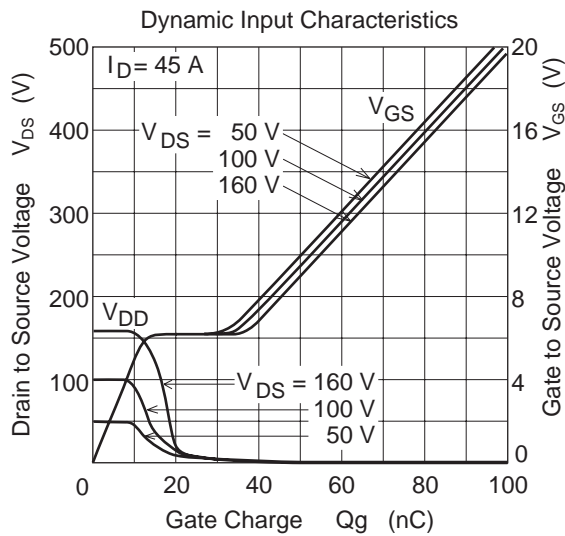
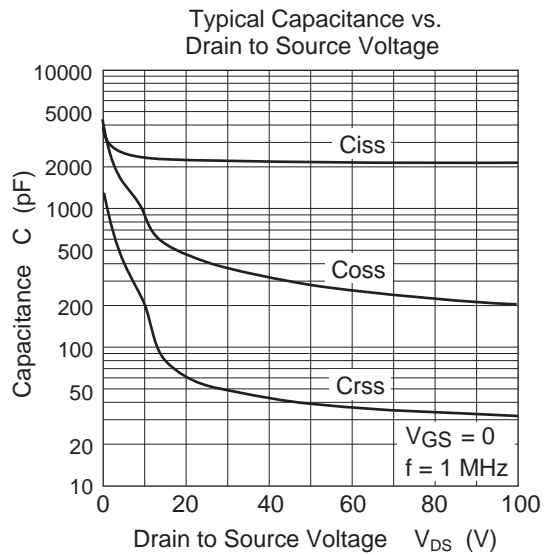
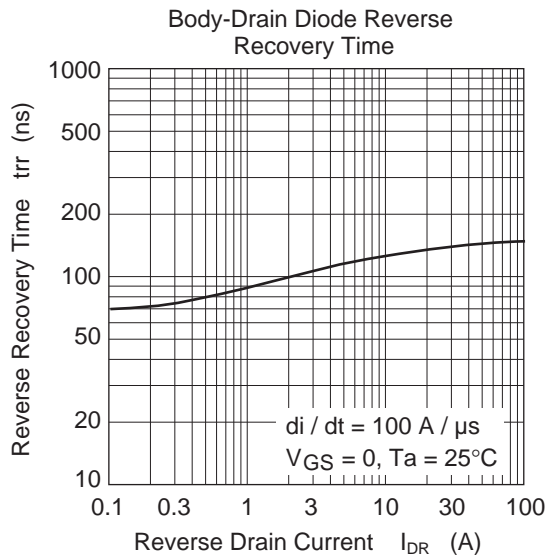
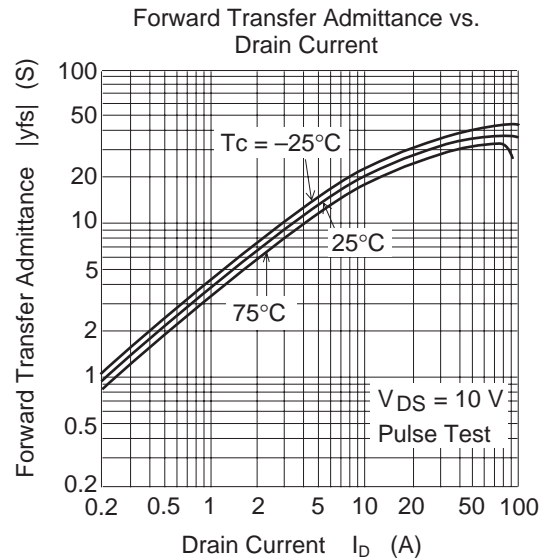
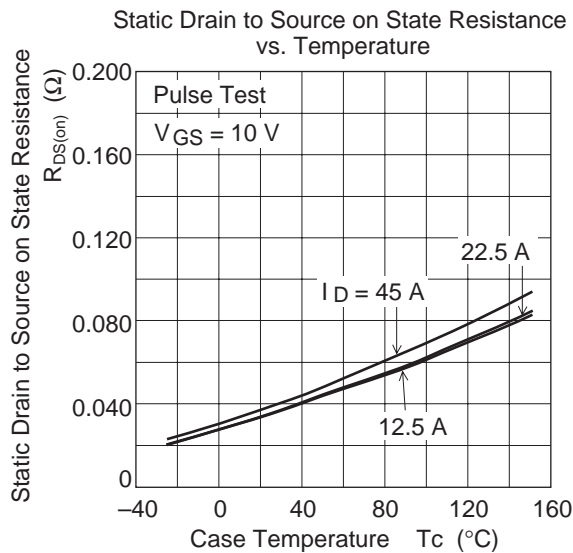
(Ta = 25°C)

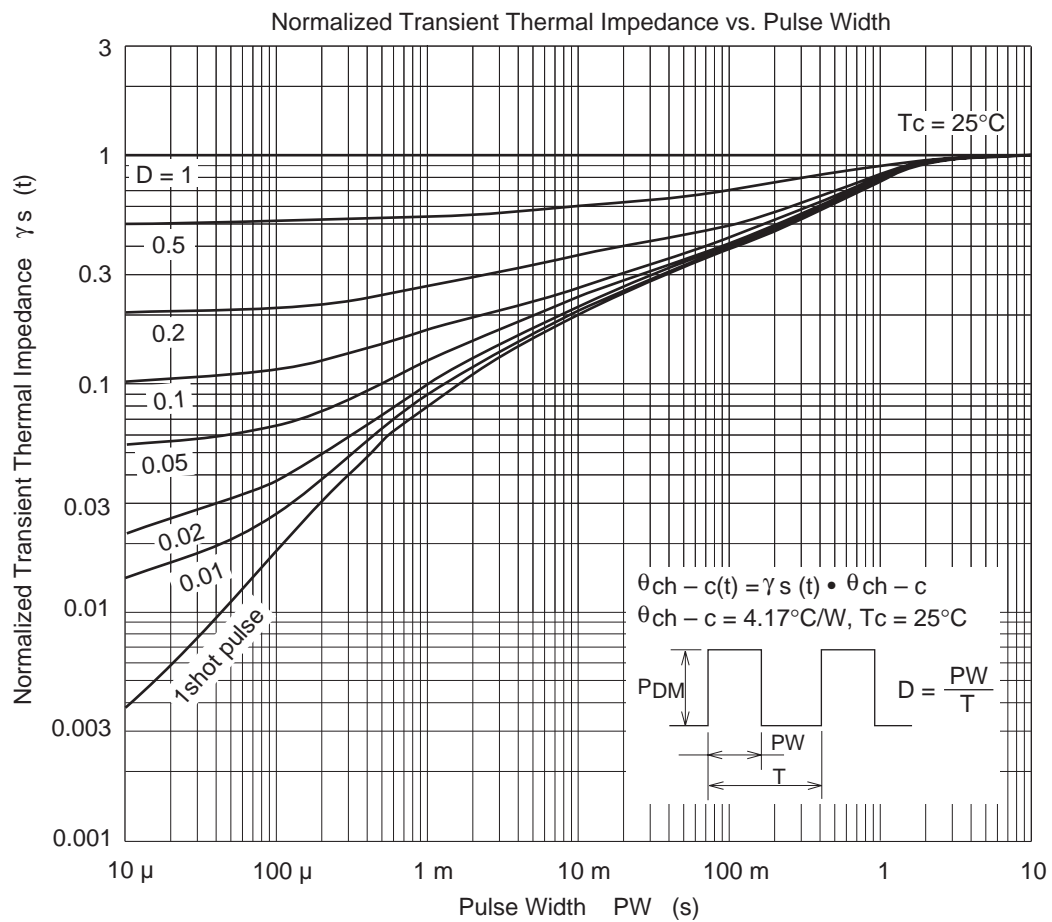
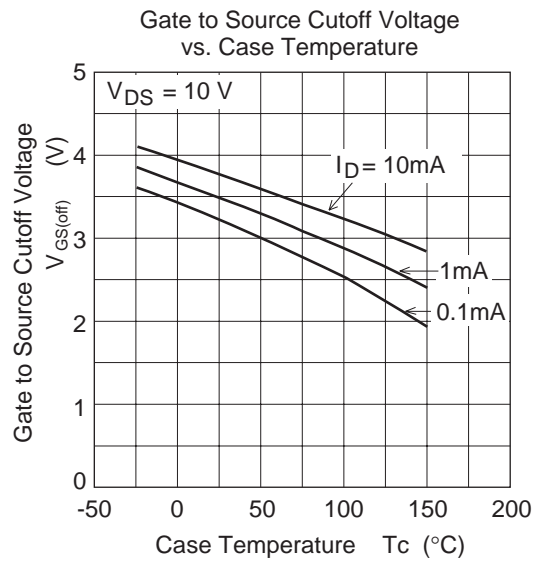
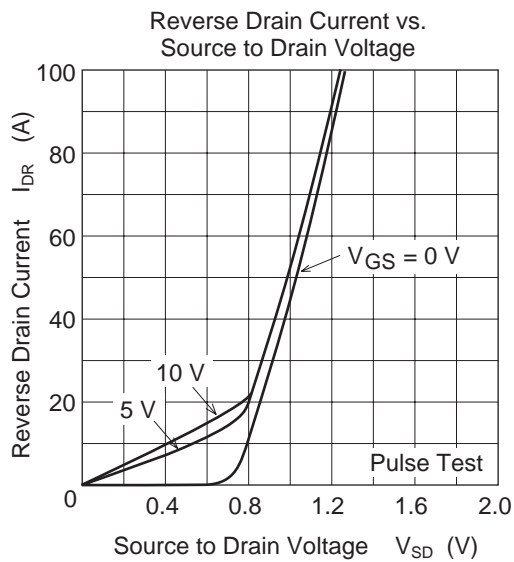
Item	Symbol	Min	Typ	Max	Unit	Test conditions
Drain to Source breakdown voltage	$V_{(BR)DSS}$	200	—	—	V	$I_D = 10 \text{ mA}$, $V_{GS} = 0$
Zero Gate voltage drain current	I_{DSS}	—	—	1	μA	$V_{DS} = 200 \text{ V}$, $V_{GS} = 0$
Gate to Source leak current	I_{GSS}	—	—	± 0.1	μA	$V_{GS} = \pm 30 \text{ V}$, $V_{DS} = 0$
Gate to Source cutoff voltage	$V_{GS(off)}$	3.0	—	4.0	V	$V_{DS} = 10 \text{ V}$, $I_D = 1 \text{ mA}$
Forward transfer admittance	$ y_{fs} $	13	22	—	S	$I_D = 12.5 \text{ A}$, $V_{DS} = 10 \text{ V}$ ^{Note4}
Static Drain to Source on state resistance	$R_{DS(on)}$	—	0.036	0.047	Ω	$I_D = 12.5 \text{ A}$, $V_{GS} = 10 \text{ V}$ ^{Note4}
Input capacitance	C_{iss}	—	2200	—	pF	$V_{DS} = 25 \text{ V}$ $V_{GS} = 0$ $f = 1 \text{ MHz}$
Output capacitance	C_{oss}	—	410	—	pF	
Reverse transfer capacitance	C_{rss}	—	54	—	pF	
Turn-on delay time	$t_{d(on)}$	—	35	—	ns	$I_D = 12.5 \text{ A}$ $V_{GS} = 10 \text{ V}$ $R_L = 8 \Omega$ $R_g = 10 \Omega$
Rise time	t_r	—	120	—	ns	
Turn-off delay time	$t_{d(off)}$	—	110	—	ns	
Fall time	t_f	—	85	—	ns	$V_{DD} = 160 \text{ V}$ $V_{GS} = 10 \text{ V}$ $I_D = 25 \text{ A}$
Total Gate charge	Q_g	—	56	—	nC	
Gate to Source charge	Q_{gs}	—	13	—	nC	
Gate to Drain charge	Q_{gd}	—	26	—	nC	$I_F = 25 \text{ A}$, $V_{GS} = 0$ ^{Note4}
Body-Drain diode forward voltage	V_{DF}	—	0.9	1.5	V	
Body-Drain diode reverse recovery time	t_{rr}	—	140	—	ns	$I_F = 25 \text{ A}$, $V_{GS} = 0$ $diF/dt = 100 \text{ A}/\mu\text{s}$
Body-Drain diode reverse recovery charge	Q_{rr}	—	0.7	—	μC	

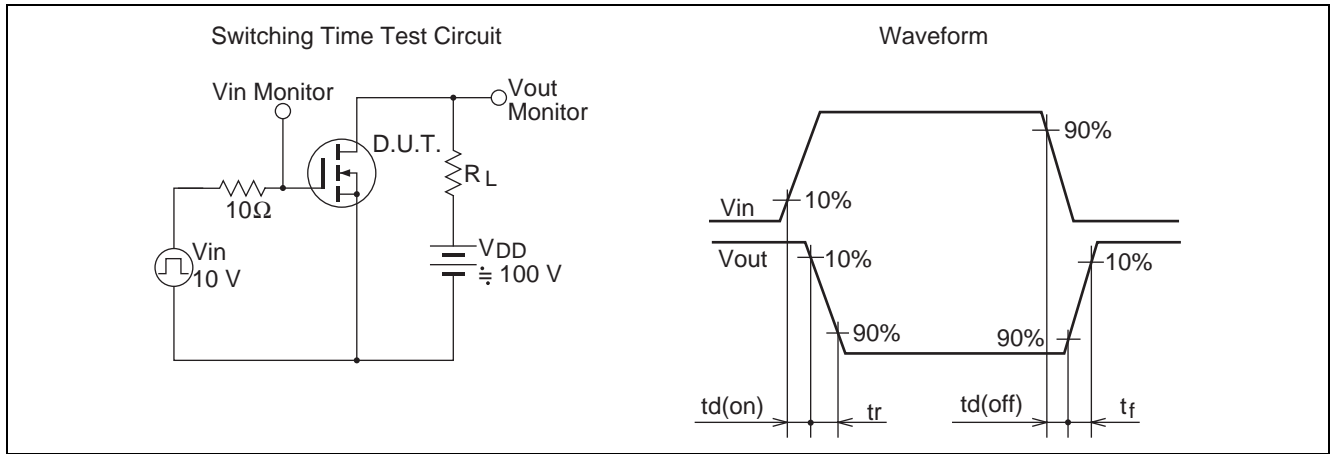
Notes: 4. Pulse test

Main Characteristics





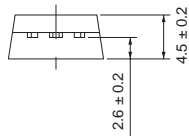
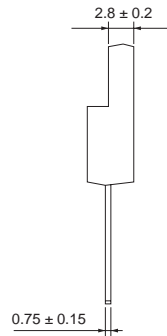
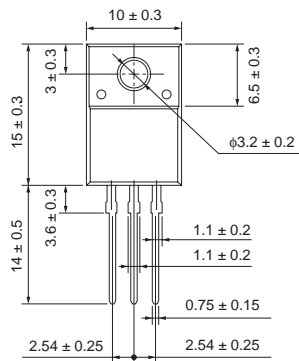




Package Dimensions

TO-220FN

EIAJ Package Code	JEDEC Code	Mass (g) (reference value)	Lead Material
—	—	2.0	Cu alloy



Note 1) The dimensional figures indicate representative values unless otherwise the tolerance is specified.

Symbol	Dimension in Millimeters		
	Min	Typ	Max
A	—	—	—
A ₁	—	—	—
A ₂	—	—	—
b	—	—	—
D	—	—	—
E	—	—	—
e	—	—	—
x	—	—	—
y	—	—	—
y ₁	—	—	—
ZD	—	—	—
ZE	—	—	—

Ordering Information

Part Name	Quantity	Shipping Container
H5N2007FN-E	50 pcs	Plastic magazine

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