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## Dual 4-to-1-line Data Selectors/Multiplexers



ADE-205-490 (Z) 1st. Edition Sep. 2000

## **Description**

Each of these data selectors/multiplexers contains inverters and drivers to supply fully complementary binary decoding data selection to the AND-OR-invert gates. Separate strobe inputs (G) are provided for each of the two four-line sections.

#### **Features**

• High Speed Operation:  $t_{pd}$  (Data to Y) = 16 ns typ ( $C_L = 50 \text{ pF}$ )

• High Output Current: Fanout of 10 LSTTL Loads

• Wide Operating Voltage:  $V_{CC} = 2$  to 6 V

• Low Input Current: 1 μA max

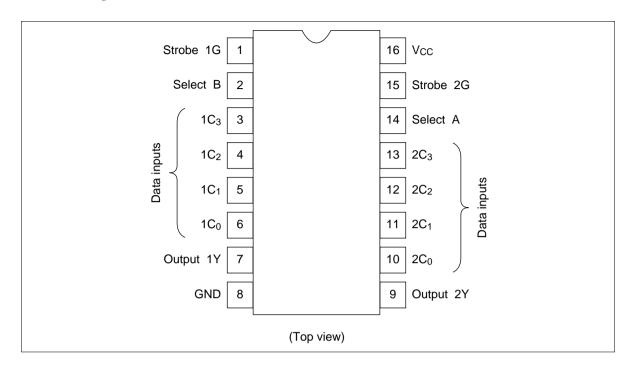
• Low Quiescent Supply Current:  $I_{CC}$  (static) = 4  $\mu$ A max (Ta = 25°C)

#### **Function Table**

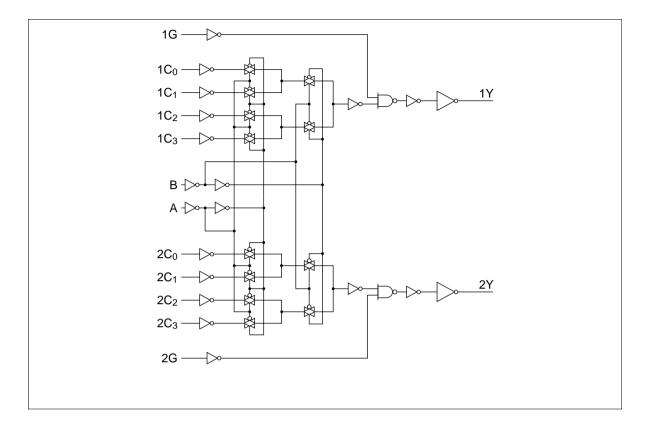
Select In	put	Data Input	ts	Strobe	Output		
В	Α	C <sub>0</sub>	C <sub>1</sub>	C <sub>2</sub>	C <sub>3</sub>	G	Υ
X	Х	Х	Х	Χ	Χ	Н	Н
L	L	L	Х	Х	Х	L	Н
L	L	Н	Х	Х	Х	L	L
L	Н	Х	L	Χ	Χ	L	Н
L	Н	Х	Н	Х	Х	L	L
Н	L	Х	Х	L	Х	L	Н
Н	L	Х	Х	Н	Χ	L	L
Н	Н	Х	Х	Х	L	L	Н
Н	Н	Х	Х	Х	Н	L	L

Select inputs A and B are common to both sections

## **Pin Arrangement**



## Logic Diagram



## **DC** Characteristics

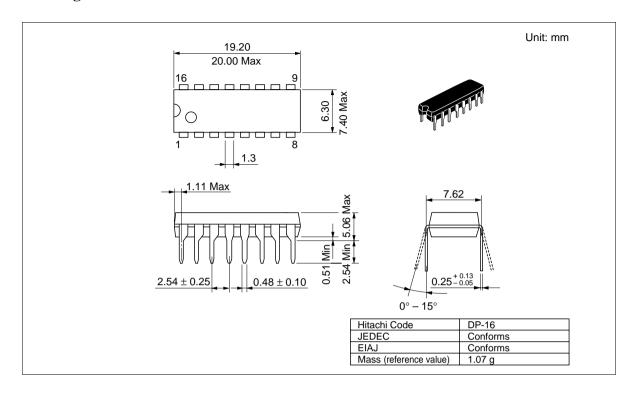
			Ta = 25°C		Ta = −40 to +85°C		_			
Item	Symbol	V <sub>cc</sub> (V)	Min	Тур	Max	Min	Max	Unit	Test Condition	ns
Input voltage	V <sub>IH</sub>	2.0	1.5	_	_	1.5	_	V		
		4.5	3.15	i —		3.15	_	_		
		6.0	4.2	_	_	4.2	_	=		
	V <sub>IL</sub>	2.0	_	_	0.5	_	0.5	V		
		4.5	_	_	1.35	_	1.35	_		
		6.0	_	_	1.8	_	1.8	=		
Output voltage	V <sub>OH</sub>	2.0	1.9	2.0	_	1.9	_	V	Vin = V <sub>IH</sub> or V <sub>IL</sub>	$I_{OH} = -20 \mu A$
		4.5	4.4	4.5	_	4.4	_	=		
		6.0	5.9	6.0	_	5.9	_	=		
		4.5	4.18	s —	_	4.13	_	=		$I_{OH} = -4 \text{ mA}$
		6.0	5.68	3 —	_	5.63	_	=		$I_{OH} = -5.2 \text{ mA}$
	V <sub>OL</sub>	2.0	_	0.0	0.1	_	0.1	V	Vin = V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OL</sub> = 20 μA
		4.5	_	0.0	0.1	_	0.1	=		
		6.0	_	0.0	0.1	_	0.1	=		
		4.5	_	_	0.26	_	0.33	_		I <sub>OL</sub> = 4 mA
		6.0	_	_	0.26	_	0.33	_		I <sub>OL</sub> = 5.2 mA
Input current	lin	6.0	_	_	±0.1	_	±1.0	μΑ	Vin = V <sub>CC</sub> or GND	
Quiescent supply current	I <sub>cc</sub>	6.0	_	_	4.0	_	40	μΑ	Vin = V <sub>cc</sub> or Gf	ND, lout = $0 \mu A$

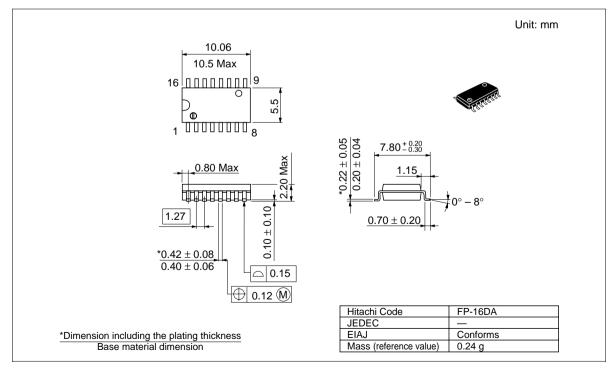
**AC Characteristics** ( $C_L = 50 \text{ pF}$ , Input  $t_r = t_f = 6 \text{ ns}$ )

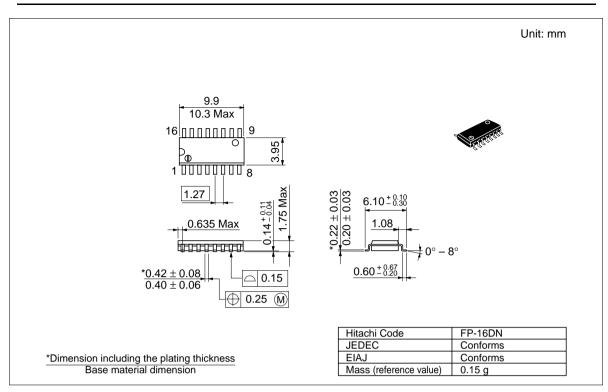
Ta = -40 to  $Ta = 25^{\circ}C$  +85°C

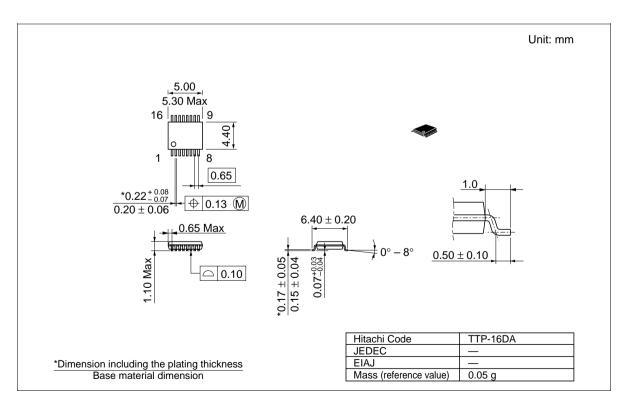
Item	Symbol	$V_{cc}$ (V)	Min	Тур	Max	Min	Max	Unit	<b>Test Conditions</b>
Propagation delay	t <sub>PLH</sub>	2.0	_	_	125	_	155	ns	Data to Y
time	$t_{\tiny PHL}$	4.5	_	16	25	_	31	_	
		6.0	_	_	21	_	26	_	
		2.0	_	_	160	_	200	ns	A or B to Y
		4.5	_	18	32	_	40	=	
		6.0	_	_	27	_	34	=	
		2.0	_	_	100	_	125	ns	G to Y
		4.5	_	10	20	_	25	=	
		6.0	_	_	17	_	21	=	
Output rise/fall	t <sub>TLH</sub>	2.0	_	_	75	_	95	ns	
time	$t_{\text{THL}}$	4.5	_	5	15	_	19	=	
		6.0	_	_	13	_	16	=	
Input capacitance	Cin	_	_	5	10	_	10	pF	

## **Package Dimensions**









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