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Renesas Technology Corp. Customer Support Dept. April 1, 2003



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Silicon P-Channel MOS FET



ADE-208-1192 (Z) 1st. Edition Mar. 2001

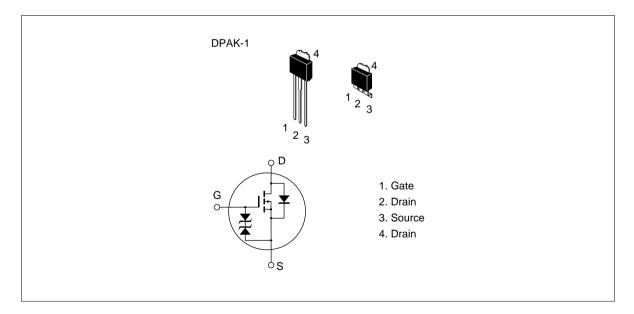
Application

High speed power switching

Features

- Low on-resistance
- High speed switching
- Low drive current
- No secondary breakdown
- Suitable for switching regulator, DC-DC converter

Outline



Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	-200	V
Gate to source voltage	V _{GSS}	±20	V
Drain current	I _D	-3	A
Drain peak current	I*1 D(pulse)	-12	А
Body to drain diode reverse drain current	I _{DR}	-3	А
Channel dissipation	Pch*2	20	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. $PW \le 10 \ \mu s$, duty cycle $\le 1\%$

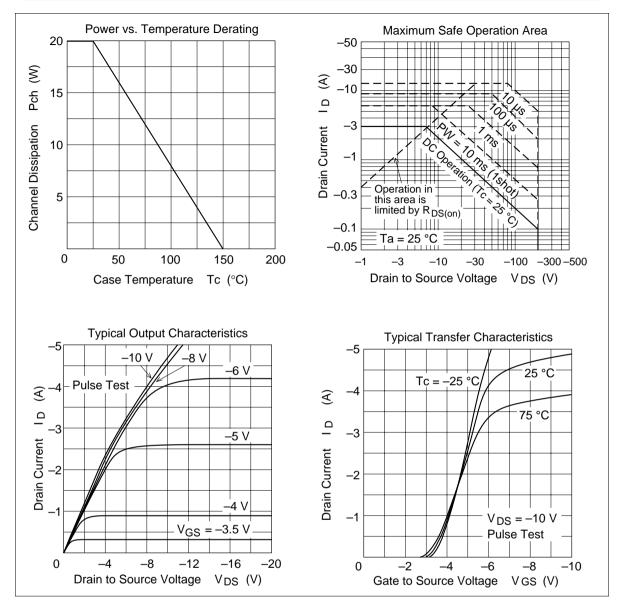
2. Value at $T_c = 25^{\circ}C$

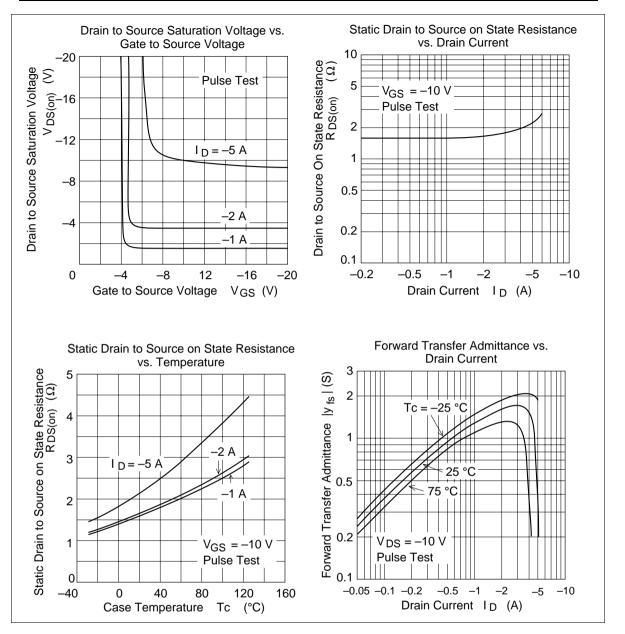
Electrical Characteristics (Ta = 25°C)

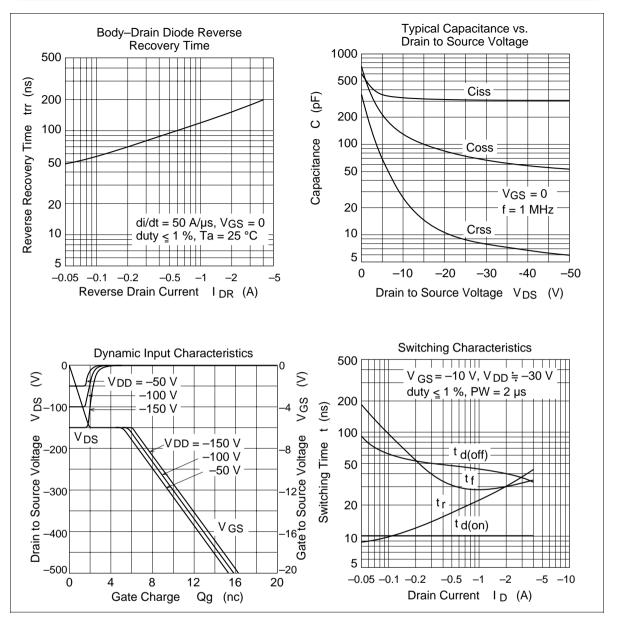
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(\text{BR})\text{DSS}}$	-200	_	_	V	$I_{\rm D} = -10$ mA, $V_{\rm GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20	_	_	V	$I_{G} = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I _{GSS}	—		±10	μA	$V_{GS} = \pm 16 \text{ V}, \text{ V}_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	—		-100	μA	$V_{\rm DS} = -160 \text{ V}, \text{ V}_{\rm GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	-2.0	_	-4.0	V	$I_{\rm D} = -1 \text{ mA}, V_{\rm DS} = -10 \text{ V}$
Static drain to source on state resistance	$R_{\text{DS(on)}}$	_	1.7	2.3	Ω	$I_{D} = -2 \text{ A}, \text{ V}_{GS} = -10 \text{ V}^{*1}$
Forward transfer admittance	y _{fs}	1.0	1.7	_	S	$I_{\rm D} = -2$ A, $V_{\rm DS} = -10$ V ^{*1}
Input capacitance	Ciss	_	330	_	pF	$V_{DS} = -10 V, V_{GS} = 0,$
Output capacitance	Coss	_	130	_	pF	f = 1 MHz
Reverse transfer capacitance	Crss	—	25		pF	
Turn-on delay time	t _{d(on)}	_	10	_	ns	$I_{\rm D} = -2$ A, $V_{\rm GS} = -10$ V,
Rise time	t,	_	30	_	ns	$R_L = 15 \Omega$
Turn-off delay time	$t_{d(off)}$	—	40		ns	
Fall time	t _f	_	30	_	ns	
Body to drain diode forward voltage	V_{DF}	—	-1.15	_	V	$I_{F} = -3 \text{ A}, V_{GS} = 0$
Body to drain diode reverse recovery time	t _{rr}	—	180	—	ns	$I_F = -3 \text{ A}, V_{GS} = 0,$ $di_F/dt = 50 \text{ A}/\mu \text{s}$
Noto: 1 Dulas test						

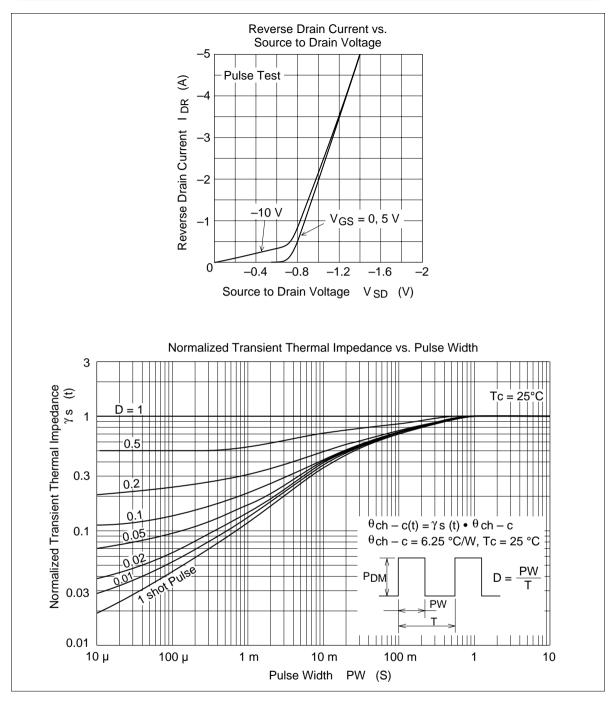
Note: 1. Pulse test

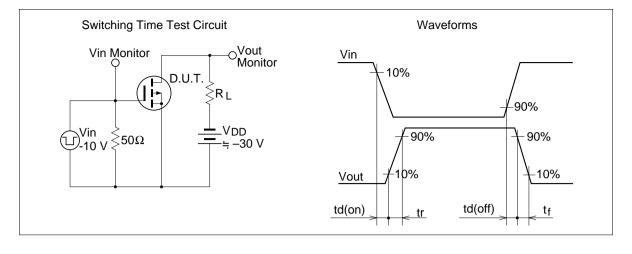




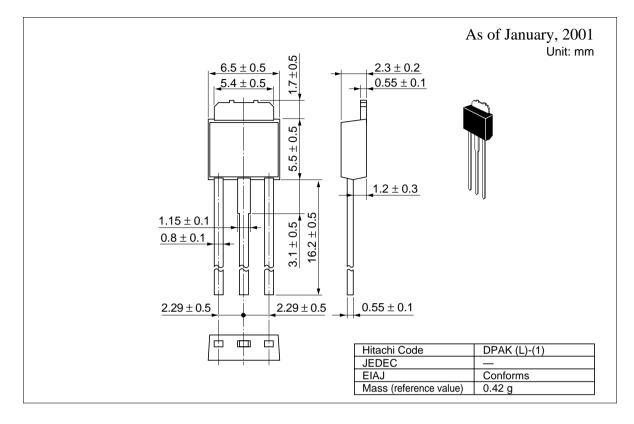


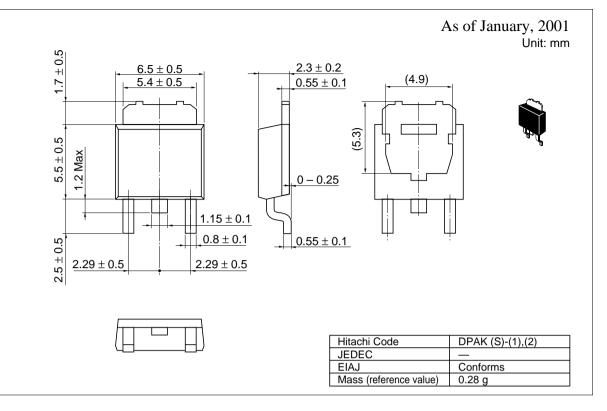


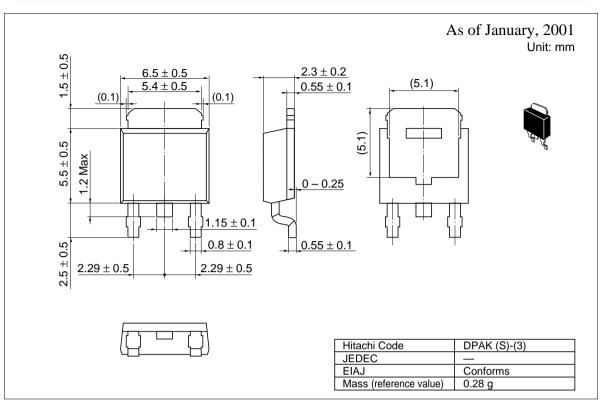




Package Dimensions







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