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Silicon P Channel MOS FET High Speed Power Switching

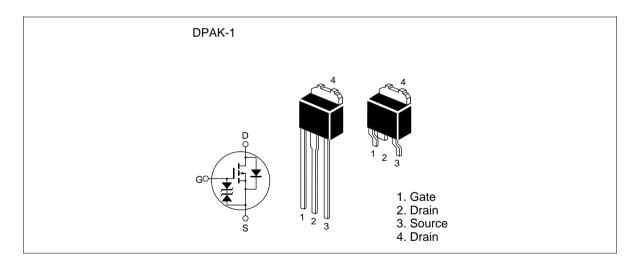


ADE-208-640A (Z) 2nd. Edition Jul. 1998

#### **Features**

- Low on-resistance  $R_{DS(on)} = 0.3 \Omega$  typ.
- Low drive current
- 4 V gete drive devices
- High speed switching

#### **Outline**



## **Absolute Maximum Ratings** (Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	-60	V
Gate to source voltage	$V_{GSS}$	±20	V
Drain current	I <sub>D</sub>	<b>-</b> 5	A
Drain peak current	I Note1	-20	A
Body-drain diode reverse drain current	I <sub>DR</sub>	<b>-</b> 5	A
Avalenche current	I <sub>AP</sub> Note3	<b>–</b> 5	A
Avalenche energy	E <sub>AR</sub> Note3	2.1	mJ
Channel dissipation	Pch <sup>Note2</sup>	20	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Note: 1. PW  $\leq$  10 $\mu$ s, duty cycle  $\leq$  1 %

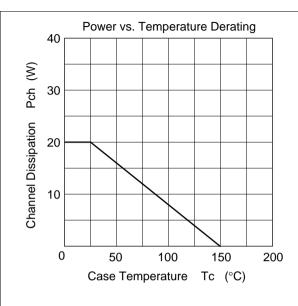
- 2. Value at Tc = 25°C
- 3. Value at Tch = 25°C, Rg  $\geq$  50  $\Omega$

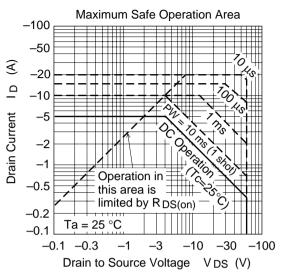
## **Electrical Characteristics** (Ta = 25°C)

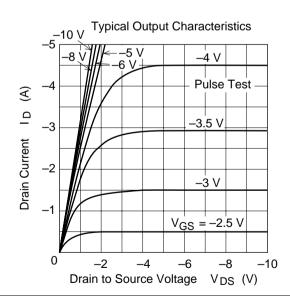
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	-60	_	_	V	$I_{D} = -10 \text{mA}, V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20	_	_	V	$I_{G} = \pm 100 \mu A, V_{DS} = 0$
Zero gate voltege drain current	I <sub>DSS</sub>	_	_	-10	μΑ	$V_{DS} = -60 \text{ V}, V_{GS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_	_	±10	μΑ	$V_{GS} = \pm 16V, V_{DS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	-1.0	_	-2.0	V	$I_{D} = -1 \text{mA}, V_{DS} = -10 \text{V}$
Static drain to source on state	R <sub>DS(on)</sub>	_	0.3	0.4	Ω	$I_{\rm D} = -3A, \ V_{\rm GS} = -10V^{\rm Note4}$
resistance	R <sub>DS(on)</sub>	_	0.5	8.0	Ω	$I_D = -3A$ , $V_{GS} = -4V$ Note4
Forward transfer admittance	y <sub>fs</sub>	1.8	3	_	S	$I_{D} = -3A, V_{DS} = -10V^{Note4}$
Input capacitance	Ciss	_	220	_	pF	V <sub>DS</sub> = -10V
Output capacitance	Coss	_	110	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	35	_	pF	f = 1MHz
Turn-on delay time	t <sub>d(on)</sub>	_	10	_	ns	$V_{GS} = -10V, I_{D} = -3A$
Rise time	t <sub>r</sub>	_	30	_	ns	$R_L = 10\Omega$
Turn-off delay time	t <sub>d(off)</sub>	_	45	_	ns	_
Fall time	t <sub>f</sub>	_	35	_	ns	_
Body-drain diode forward voltage	$V_{DF}$	_	-1.35	_	V	$I_F = -5A, V_{GS} = 0$
Body-drain diode reverse recovery time	t <sub>rr</sub>	_	55	_	ns	$I_F = -5A, V_{GS} = 0$ diF/ dt = 50A/ $\mu$ s

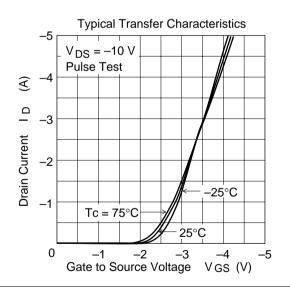
Note: 4. Pulse test

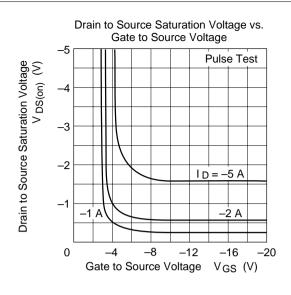
#### **Main Characteristics**

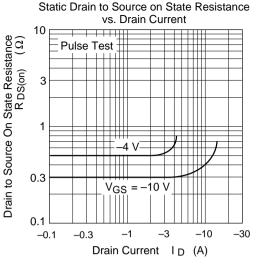


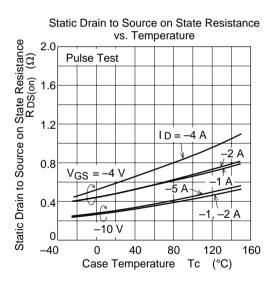


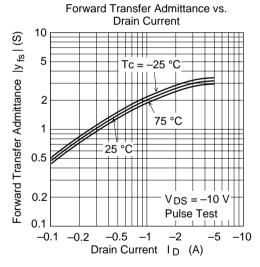


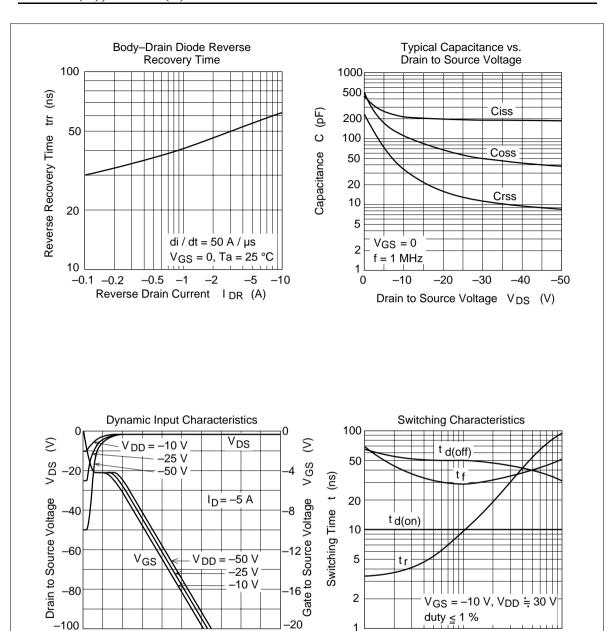












0

4

8

Gate Charge

12

Qg (nc)

16

20

-0.1 -0.2

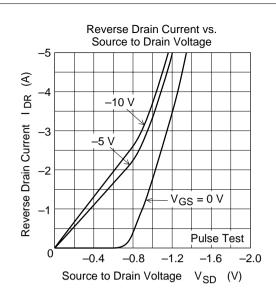
-0.5

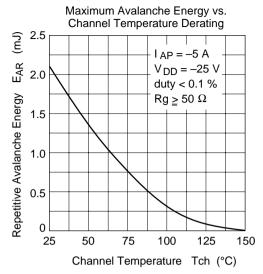
-2

Drain Current ID (A)

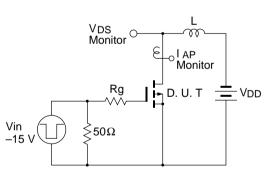
-10

-5



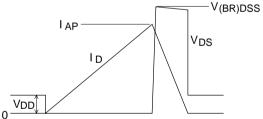


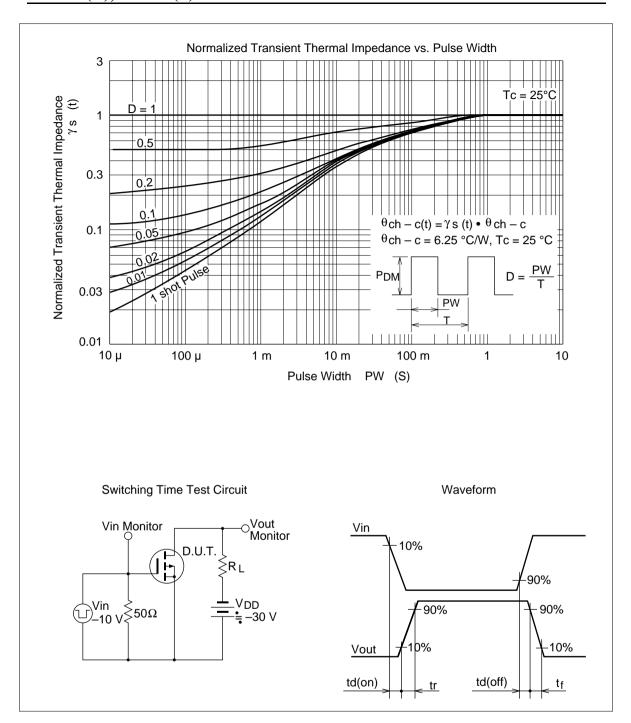




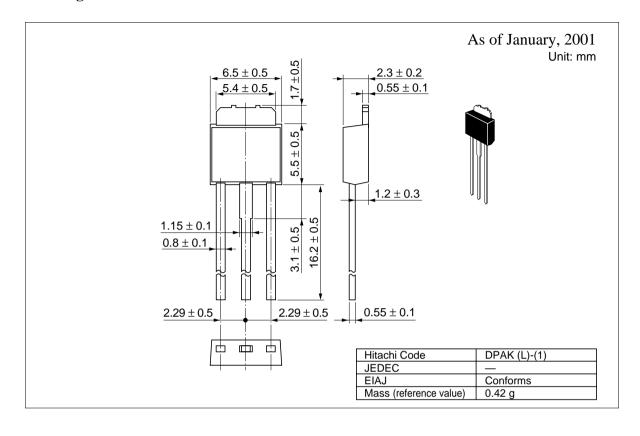
#### Avalanche Waveform

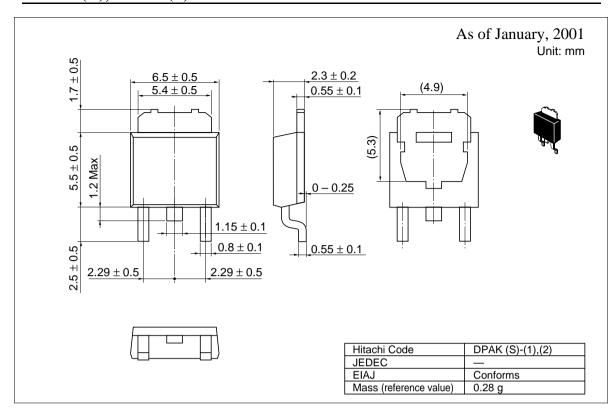
$$E_{AR} = \frac{1}{2} \cdot L \cdot I_{AP}^{2} \cdot \frac{V_{DSS}}{V_{DSS} - V_{DD}}$$

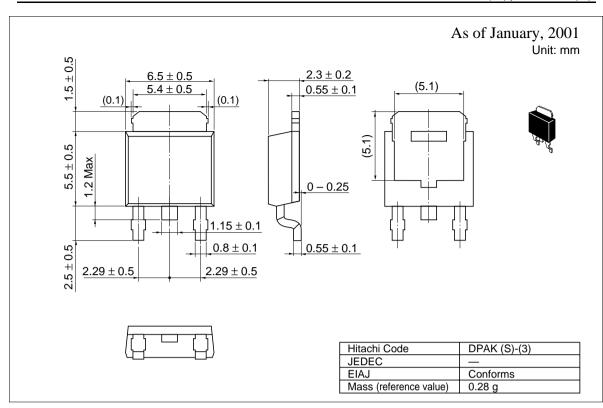




#### **Package Dimensions**







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