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HVD355B

Variable Capacitance Diode for VCO



ADE-208-950 (Z)

Rev. 0 Jun. 2000

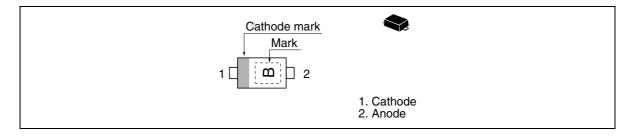
Features

- High capacitance ratio. (n = 2.20 min)
- Low series resistance. (rs = $0.6 \Omega \max$)
- Super small Flat Package (SFP) is suitable for surface mount design.

Ordering Information

Туре No.	Laser Mark	Package Code
HVD355B	В	SFP

Pin Arrangement



HVD355B

Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Value	Unit
Reverse voltage	V _R	15	V
Junction temperature	Tj	125	°C
Storage temperature	Tstg	–55 to +125	°C

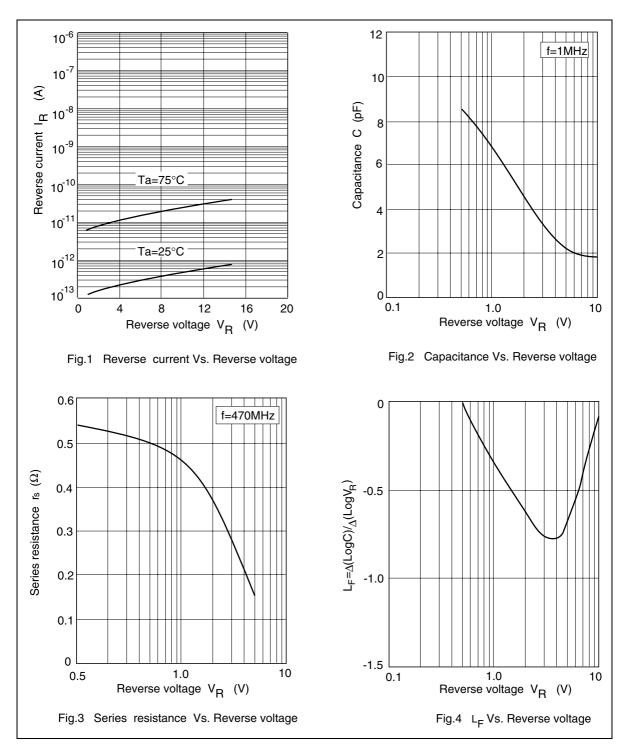
Electrical Characteristics

 $(Ta = 25^{\circ}C)$

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse current	I _{R1}		_	10	nA	$V_{_{R}} = 15V$
	I _{R2}	_	_	100		V _R = 15 V, Ta = 60°C
Capacitance	C ₁	6.40	_	7.20	pF	$V_{_{\rm R}} = 1 \text{ V}, \text{ f} = 1 \text{ MHz}$
	C ₄	2.55	_	2.95		$V_{_{R}} = 4V, f = 1 MHz$
Capacitance ratio	n	2.20	_			C ₁ / C ₄
Series resistance	r _s	_		0.6	Ω	$V_{_{R}} = 1 V, f = 470 MHz$

Note: Please do not use the soldering iron due to avoid high stress to the SFP package.

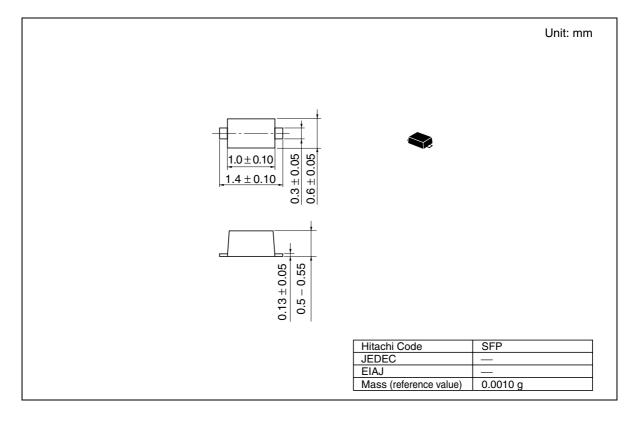
Main Characteristic



RENESAS

HVD355B

Package Dimensions





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Hitachi. Ltd.

Semiconductor & Integrated Circuits Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan Tel: (03) 3270-2111 Fax: (03) 3270-5109

Hitachi Europe Ltd.

URL	NorthAmerica		http://semiconductor.hitachi.com/
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For further information write to:

Hitachi Semiconductor (America) Inc. 179 East Tasman Drive San Jose, CA 95134 Tel: <1> (408) 433-1990 Maidenhead

Electronic Components Group Whitebrook Park Lower Cookham Road Fax: <1>(408) 433-0223 Berkshire SL6 8YA, United Kingdom Tel: <44> (1628) 585000 Fax: <44> (1628) 585200

> Hitachi Europe GmbH Electronic Components Group Dornacher Straße 3 D-85622 Feldkirchen, Munich Germany Tel: <49> (89) 9 9180-0 Fax: <49> (89) 9 29 30 00

Hitachi Asia Ltd. Hitachi Tower 16 Collyer Quay #20-00 Singapore 049318 Tel: <65>-538-6533/538-8577 Fax : <65>-538-6933/538-3877 URL : http://www.hitachi.com.sg

Hitachi Asia Ltd (Taipei Branch Office) 4/F, No. 167, Tun Hwa North Road Hung-Kuo Building Taipei (105), Taiwan Tel: <886>-(2)-2718-3666 Fax : <886>-(2)-2718-8180 Telex : 23222 HAS-TP URL : http://www.hitachi.com.tw

Hitachi Asia (Hong Kong) Ltd. Group III (Electronic Components) 7/F., North Tower World Finance Centre Harbour City, Canton Road Tsim Sha Tsui, Kowloon Hong Kong Tel : <852>-(2)-735-9218 Fax : <852>-(2)-730-0281 URL : http://semiconductor.hitachi.com.hk

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