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Renesas Technology Corp. Customer Support Dept. April 1, 2003



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Silicon Epitaxial Trench Pin Diode for Antenna Switching



ADE-208-948 (Z)

Rev. 0 Jul. 2000

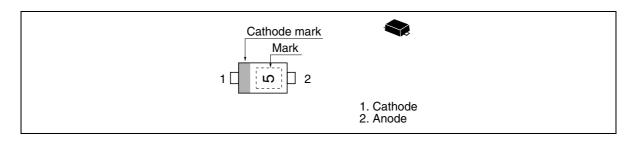
Features

- Adopting the trench structure improves low capacitance. (C = 0.6 pF max)
- Low forward resistance. (rf = $2.0 \Omega \text{ max}$)
- Low operation current.
- Super small Flat Package (SFP) is suitable for surface mount design.

Ordering Information

Type No	Laser Mark	Package Code
HVD135	5	SFP

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Value	Unit	
Peak reverse voltage	$V_{_{RM}}$	65	V	
Reverse voltage	V _R	60	V	
Forward current	I _F	100	mA	
Power dissipation	Pd	150	mW	
Junction temperature	Tj	125	°C	
Storage temperature	Tstg	−55 to +125	°C	

Electrical Characteristics

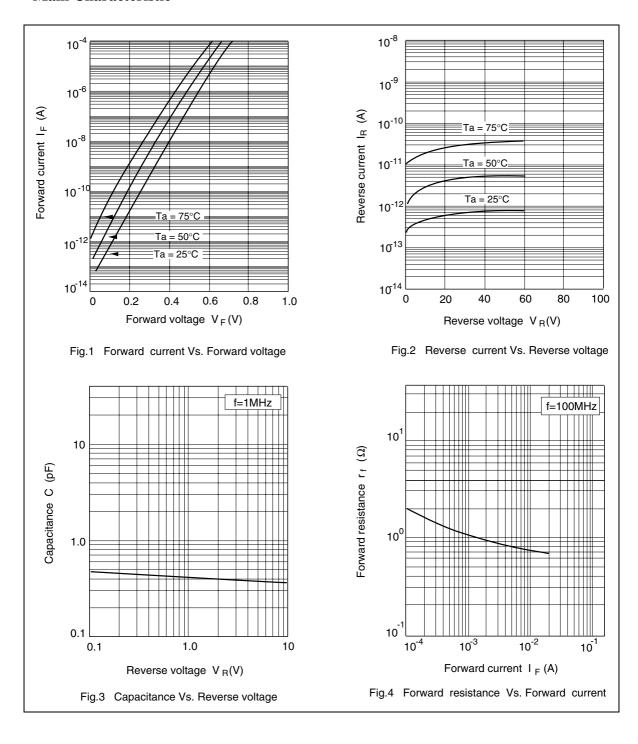
 $(Ta = 25^{\circ}C)$

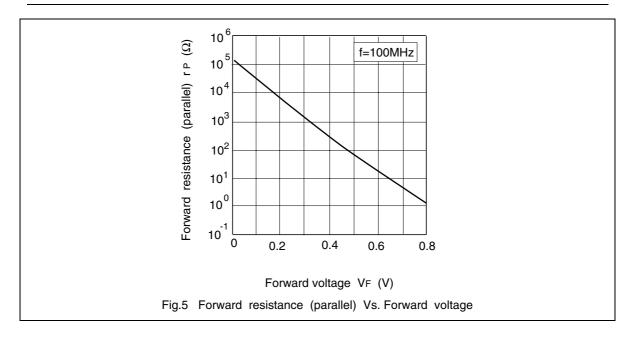
Item	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse current	I _R	_	_	0.1	μΑ	V _R = 60 V
Forward voltage	V _F	_	_	0.9	٧	I _F = 2 mA
Capacitance	С	_	_	0.6	pF	V _R = 1 V, f = 1 MHz
Forward resistance	r _f	_	_	2.0	Ω	I _F = 2 mA, f = 100 MHz
ESD-Capability *1	_	100	_	_	V	$C = 200 \text{ pF}, R = 0 \Omega$, Both forward and reverse direction 1 pulse.

Notes : 1. Failure criterion ; $I_{\scriptscriptstyle R} > 100$ nA at $V_{\scriptscriptstyle R} = 60$ V

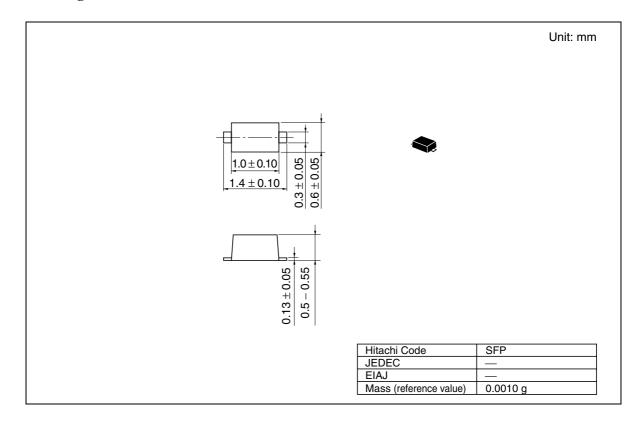
^{2.} Please do not use the soldering iron due to avoid high stress to the SFP package.

Main Characteristic





Package Dimensions



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