

# SAW Components

Data Sheet B3730





#### SAW Components

Passivation layer: Elpas

# B3730 433,92 MHz

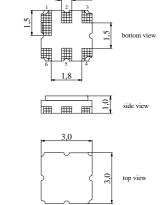
Low-loss Filter

#### **Data Sheet**

Features

(SMT)

# Ceramic package **DCC6E**



#### Terminals

Ni, gold plated

typ. dimensions in mm, approx. weight 0,037 g

### Pin configuration<sup>1)</sup>

1 Input (recommended) or Input Ground

RF low-loss filter for remote control receivers

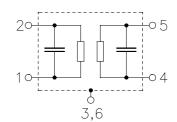
Balanced and unbalanced operation possible

Compliant to EU RoHs Directive (2002/95/EC)
Lead free soldering compatible with J - STD20C

AEC-Q200 qualified component family

■ Package for Surface Mounted Technology

- 2 Input Ground (recommended) or Input
- 4 Output (recommended) or Output Ground
- 5 Output Ground (recommended) or Output
- 3,6 to be grounded



Туре	Ordering code	Marking and package according to	Packing according to		
B3730	B39431-B3730-H110	C61157-A7-A143	F61074-V8168-Z000		

Electrostactic Sensitive Device (ESD)

### **Maximum ratings**

Operable temperature range	T <sub>A</sub>	-45/+120	°C	
Storage temperature range	$T_{\rm stg}$	-45/+120	°C	
DC voltage	V <sub>DC</sub>	6	V	
Source power	$P_S$	10	dBm	source impedance 50 $\Omega$

<sup>1)</sup> The recommended pin configuration usually offers best suppression of electrical crosstalk. The filter characteristics refer to this configuration.



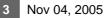
Nov 04, 2005



SAW Components					B3730
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Characteristics					
Reference temperature: T <sub>A</sub>	= -45 .	+95°C			
			ning network		
Terminating load impedance: Z <sub>L</sub>	= 50 Ω	and match	ning network	κ.	
		min.	typ.	max.	
Center frequency	f <sub>C</sub>	_	433,92	—	MHz
(center frequency between 3 dB points)					
Minimum insertion attenuation	$\alpha_{min}$				
ncluding loss in matching elements ( $Q_{L} = 47$ )		_	2,4	3.1	dB
excluding loss in matching elements		_	1,7	2,4	dB
-					
Pass band (relative to $\alpha_{\min}$ )					
433,78 434,06 MHz		-	0,6	2,0	dB
433,74 434,10 MHz	<u>z</u>		0,8	3,0	dB
433,70 434,14 MHz	2		1,2	6,0	dB
Filter bandwidth					
α <sub>rel</sub> ≤ 3 dE	3	0,65	0,72	0,79	MHz
Relative attenuation (relative to $\alpha_{min}$ )	$\alpha_{rel}$				
10,00 414,00 MHz	2	53	58	_	dB
414,00 423,50 MHz	<u>z</u>	48	53	—	dB
423,50 431,52 MHz	<u>z</u>	35	40	—	dB
431,52 432,90 MHz	<u>z</u>	20	24	—	dB
432,90 433,10 MHz	2	17	24		dB
434,92 444,00 MHz	<u>z</u>	15	19	—	dB
444,00 500,00 MHz	2	45	50		dB
500,00 810,00 MHz	2	50	54	_	dB
810,001500,00 MHz	2	60	65	_	dB
1500,002500,00 MHz	2	55	60	_	dB
mpedance for pass band matching 1)					
Input: $Z_{\rm IN} = R_{\rm IN}    C_{\rm IN}$		_	250    2,9		Ω    pl
Output: $Z_{OUT} = R_{OUT}    C_{OUT}$			250    2,9		Ω    pl

<sup>1)</sup> Impedance for passband matching bases on an ideal, perfect matching of the SAW filter to source- and to load impedance (here 50 Ohm). After the SAW filter is removed and input impedance into the input matching / output matching network is calculated.

The conjugate complex value of these characteristic impedances are the input and output impedances for flat passband. For more details, we refer to EPCOS application note #4.

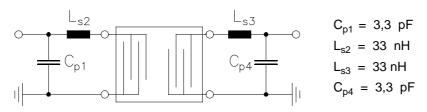




### Low-loss Filter

Data Sheet

Matching network to 50  $\Omega$  (element values depend on pcb layout and equivalent circuit)



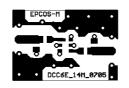
Minimising the crosstalk

For a good ultimate rejection a low crosstalk is necessary. Low crosstalk can be realised with a good RF layout. The major crosstalk mechanism is caused by the "ground-loop" problem.

Grounding loops are created if input-and output transducer GND are connected on the top-side of the PCB and fed to the system grounding plane by a common via hole. To avoid the common ground path, the ground pin of the input- and output transducer are fed to the system ground plane (bottom PCB plane) by their own via hole. The transducers' grounding pins should be isolated from the upper grounding plane.

A common GND inductivity of 0.5nH degrades the ultimate rejection (crosstalk) by 20dB.

The optimised PCB layout, including matching network for transformation to 50 Ohm, is shown here. In this PCB layout the grounding loops are minimised to realise good ultimate rejection.



Optimised PCB layout for SAW filters in DCC6E package, pinning 1,4 (top side, scale 1:1)

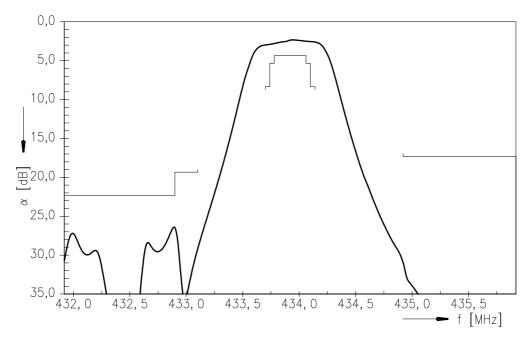
The bottom side is a copper plane (system ground area). The input and output grounding pins are isolated and connected to the common ground by separated via holes.

For good contact of the upper grounding area with the lower side it is necessary to place enough via holes.

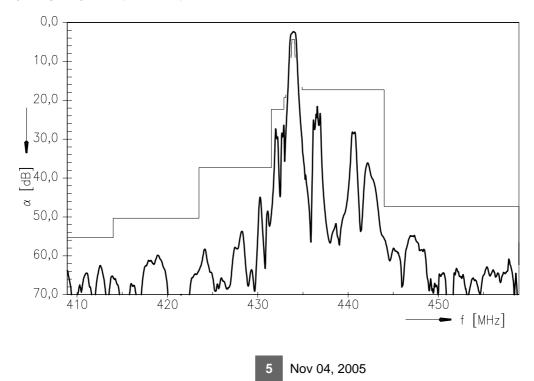


**Data Sheet** 

# **Frequency response**



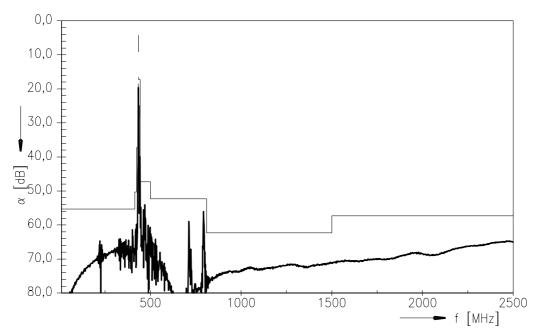
# Frequency response (wideband)





#### Data Sheet

#### Frequency response (ultimate rejection)



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