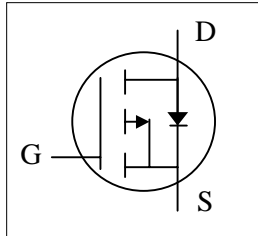




- ▼ Simple Drive Requirement
- ▼ 2.5V Gate Drive Capability
- ▼ Fast Switching Characteristic

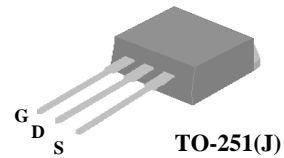
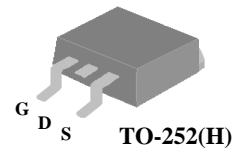


$BV_{DSS}$	-20V
$R_{DS(ON)}$	150m $\Omega$
$I_D$	-10A

## Description

Advanced Power MOSFETs from APEC provide the designer with the best combination of fast switching, low on-resistance and cost-effectiveness.

This device is suited for low voltage and lower power applications.



## Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	- 20	V
$V_{GS}$	Gate-Source Voltage	$\pm 12$	V
$I_D @ T_C=25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	-10	A
$I_D @ T_C=100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	-6.2	A
$I_{DM}$	Pulsed Drain Current <sup>1</sup>	-24	A
$P_D @ T_C=25^\circ C$	Total Power Dissipation	15.6	W
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ C$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ C$

## Thermal Data

Symbol	Parameter	Value	Units
Rthj-c	Maximum Thermal Resistance, Junction-case	8.0	$^\circ C/W$
Rthj-a	Maximum Thermal Resistance, Junction-ambient	110	$^\circ C/W$



# AP3310GH/J

## Electrical Characteristics @T<sub>j</sub>=25°C(unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA	-20	-	-	V
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-2.8A	-	-	150	mΩ
		V <sub>GS</sub> =-2.5V, I <sub>D</sub> =-2.0A	-	-	250	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250uA	-0.5	-	-	V
g <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> =-5V, I <sub>D</sub> =-2.8A	-	2.8	-	S
I <sub>DSS</sub>	Drain-Source Leakage Current (T <sub>j</sub> =25°C)	V <sub>DS</sub> =-20V, V <sub>GS</sub> =0V	-	-	-1	uA
	Drain-Source Leakage Current (T <sub>j</sub> =150°C)	V <sub>DS</sub> =-16V, V <sub>GS</sub> =0V	-	-	-25	uA
I <sub>GSS</sub>	Gate-Source Leakage	V <sub>GS</sub> =±12V	-	-	±100	nA
Q <sub>g</sub>	Total Gate Charge <sup>2</sup>	I <sub>D</sub> =-2.8A	-	4.2	-	nC
Q <sub>gs</sub>	Gate-Source Charge	V <sub>DS</sub> =-6V	-	1.2	-	nC
Q <sub>gd</sub>	Gate-Drain ("Miller") Charge	V <sub>GS</sub> =-5V	-	0.4	-	nC
t <sub>d(on)</sub>	Turn-on Delay Time <sup>2</sup>	V <sub>DS</sub> =-6V	-	7	-	ns
t <sub>r</sub>	Rise Time	I <sub>D</sub> =-1A	-	8	-	ns
t <sub>d(off)</sub>	Turn-off Delay Time	R <sub>G</sub> =6Ω, V <sub>GS</sub> =-5V	-	13	-	ns
t <sub>f</sub>	Fall Time	R <sub>D</sub> =6Ω	-	5	-	ns
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> =0V	-	320	-	pF
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> =-6V	-	75	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance	f=1.0MHz	-	55	-	pF

## Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V <sub>SD</sub>	Forward On Voltage <sup>2</sup>	T <sub>j</sub> =25°C, I <sub>S</sub> =-10A, V <sub>GS</sub> =0V	-	-	-1.2	V
trr	Reverse Recovery Time <sup>2</sup>	I <sub>S</sub> =-2.8A, V <sub>GS</sub> =0V,	-	17	-	ns
Q <sub>rr</sub>	Reverse Recovery Charge	di/dt=100A/μs	-	9	-	nC

### Notes:

- 1.Pulse width limited by Maximum junction temperature.
- 2.Pulse test

THIS PRODUCT IS AN ELECTROSTATIC SENSITIVE, PLEASE HANDLE WITH CAUTION.

THIS PRODUCT HAS BEEN QUALIFIED FOR CONSUMER MARKET. APPLICATIONS OR USES AS CRITERIAL COMPONENT IN LIFE SUPPORT DEVICE OR SYSTEM ARE NOT AUTHORIZED.

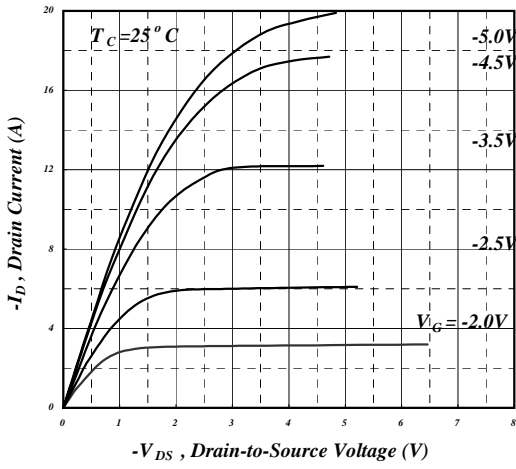


Fig 1. Typical Output Characteristics

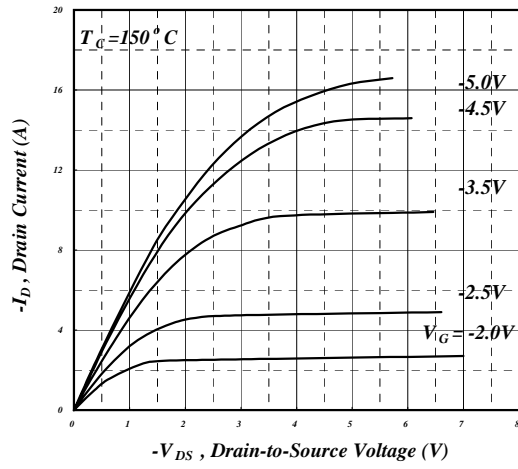


Fig 2. Typical Output Characteristics

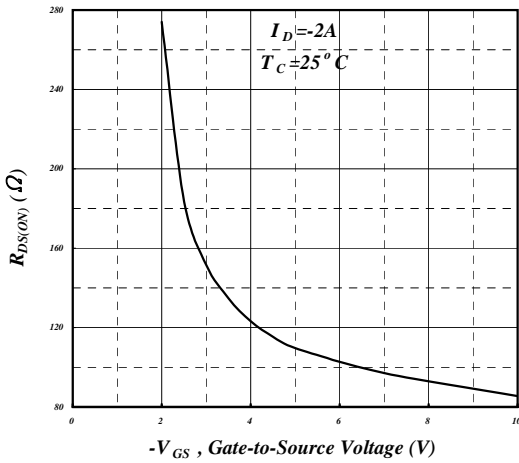


Fig 3. On-Resistance v.s. Gate Voltage

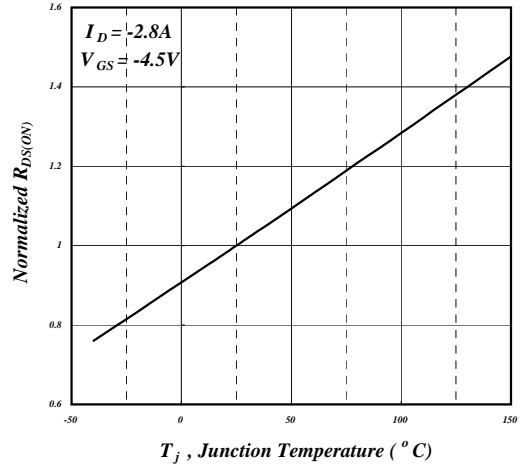


Fig 4. Normalized On-Resistance v.s. Junction Temperature

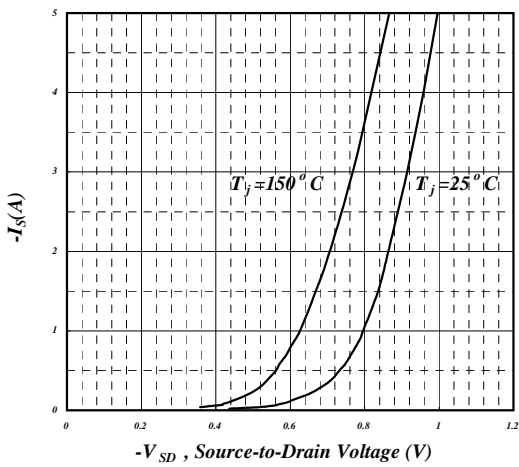


Fig 5. Forward Characteristic of Reverse Diode

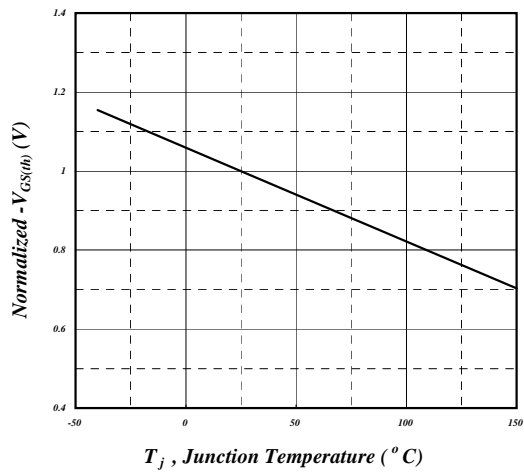


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

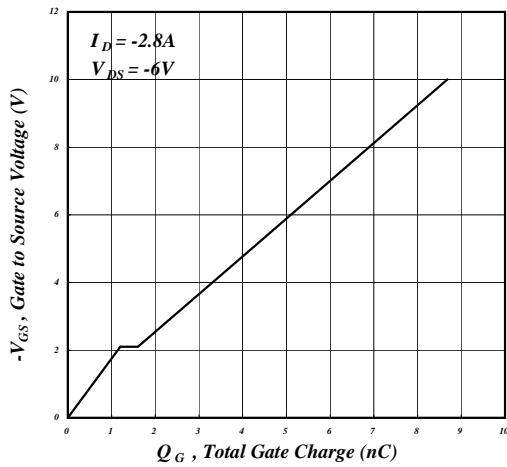


Fig 7. Gate Charge Characteristics

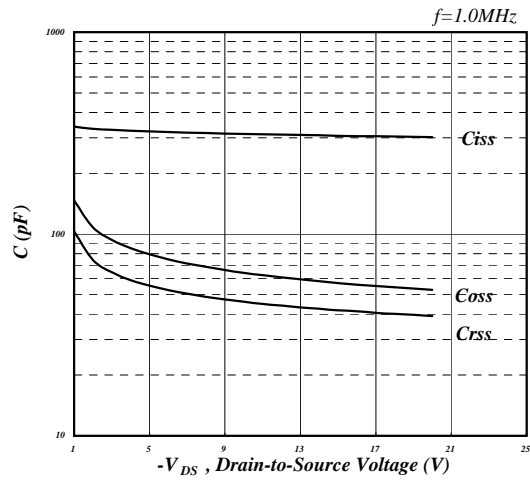


Fig 8. Typical Capacitance Characteristics

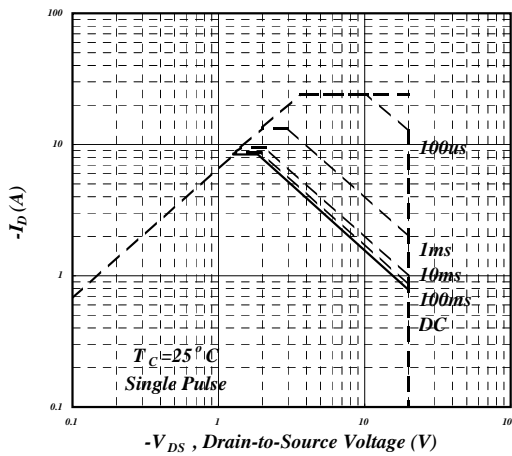


Fig 9. Maximum Safe Operating Area

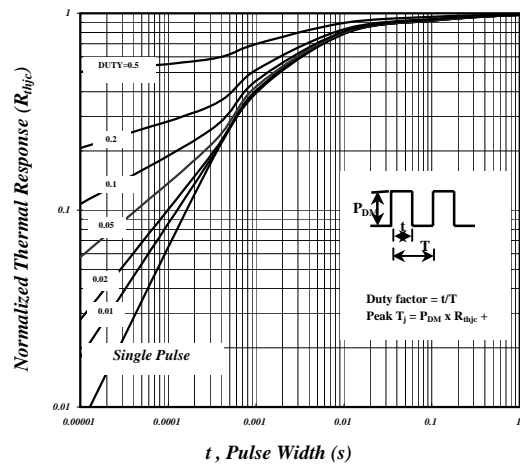


Fig 10. Effective Transient Thermal Impedance

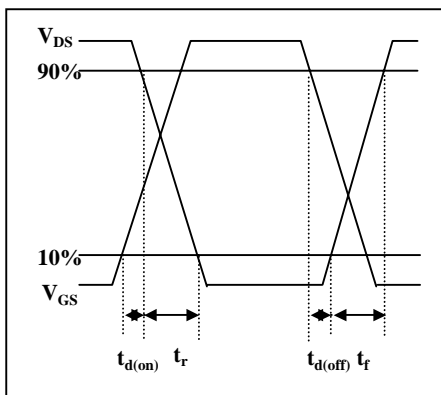


Fig 11. Switching Time Waveform

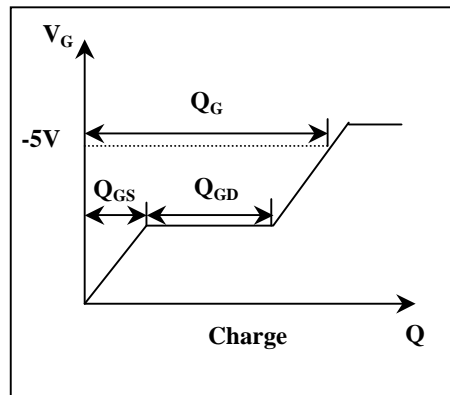
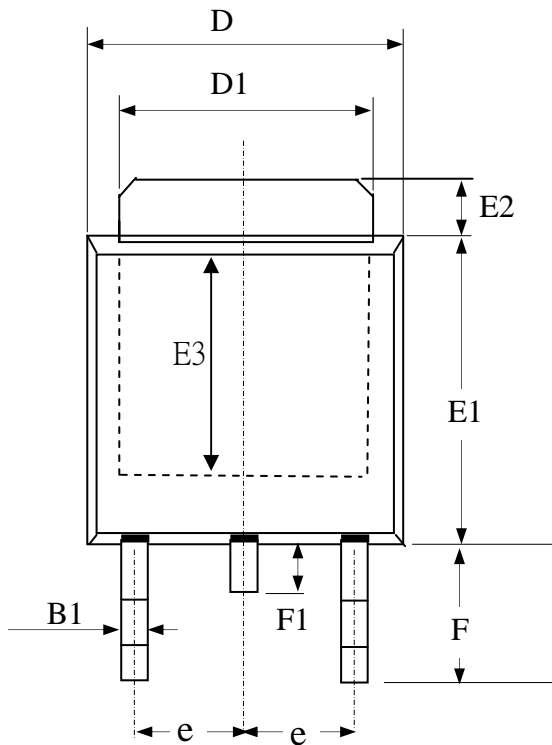


Fig 12. Gate Charge Waveform

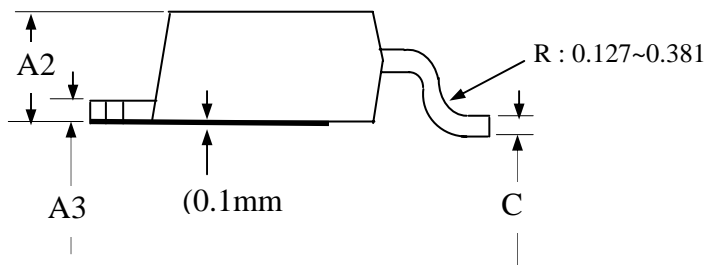


## Package Outline : TO-252

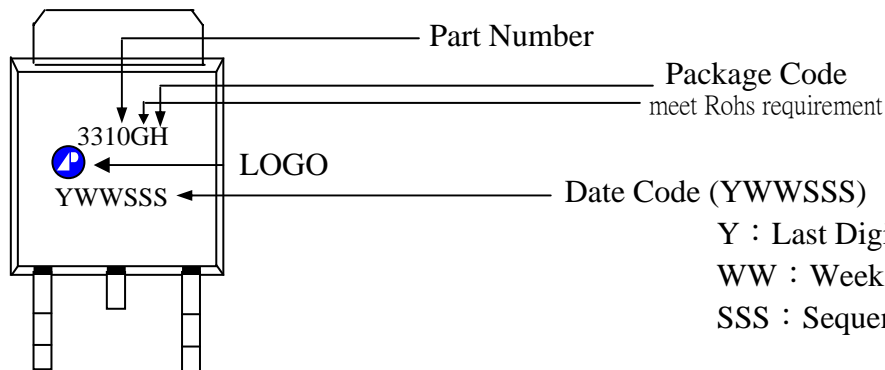


SYMBOLS	Millimeters		
	MIN	NOM	MAX
A2	1.80	2.30	2.80
A3	0.40	0.50	0.60
B1	0.40	0.70	1.00
D	6.00	6.50	7.00
D1	4.80	5.35	5.90
E3	3.50	4.00	4.50
F	2.20	2.63	3.05
F1	0.5	0.85	1.20
E1	5.10	5.70	6.30
E2	0.50	1.10	1.80
e	--	2.30	--
C	0.35	0.50	0.65

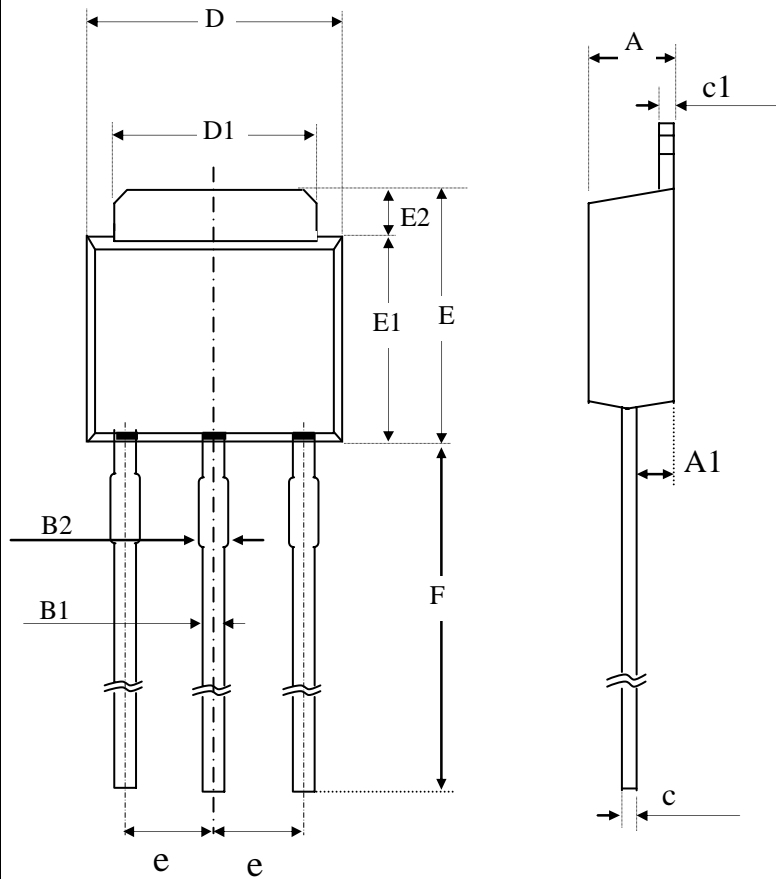
- 1.All Dimensions Are in Millimeters.
- 2.Dimension Does Not Include Mold Protrusions.



## Part Marking Information & Packing : TO-252



Date Code (YWWSSS)  
 Y : Last Digit Of The Year  
 WW : Week  
 SSS : Sequence



SYMBOLS	Millimeters		
	MIN	NOM	MAX
	Original	Original	Original
A	2.10	2.30	2.50
A1	0.60	1.20	1.80
B1	0.40	0.60	0.80
B2	0.60	0.95	1.25
c	0.40	0.50	0.65
c1	0.40	0.55	0.70
D	6.00	6.50	7.00
D1	4.80	5.40	5.90
E1	5.00	5.50	6.00
E2	1.20	1.70	2.20
e	----	2.30	----
F	7.00	---	16.70

- 1.All Dimensions Are in Millimeters.
- 2.Dimension Does Not Include Mold Protrusions.

### Part Marking Information & Packing : TO-251

