

# M709L M710L

# PCM REMOTE CONTROL TRANSMITTERS (LOW VOLTAGE)

- 2.2V TO 5V OPERATING SUPPLY VOLTAGE RANGE
- M709L: 40 COMMANDS x 16 ADDRESSES
- M710L: 64 COMMANDS x 16 ADDRESSES
- ADDRESS ORGANIZATION PROVIDES WIDE RANGE OF SIMULTANEOUS APPLI-CATIONS WITHOUT INTERFERENCE BE-TWEEN SYSTEMS
- IMPROVED PCM TRANSMISSION CODE PROVIDES EASY RECOGNITION OF FALSE SIGNALS
- "FLASH" OR "CARRIER" PIN SELECTABLE TRANSMISSION MODES
- END OF TRANSMISSION CODE
- SINGLE CONTACT MATRIX KEYBOARD
- INTEGRATED ANTIBOUNCE AND INTERLOCK
- WIDE REFERENCE FREQUENCY RANGE (445 to 510 kHz ceramic resonator)
- VERY LOW POWER CONSUMPTION DUR-ING TRANSMISSION. OUTPUT DUTY CY-CLE 0.15% (flash mode), 0.7% (carrier mode)
- FULLY COMPATIBLE WITH M491 AND M494 (In flash mode)

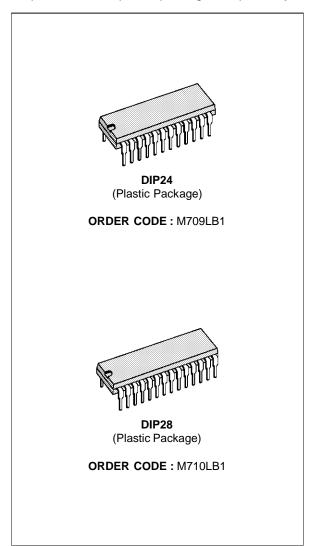
### DESCRIPTION

These ICs have been developed for remote control in consumer applications (TV, radio, video recorders) or in the industrial field and use a highly reliable transmission code which has a capacity of 1024 channels. Each transmitted word is structured into 4 bits which constitute the address and 6 bits which constitute the command (64 commands available). One command (1 st = 000000)is used to transmit the "end of transmission code' when the key is released. Additional bits are transmitted for synchronization of transmitter and receiver clocks and for security checks. The address organization provides a wide range of simultaneous applications without interference between systems. The receiver accepts the decoded command only if the transmitted address matches the address selected at the receiver. 16 addresses are available for this purpose.

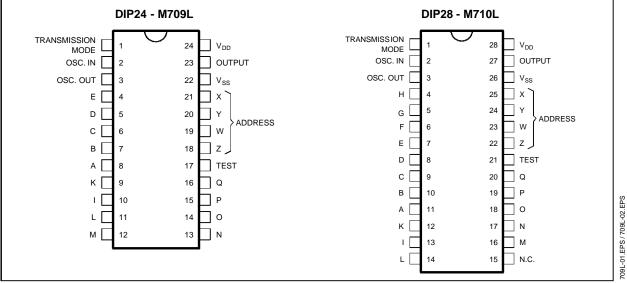
The reference oscillator is controlled by a cheap ceramic or LC resonator. Two types of transmission mode are available : "Flash" or "Carrier" mode.

The M709L is a simplified version of the M710L which can only transmit 40 commands with 16 possible addresses. The M710L on the other hand has the full system capacity : it can transmit 64 commands with 16 addresses.

The M709L and M710L are produced with CMOS Si-gate technology and are available in 24 and 28-pin dual in-line plastic packages respectively.

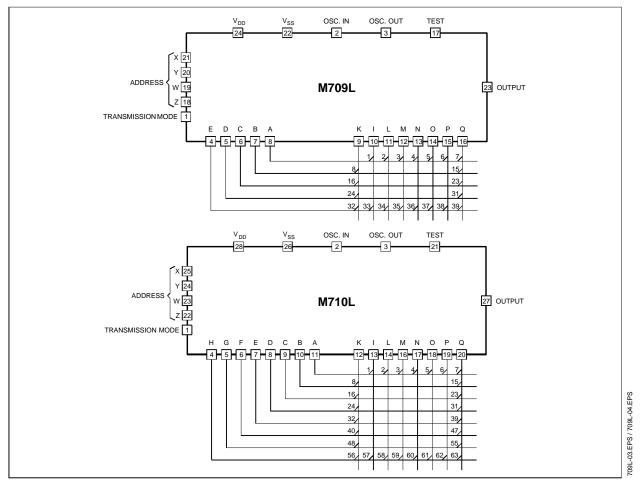


#### **PIN CONNECTIONS**



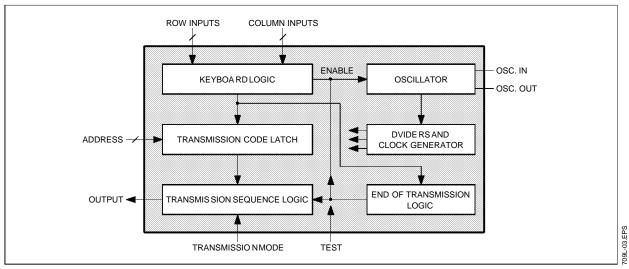
Note : The test pin must be connected to Vss.

### **FUNCTIONAL DESCRIPTION**





### **BLOCK DIAGRAM**



### **ABSOLUTE MAXIMUM RATINGS**

Symbol	Parameter	Value	Unit
V <sub>DD</sub>	Supply Voltage	-0.3 to 5.5	V
Vı	Input Voltage	-0.3 to V <sub>DD</sub> + 0.3	V
lo	IR Output Current (t< 50μs)	10	mA
Ptot	Total Package Power Dissipation	200	mW
T <sub>oper</sub>	Operating Temperature	0, +70	°C
T <sub>stg</sub>	Storage Temperature	-55, +125	°C

Stresses above those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

# **RECOMMENDED OPERATING CONDITIONS**

Symbol	Parameter	Value	Unit
$V_{DD}$	Supply Voltage	2.2 to 5	V
Vı	Input Voltage	0 to V <sub>DD</sub>	V
lo	IR Output Current (t< 50μs)	max. 2.5	mA
fref	Reference Frequency	445 to 510	kHz
T <sub>oper</sub>	Operating Temperature	0 to 70	°C
rs	Serial Resistance of a Closed Key Contact	max. 2.5	kΩ
r <sub>p</sub>	Parallel Resistance of Open Key Contact	min. 2.2	MΩ
Rs	Serial Resistance of the Ceramic Resonator	max. 20	Ω



# **STATIC ELECTRICAL CHARACTERISTICS** (over recommended operating conditions) Typical values are at 9V and $T_{AMB} = 25^{\circ}C$

Symbol	Parameter	Pins	Test Conditions	Min.	Тур.	Max.	Unit
I <sub>DD</sub>	Supply Current		V <sub>DD</sub> = 5V, IR Output Open : Stand-by Operating (one key closed)		3 4	10 7	μA mA
Юн	High State IR Output Current		$V_{DD} = 3V, V_{OH} = 2V$ $V_{DD} = 2.2V, V_{OH} = 1V$	-1 -0.3	-2 -0.5		mA mA
I <sub>OL</sub>	Low State IR Output Current		$V_{DD} = 3V, V_{OL} = 1V$ $V_{DD} = 2.2V, V_{OL} = 1V$	1 0.3	2 0.5		mA mA
Ін	Input High Current	Address Selection Inputs	V <sub>DD</sub> = 3V, V <sub>IL</sub> = 3V (oscillator running)			150	μΑ
١L	Input Leakage Current	Trans. Mode Test Pin	$V_{DD} = 3V, V_{IN} = 0 \text{ to } 3V$			1	μA

# **TRUTH TABLE**

Command		Input Code												Со	mma	nd E	Bits					
No	Α	В	С	D	Е	F	G	H	Κ		L	Μ	Ν	0	Ρ	Q	C1	C2	C3	C4	C5	C6
0	END	O OF	TRA	NSN	IISSI	ON											0	0	0	0	0	0
1	X									Х							1	0	0	0	0	0
2	X										Х						0	1	0	0	0	0
2 3 4 5 6 7	X											Х					1	1	0	0	0	0
4	X												Х				0	0	1	0	0	0
5	X X													Х			1	0	1	0	0	0
6	X														Х		0	1	1	0	0	0
	X															Х	1	1	1	0	0	0
8 9		Х							Х								0	0	0	1	0	0
9		Х								Х							1	0	0	1	0	0
10		Х									Х						0	1	0	1	0	0
11		Х										Х					1	1	0	1	0	0
12		Х											Х				0	0	1	1	0	0
13		Х												Х			1	0	1	1	0	0
14		Х													Х		0	1	1	1	0	0
15		Х														Х	1	1	1	1	0	0
16			Х						Х								0	0	0	1	0	0
17			Х							Х							1	0	0	1	0	0
18			Х								Х						0	1	0	1	0	0
19			Х									Х					1	1	0	1	0	0
20			X X X X X X X										Х				0	0	1	1	0	0
21 22			Х											Х			1	0	1	1	0	0
22			Х												Х		0	1	1	1	0	0
23			Х													Х	1	1	1	1	0	0
24				Х					Х								0	0	0	1	0	0
25				Х						Х							1	0	0	1	0	0
26				Х							Х						0	1	0	1	0	0
27				Х								Х					1	1	0	1	0	0
28				Х									Х				0	0	1	1	0	0
29				X X										Х			1	0	1	1	0	0
30															Х		0	1	1	1	0	0
31				Х												Х	1	1	1	1	0	0



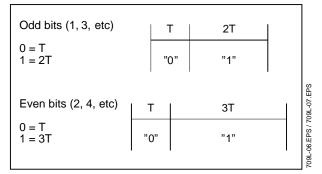
Command								nput	Cod	е							Command Bits					
No	Α	В	С	D	Ε	F	G	Н	Κ		L	Μ	Ν	0	Ρ	Q	C1	C2	C3	C4	C5	C6
32					X X				Х								0	0	0	1	0	0
33					Х					Х							1	0	0	1	0	0
34					Х						Х						0	1	0	1	0	0
35					Х							Х					1	1	0	1	0	0
34 35 36					X X X								Х				0	0	1	1	0	0
37					Х									Х			1	0	1	1	0	0
38					Х										Х		0	1	1	1	0	0
39					X X											Х	1	1	1	1	0	0
40						Х			Х								0	0	0	1	0	1
41						Х				Х							1	0	0	1	0	1
42						Х					Х						0	1	0	1	0	1
43						X X						Х					1	1	0	1	0	1
44						Х							Х				0	0	1	1	0	1
45						X X X								Х			1	0	1	1	0	1
46						Х									Х		0	1	1	1	0	1
47						Х										Х	1	1	1	1	0	1
48							Х		Х								0	0	0	0	1	1
49							Х			Х							1	0	0	0	1	1
50							Х				Х						0	1	0	0	1	1
51							Х					Х					1	1	0	0	1	1
52							Х						Х				0	0	1	0	1	1
51 52 53							Х							Х			1	0	1	0	1	1
54							Х								Х		0	1	1	0	1	1
55							Х									Х	1	1	1	0	1	1
56								Х	Х								0	0	0	1	1	1
57								Х		Х							1	0	0	1	1	1
58								Х			Х						0	1	0	1	1	1
59								Х				Х					1	1	0	1	1	1
60								Х					Х				0	0	1	1	1	1
61								Х						Х			1	0	1	1	1	1
62								Х							Х		Ó	1	1	1	1	1
63								Х							-	Х		1	1	1	1	1

### **TRUTH TABLE** (continued)

### DESCRIPTION

The signals are transmitted with infrared light using pulse code modulation. Each word consists of 12 bits. The binary information of a bit is determined by the time interval between two pulses.

If "T" is the time base, the bits are coded as follows :



The different code introduced for the even and odd "1s" improves the capability to recognize false codes at the receiver end. For example the double error which can cause the exchange "10" with "01" is easily detected.

A parity bit is also added in order to further increase the reliability of the transmission. This bits is "1" if the number of transmitted "1s" is even while it is "0" if the number of transmitted "1s" is odd.

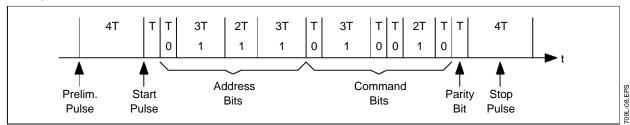
In addition, every word contains a preliminary pulse, a start pulse and a stop pulse. The spacing between the preliminary and the start pulse is 4T. This is followed after 1T by 11 data pulses (one parity bit), and terminated after 4T interval by a stop pulse.

Consequently, a word in which the binary digit 0 occurs ten times has a total duration of 21T.

A word containing ten "1s" has a duration of 36T.



Example:



# SYNCHRONIZATION BETWEEN TRANSMITTER AND RECEIVER

The transmitter and the receiver can operate with different reference frequencies.

Typical values suitable for correct operation of the system should be between 445 and 510kHz, using a cheap ceramic resonator.

Synchronization between the transmitter and the receiver, necessary to obtain the wide range of frequency tolerance described above is achieved by measuring in the receiver the interval between the start pulse and the first data pulse, storing this value and using it as time base T.

### KEYBOARD (pins A to Q) / CODE REPETITION

One column input (K to Q) has to be connected to one row (A to H) input to activate the transmitter.

The contact must be continuously closed for a minimum of 25ms.

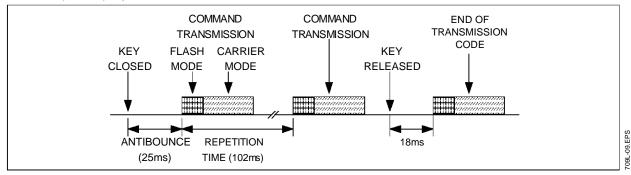
Double and multiple contact operations are not accepted.

The command information is repeatedly transmitted at intervals of 102ms ( $f_{ref} = 500$ kHz) as long as the push button remains operated.

When the contact is interrupted the circuit transmits, after a pause of about 18ms, the "end of transmission code" and returns to stand-by mode.

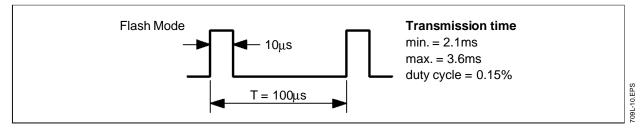
If the contact is interrupted while a command is being transmitted the circuit carries on with the transmission to the end. After a pause of about 18ms it transmits the end of transmission code.

No command is accepted until the "end of transmission code" is over.

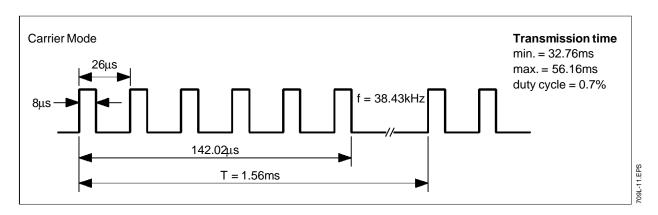


### TRANSMISSION MODE (pin 1)

The M709/M710 can operate in Flash (pin  $1 = V_{DD}$ ) or Carrier (pin  $1 = V_{SS}$ ) transmission modes. Using a reference frequency of 500kHz the output signal has these formats :

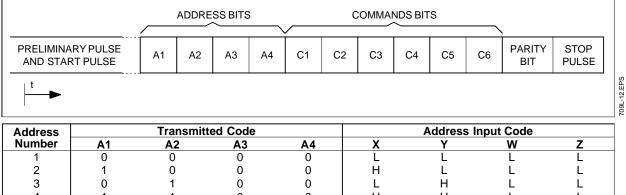






# ADDRESS (pins X, Y, W, Z)

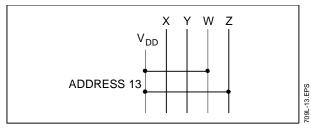
The Address information is coded and transmitted as follows :

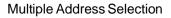


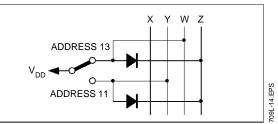
2	1 1	0	0	0		L	L	L
3	0	1	0	0	L	Н	L	L
4	1	1	0	0	Н	Н	L	L
5	0	0	1	0	L	L	Н	L
6	1	0	1	0	Н	L	Н	L
7	0	1	1	0	L	Н	Н	L
8	1	1	1	0	Н	Н	Н	L
9	0	0	0	1	L	L	L	н
10	1	0	0	1	Н	L	L	н
11	0	1	0	1	L	Н	L	Н
12	1	1	0	1	Н	Н	L	н
13	0	0	1	1	L	L	Н	н
14	1	0	1	1	н	L	Н	н
15	0	1	1	1	L	Н	Н	Н
16	1	1	1	1	Н	Н	Н	н

The address inputs have internal pull-downs which are disabled during stand-by.

### Single Address Selection





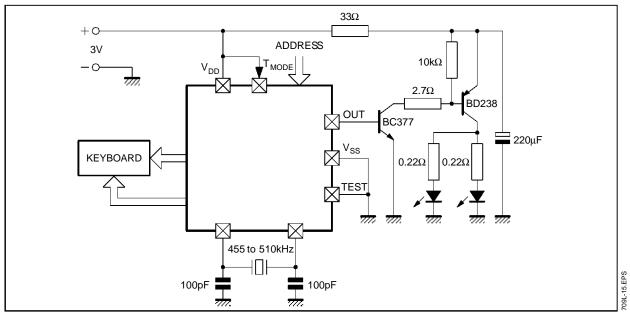


Note : unused inputs can be left open or connected to  $\mathsf{V}_{\mathsf{SS}}.$ 

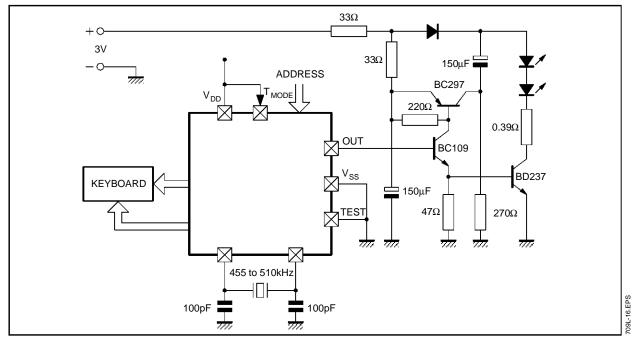


### TYPICAL APPLICATION (flash mode)

### STANDARD

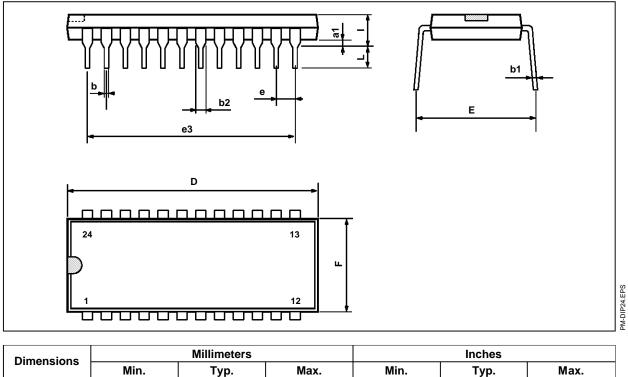


# **VOLTAGE DUPLICATOR**





### PACKAGE MECHANICAL DATA 24 PINS - PLASTIC DIP

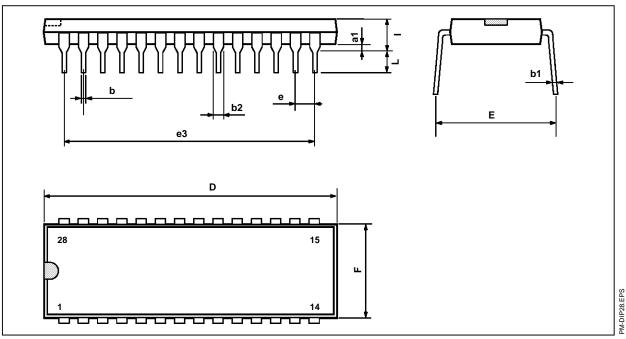


Dimensions		WIIIIIIII CLEI S		IIICIIES					
Dimensions	Min.	Тур.	Max.	Min.	Тур.	Max.			
a1		0.63			0.025				
b		0.45			0.018				
b1	0.23		0.31	0.009		0.012			
b2		1.27			0.050				
D			32.2			1.268			
E	15.2		16.68	0.598		0.657			
е		2.54			0.100				
e3		27.94			1.100				
F			14.1			0.555			
i		4.445			0.175				
L		3.3			0.130				



# PACKAGE MECHANICAL DATA

28 PINS - PLASTIC DIP



Dimensions		Millimeters		Inches						
Dimensions	Min.	Тур.	Max.	Min.	Тур.	Max.				
a1		0.63			0.025					
b		0.45			0.018					
b1	0.23		0.31	0.009		0.012				
b2		1.27			0.050					
D			37.4			1.470				
E	15.2		16.68	0.598		0.657				
е		2.54			0.100					
e3		33.02			1.300					
F			14.1			0.555				
i		4.445			0.175					
L		3.3			0.130					

Information furnished is believed to be accurate and reliable. However, SGS-THOMSON Microelectronics assumes no responsibility for the consequences of use of such information nor for any infringement of patents or other rights of third parties which may result from its use. No licence is granted by implication or otherwise under any patent or patent rights of SGS-THOMSON Microelectronics. Specifications mentioned in this publication are subject to change without notice. This publication supersedes and replaces all information previously supplied. SGS-THOMSON Microelectronics products are not authorized for use as critical components in life support devices or systems without express written approval of SGS-THOMSON Microelectronics.

© 1995 SGS-THOMSON Microelectronics - All Rights Reserved

Purchase of I<sup>2</sup>C Components of SGS-THOMSON Microelectronics, conveys a license under the Philips I<sup>2</sup>C Patent. Rights to use these components in a I<sup>2</sup>C system, is granted provided that the system conforms to the I<sup>2</sup>C Standard Specifications as defined by Philips.

#### SGS-THOMSON Microelectronics GROUP OF COMPANIES

Australia - Brazil - China - France - Germany - Hong Kong - Italy - Japan - Korea - Malaysia - Malta - Morocco The Netherlands - Singapore - Spain - Sweden - Switzerland - Taiwan - Thailand - United Kingdom - U.S.A.

