TOSHIBA Bipolar Digital Integrated Circuit Multi Chip

# TD62M2701FG

#### Low Saturation Voltage H-Bridge Driver

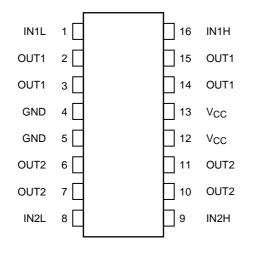
TD62M2701FG is multi-chip H-bridge driver IC incorporates 4 low saturation discrete transistors which equipped bias-resistor and fly-wheel diode. This IC is suitable for forward-reverse control on a battery use motor drive applications.

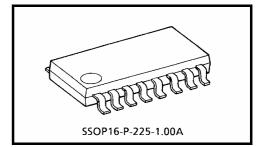
#### Features

- Suitable for high efficiency motor drive circuit
- Built-in fly-wheel diode (lower side)
- Built-in bias resistor (lower side):  $R = 10 \text{ k}\Omega$  (typ.)
- SSOP 16 (1 mm pitch) package sealed
- Low saturation voltage: VCE (sat) (upper + lower) = 0.23 V (typ.): IO = 1 A

 $= 0.45 V (typ.): I_0 = 2 A$ 

#### Pin Assignment (top view)

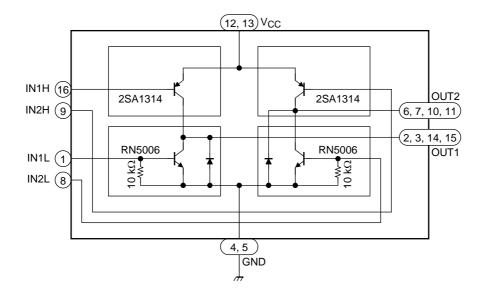




Weight: 0.14 g (typ.)

# **TOSHIBA**

### **Block Diagram**



### Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit	
Supply voltage	V <sub>CC</sub>	10	V	
Collector-base voltage	V <sub>CBO</sub>	10	V	
Collector-emitter voltage	V <sub>CER</sub>	10	V	
Emitter-base voltage	V <sub>EBO</sub>	6	V	
Output current	IOUT	2	A	
	IO (PEAK)	4 (Note 1)		
Base current	Ι <sub>Β</sub>	±0.4	A	
	IB (PEAK)	±0.8 (Note 1)		
Diode forward current	١ <sub>F</sub>	2 (Note 2)	А	
Power dissipation	PD	490	mW	
Junction temperature	Тј	150	°C	
Operating temperature	T <sub>opr</sub>	-40 to 85	°C	
Storage temperature	T <sub>stg</sub>	-55 to 150	°C	

Note 1: T = 10 ms max and maximum duty is less than 30%

Note 2: T = 10 ms single pulse

**Electrical Characteristics (Ta = 25°C)** 

Characteristics		Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Current gain	Upper side	h <sub>FE</sub> (1)	_	$V_{CE} = -1 \ V, \ I_C = -0.5 \ A$	200	_	700	_
	Lower side	h <sub>FE</sub> (1)	_	$V_{CE} = 1 \text{ V}, I_{C} = 0.5 \text{ A}$	160		700	
		h <sub>FE (2)</sub>	_	$V_{CE} = 1 \text{ V}, I_{C} = 2.0 \text{ A}$	60	130		
Output saturation voltage	Upper side	V <sub>CE</sub> (sat)	_	I <sub>C</sub> = -1 A, I <sub>B</sub> = -25 mA		-0.10	-0.22	V
				I <sub>C</sub> = -2 A, I <sub>B</sub> = -50 mA		-0.20	-0.45	
	Lower side			I <sub>C</sub> = 1 A, I <sub>B</sub> = 25 mA		0.13	0.22	
				I <sub>C</sub> = 2 A, I <sub>B</sub> = 50 mA		0.25	0.45	
	Summing total		_	I <sub>C</sub> = 0.5 A, I <sub>B</sub> = 12.5 mA			0.20	
				I <sub>C</sub> = 1 A, I <sub>B</sub> = 25 mA		0.23	0.42	
				I <sub>C</sub> = 2 A, I <sub>B</sub> = 50 mA		0.45	0.85	
Transition frequency		fT		$V_{CE} = 2 V, I_{C} = 0.5 A$		150		MHz
Output leakage current	Upper side	- I <sub>OL</sub>	_	V <sub>CC</sub> = -10 V		0	-5	μA
	Lower side			V <sub>CC</sub> = 10 V		0	5	
Diode forward voltage (lower Side)		V <sub>F</sub>	_	I <sub>F</sub> = 300 mA		0.89	1.2	v
				I <sub>F</sub> = 450 mA, 10 ms		1.60		
Base-emitter resistance		R <sub>BE</sub>	—	_	7	10	13	kΩ
Base-emitter forward voltage	Upper side	VBE (PNP)	_	$V_{CE} = -1 V, I_C = -2 A$		-0.84	-1.5	
	Lower side	V <sub>BE (NPN)</sub>	_	$V_{CE} = 1 V, I_{C} = 2 A$		0.84	1.5	V

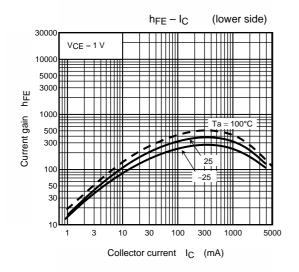
#### **Precautions for Using**

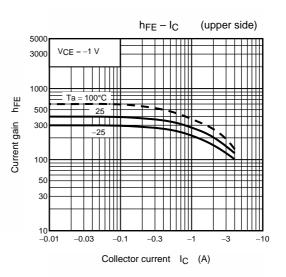
This IC does not integrate protection circuits such as overcurrent and overvoltage protectors.

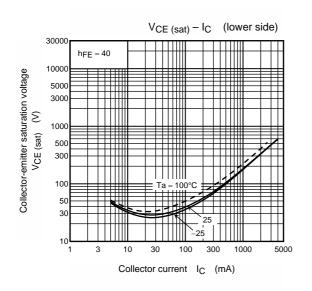
Thus, if excess current or voltage is applied to the IC, the IC may be damaged. Please design the IC so that excess current or voltage will not be applied to the IC.

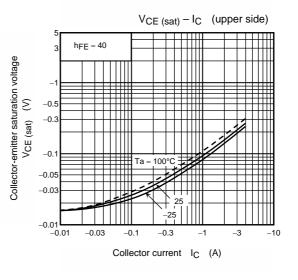
Utmost care is necessary in the design of the output line, V<sub>CC</sub> and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.

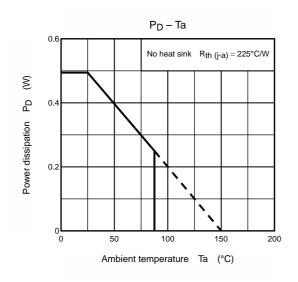
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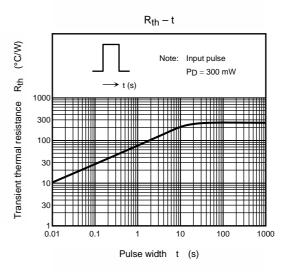






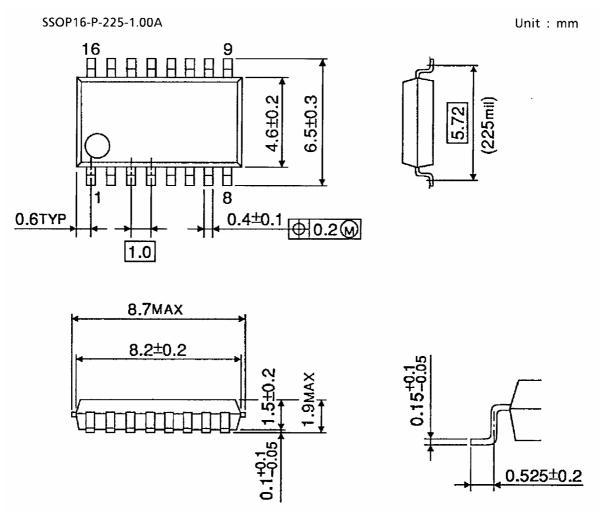




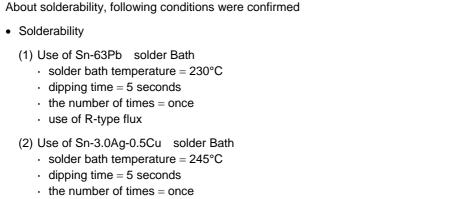


# **TOSHIBA**

### Package Dimensions



Weight: 0.14 g (typ.)



• use of R-type flux

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