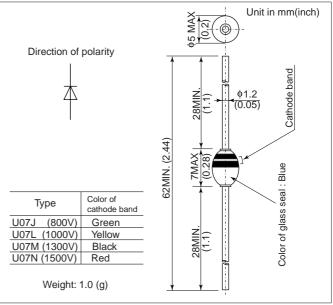


## FEATURES

- For high speed switching.
- Diffused-junction. Glass passivated and encapsulated.

### **OUTLINE DRAWING**



### **ABSOLUTE MAXIMUM RATINGS**

ltems	Туре		U07J	U07L	U07M	U07N			
Repetitive Peak Reverse Voltage	$V_{RRM}$	V	800	1000	1300	1500			
Non-Repetitive Peak Reverse Voltage	V <sub>RSM</sub>	V	1000	1300	1600	1800			
Average Forward Current	I <sub>F(AV)</sub>	А	Single-phase half sine wave $180^{\circ}$ conduction 1.0 (TL = 60°C, Lead length = 10mm )						
Surge(Non-Repetitive) Forward Current	I <sub>FSM</sub>	А	50( Without PIV, 10ms conduction, Tj = 140°C start )						
I <sup>2</sup> t Limit Value	l <sup>2</sup> t	A <sup>2</sup> s	10( Time = 2 ~ 10ms, I = RMS value )						
Operating Junction Temperature	Tj	°C	-65 ~ +140						
Storage Temperature	T <sub>stg</sub>	°C	-65 ~ +200						

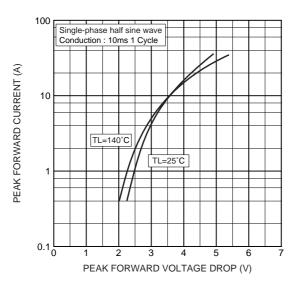
Notes (1) Lead mounting : Lead temperature 300°C max. to 3.2mm from body for 5sec. max.. (2) Mechanical strength : Bending 90°×2 cycles or 180°×1 cycle, Tensile 3kg, Twist 90°×1 cycle.

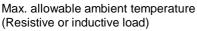
## CHARACTERISTICS(T<sub>L</sub>=25°C)

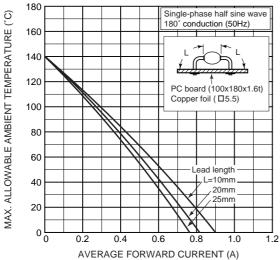
Items	Symbols	Units	Min.	Тур.	Max.	Test Conditions
Peak Reverse Current	I <sub>RRM</sub>	μA	_	2.0	10	Rated V <sub>RRM</sub>
Peak Forward Voltage	V <sub>FM</sub>	V	_	_	2.5	$I_{FM}$ =1.0 Ap, Single-phase half sine wave 1 cycle
Reverse Recovery Time	trr	μs	_	_	0.4	I <sub>F</sub> =2mA, V <sub>R</sub> =-15V
Steady State Thermal Impedance	R <sub>th(j-a)</sub> R <sub>th(j-l)</sub>	°C/W	_	_	60 30	Lead length = 10 mm

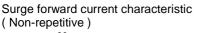
# **U07**

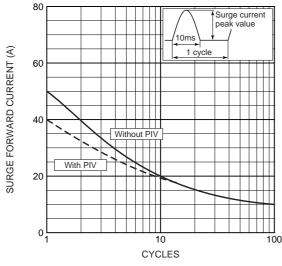
#### Forward characteristics



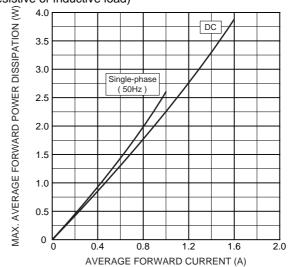




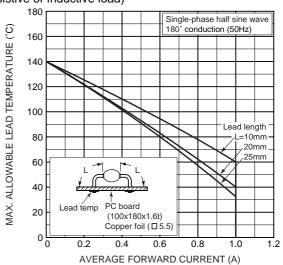




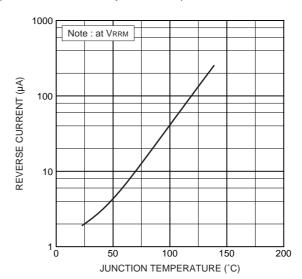
Max. average forward power dissipation (Resistive or inductive load)



Max. allowable lead temperature (Resistive or inductive load)

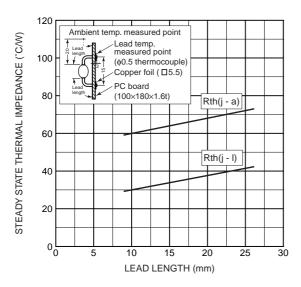


Typ. reverse current vs. junction temperature

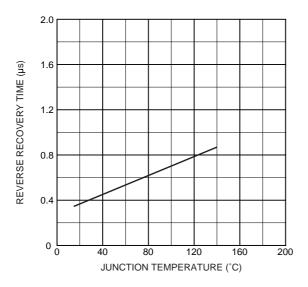


# **U07**

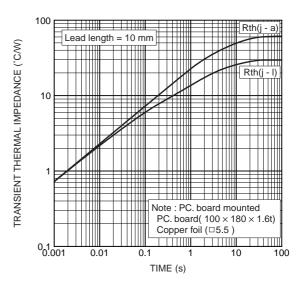
#### Steady state thermal impedance



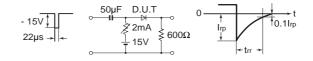
Typ. reverse recovery time vs. junction temperature



#### Transient thermal impedance



#### Reverse recovery time(trr) test circuit



# **HITACHI POWER SEMICONDUCTORS**

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