Toshiba Bipolar Digital Integrated Circuit Multi Chip

# TD62M4601FG

#### 4ch Low Saturation Voltage Source Driver

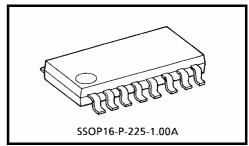
TD62M4601FG is multi chip IC incorporates 4 low saturation discrete (2SA1357) transistors.

This IC is suitable for a battery use motor drive and LED display module applications.

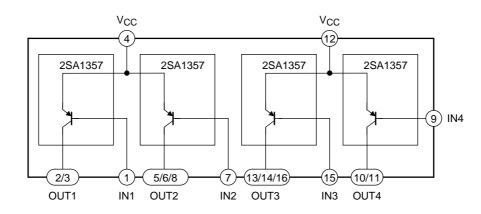
#### Features

- Suitable for motor drive circuit and LED display module
- External bias resistor
- Low saturation voltage
  - VCE (sat) = 0.12 V (typ.) at IC = -1 A VCE (sat) = 0.25 V (typ.) at IC = -2 A
- SSOP16 (1 mm pitch) small package sealed

### **Block Diagram**



#### Weight: 0.14 g (typ.)



#### Pin Assignment (top view)

IN1 [	1	16	] Ουτз
OUT1 [	2	15	] IN3
OUT1 [	3	14	] Ουτ3
Vcc [	4	13	] Ουτ3
OUT2 [	5	12	] ∨cc
OUT2 [	6	11	] OUT4
IN2 [	7	10	] OUT4
OUT2 [	8	9	] IN4
1			1

### Maximum Ratings (Ta = 25°C)

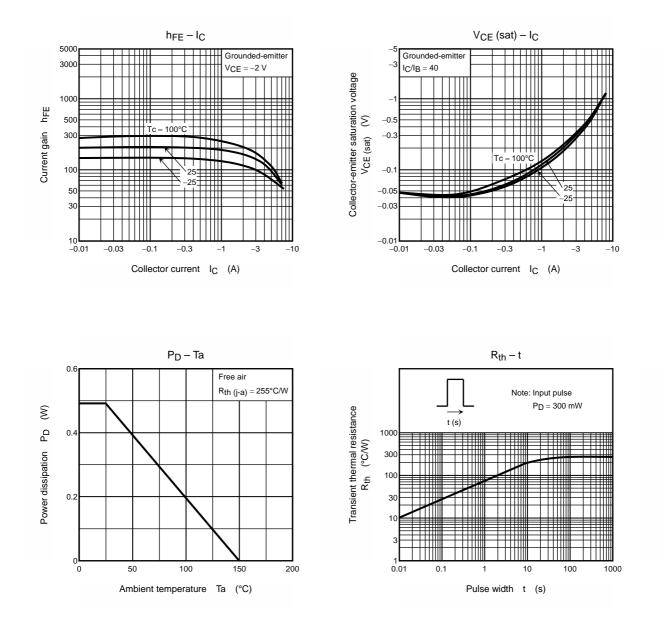
Characteristics	Symbol	Rating	Unit	
Supply voltage	V <sub>CC</sub>	-20	V	
	V <sub>CBO</sub>	-20		
Breakdown voltage	V <sub>CEO</sub>	-20	V	
	V <sub>EBO</sub>	-8		
Output current	Ι <sub>Ο</sub>	-2	A/ch	
	I <sub>O (PRAK)</sub>	-4 (Note)		
Base current	Ι <sub>Β</sub>	-1	А	
Power dissipation	PD	490	mW	
Junction temperature	Тј	150	°C	
Operating temperature	T <sub>opr</sub>	-40 to 85	°C	
Storage temperature	T <sub>stg</sub>	-55 to 150	°C	

Note: T = 10 ms max and maximum duty is less than 30%.

#### **Electrical Characteristics (Ta = 25°C)**

Characteristics	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Current gain	h <sub>FE (1)</sub>		$V_{CE}=-2~V,~I_C=-0.5~A$	100		320	
	h <sub>FE (2)</sub>		$V_{CE} = -2 V, I_C = -2.0 A$	70	140		
Saturation voltage	V <sub>CE (sat)</sub>	_	$I_C = -1 \text{ A}, I_B = -25 \text{ mA}$		-0.12	-0.25	v
Saturation voltage			$I_{C} = -2 \text{ A}, I_{B} = -50 \text{ mA}$	_	-0.25	-0.50	
Transition frequency	f <sub>T</sub>	_	$V_{CE} = -2 V, I_C = -0.5 A$	_	100	_	MHz
Leakage current	I <sub>OL</sub>		$V_{CC} = -20 V$		0	-10	μΑ
Base-emitter forward voltage	V <sub>BE</sub>		$V_{CE} = -2 V, I_C = -2.0 A$	_	-0.84	-1.5	V

# **TOSHIBA**



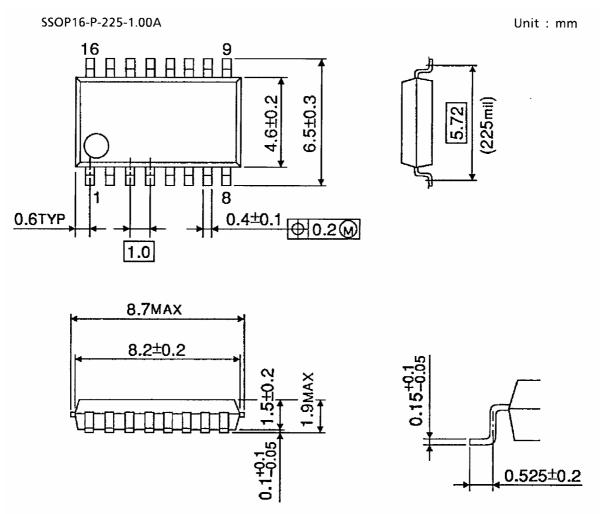
#### **Precautions for Using**

This IC does not integrate protection circuits such as overcurrent and overvoltage protectors. Thus, if excess current or voltage is applied to the IC, the IC may be damaged. Please design the IC so that excess current or voltage will not be applied to the IC.

Utmost care is necessary in the design of the output line, V<sub>CC</sub> and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.

# **TOSHIBA**

## Package Dimensions



Weight: 0.14 g (typ.)



• use of R-type flux

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