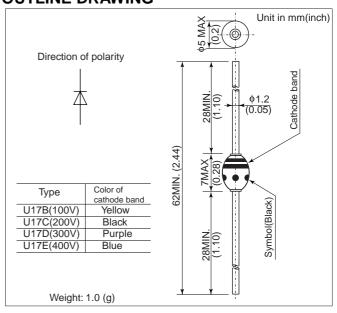
U17

FEATURES

- Transient surge voltage protection.
- Diffused-junction. Glass passivated and encapsulated.

OUTLINE DRAWING



ABSOLUTE MAXIMUM RATINGS

Items	Ту	ре	U17B	U17C	U17D	U17E				
Repetitive Peak Reverse Voltage	V_{RRM}	V	100	200	300	400				
Peak Reverse Power	P _{RM}	kW	3(Tj = 25°C,Impulse duration 10μs Non-repetitive)							
Average Forward Current	I _{F(AV)}	А	2.5 (Single-phase half sine wave 180° conduction $T_L=90^{\circ}$ C, Lead length = 10 mm							
Surge(Non-Repetitive) Forward Current	I _{FSM}	Α	100(Without PIV, 10ms conduction, Tj = 175°C start)							
I ² t Limit Value	l ² t	A ² s	40(Time = 2 ~ 10ms, I = RMS value)							
Operating Junction Temperature	T _j	°C	-40 ~ +175							
Storage Temperature	T _{stg}	°C	-40 ~ + 175							

Notes

- (1) Lead mounting: Lead temperature 300°C max. to 3.2mm from body for 5sec. max..
- (2) Mechanical strength: Bending 90°×2 cycles or 180°×1 cycle, Tensile 3kg, Twist 90°×1 cycle.

CHARACTERISTICS(T, =25°C)

Items	Symbols	Units	Min.	Тур.	Max.	Test Conditions			
		μΑ	_	4	50	B class	1		
Peak Reverse Current	I _{RRM}			1.5	20	C,D class	Rated V _{RRM}		
				0.6	10	E class			
Peak Forward Voltage	V_{FM}	V	_	_	1.1 I _{FM} =2.5Ap, Single-phase half sin wave 1 cycle				
Reverse Recovery Time	trr	μs	_	3.0	_	I_F =2mA, V_R =-15V			
Avalanche Voltage	V _{AVL}	V		Table.1		I_{RM} =1.0mA, Single-phase half sine wave 1 pps, Time \leq 5s			
Avalanche Voltage Temperature Coefficient	α	%/°C	_	0.080	_	$\frac{\Delta VAVL}{VAVL} \times \frac{1}{175-25}$	<100		
Steady State Thermal Impedance	$R_{th(j-a)}$ $R_{th(j-l)}$	°C/W	_	_	60 30	Lead length = 10 mm			

U17

TABLE.1

			U17															
V _{RRM} Class			В				С			D				Е				
V _A	_{vL} Symb	ols	27 30 33 36			33	36	39	44	44	50	55	63	55	63	70	Units	
Т	TYP. V _{AVL}		270	300	330	360	330	360	390	440	440	500	550	630	550	630	700	٧
	Α	MIN	230	255	280	305	280	305	330	375	375	425	465	535	465	535	595	V
V_{AVL}	±15%	MAX	310	345	380	415	380	415	450	505	505	575	635	725	635	725	805	
Band	В	MIN	250	280	305	330	305	330	360	405	405	460	505	580	505	580	645	
	±7.5%	MAX	290	320	355	390	355	390	420	475	475	535	590	680	590	680	750	

As required, the avalanche voltage can be selected as follows:

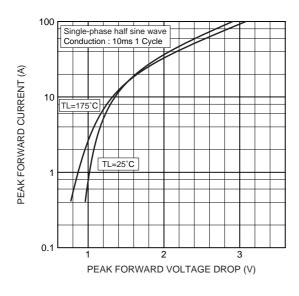
"example" U17C36A

 V_{RRM} 200V

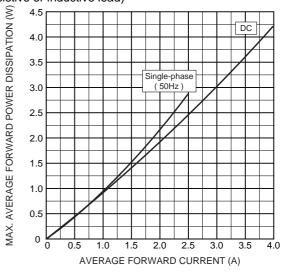
IF_(AV) 2.5A

V_{AVL} 305~415V

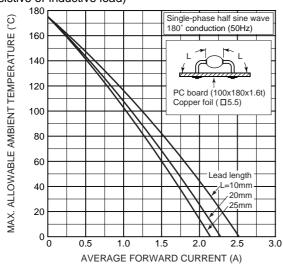
Forward characteristics



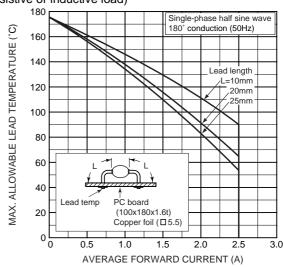
Max. average forward power dissipation (Resistive or inductive load)



Max. allowable ambient temperature (Resistive or inductive load)

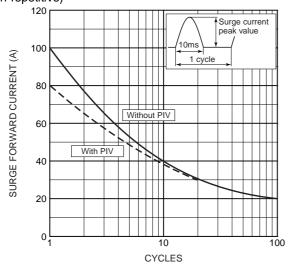


Max. allowable lead temperature (Resistive or inductive load)

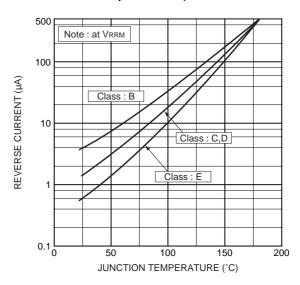


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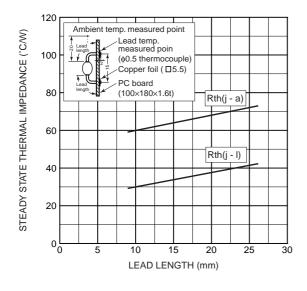
Surge forward current characteristic (Non-repetitive)



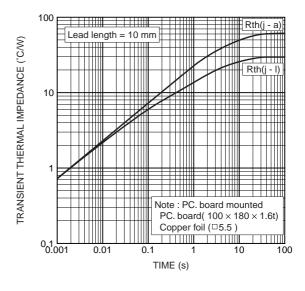
Typ. Reverse current vs. junction temperature



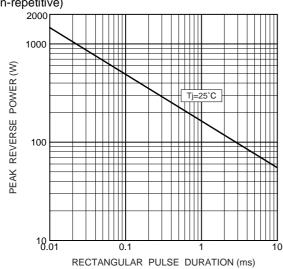
Steady-state thermal impedance



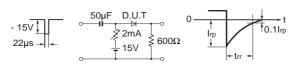
Transient thermal impedance



Typical reverse power characteristic (Non-repetitive)



Reverse recovery time (trr) test circuit



HITACHI POWER SEMICONDUCTORS

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