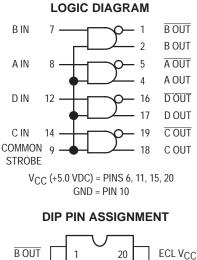
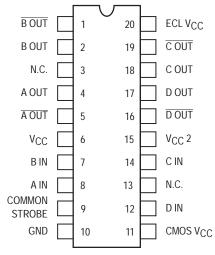
Quad CMOS to PECL* Translator

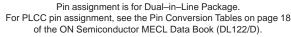
The MC10H352 is a quad translator for interfacing data between a CMOS logic section and the PECL section of digital systems when only a +5.0 Vdc power supply is available. The MC10H352 has CMOS compatible inputs and PECL complementary open–emitter outputs that allow use as an inverting/non–inverting translator or as a differential line driver. When the common strobe input is at a low logic level, it forces all true outputs to the PECL low logic state (\approx +3.2 V) and all inverting outputs to the PECL high logic state (\approx +4.1 V).

The MC10H352 can also be used with the MC10H350 to transmit and receive CMOS information differentially via balanced twisted pair lines.

- Single +5.0 V Power Supply
- All VCC Pins Isolated On Chip
- Differentially Drive Balanced Lines
- t_{pd} = 1.3 nsec Typical



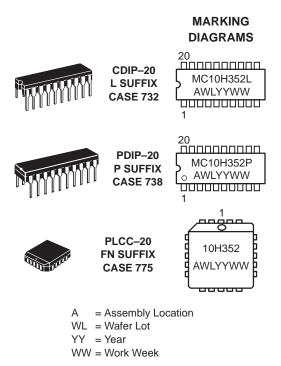






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ORDERING INFORMATION

Device	Package	Shipping
MC10H352L	CDIP-20	18 Units/Rail
MC10H352P	PDIP-20	18 Units/Rail
MC10H352FN	PLCC-20	46 Units/Rail

MC10H352

MAXIMUM RATINGS

Symbol	Characteristic	Rating	Unit
VCC	Power Supply	0 to +7.0	Vdc
VI	Input Voltage (V _{CC} = 5.0 V)	0 to V _{CC}	Vdc
lout	Output Current — Continuous — Surge	50 100	mA
TA	Operating Temperature Range	0 to +75	°C
T _{stg}	Storage Temperature Range — Plastic — Ceramic	−55 to +150 −55 to +165	°C

ELECTRICAL CHARACTERISTICS $(V_{CC} = V_{CC1} = V_{CC2} = 5.0 \text{ V} \pm 5.0\%)^{\dagger}$

		0 °		25°		75 °		
Symbol	Characteristic	Min	Max	Min	Max	Min	Max	Unit
ECL	Power Supply	—	50	—	45	—	50	mA
TTL	Current	_	20	—	15	—	20	mA
IR	Reverse Current Pins 7, 8, 12, 14 Pin 9		25 100		20 80		25 100	μA
١ _F	Forward Current Pins 7, 8, 12, 14 Pin 9		-0.8 -3.2		0.6 2.4		-0.8 -3.2	mA
V _{(BR)in}	Input Voltage Breakdown	5.5	-	5.5	-	5.5	—	Vdc
VI	Input Clamp Voltage (I _{in} = -18 mA)	—	-1.5	—	-1.5	—	-1.5	Vdc
VOH	High Output Voltage (Note 1.)	3.98	4.16	4.02	4.19	4.08	4.27	Vdc
VOL	Low Output Voltage (Note 1.)	3.05	3.37	3.05	3.37	3.05	3.37	Vdc
VIH	High Input Voltage	3.15	—	3.15	_	3.15	—	Vdc
VIL	Low Input Voltage	_	1.5	—	1.5	—	1.5	Vdc

1. With V_{CC} at 5.0 V. V_{OH}/V_{OL} change 1:1 with V_{CC}. **Positive Emitter Coupled Logic

AC PARAMETERS

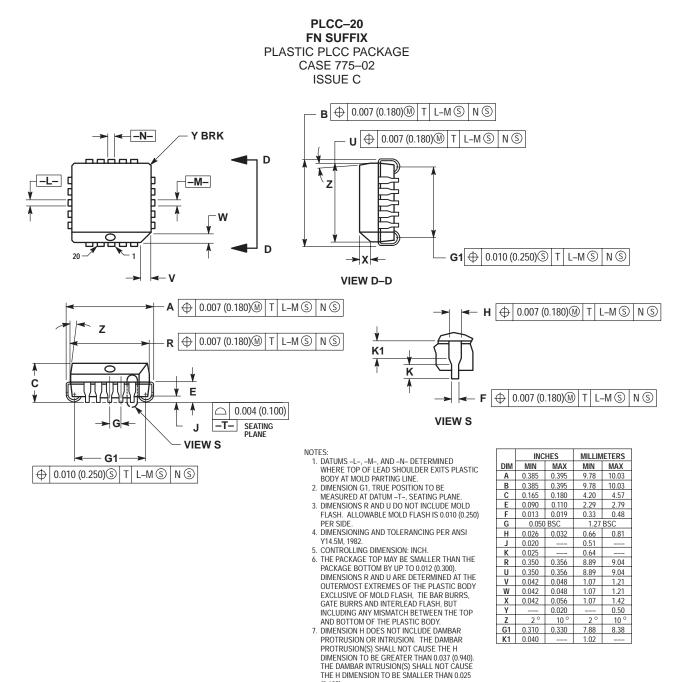
		0 °		25 °		75 °		
Symbol	Characteristic	Min	Мах	Min	Мах	Min	Мах	Unit
^t pd	Propagation Delay (Note 2.)	0.4	1.9	0.4	2.0	0.4	2.1	ns
tr	Rise Time (20% to 80%)	0.4	1.9	0.4	2.0	0.4	2.1	ns
t _f	Fall Time (80% to 20%)	0.4	1.9	0.4	2.0	0.4	2.1	ns
fmax	Maximum Operating Frequency	150	—	150	—	150	_	MHz

2. Propagation delay is measured on this circuit from $V_{CC}/2$ on the input waveform to the 50% point on the output waveform.

†Each MECL 10H series circuit has been designed to meet the dc specifications shown in the test table, after thermal equilibrium has been established. The circuit is in a test socket or mounted on a printed circuit board and transverse air flow greater than 500 lfpm is maintained. Outputs are terminated through a 50–ohm resistor to V_{CC} – 2.0 Vdc.

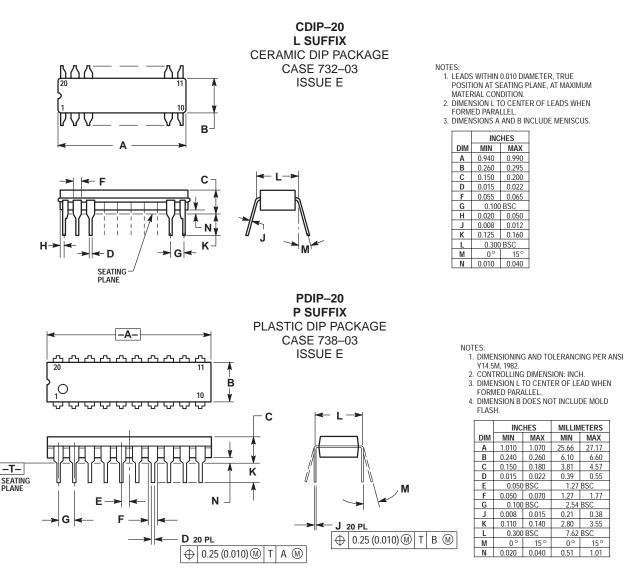
MC10H352

PACKAGE DIMENSIONS



(0.635).

MC10H352



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