

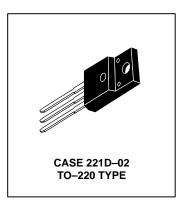
# FULL PAK™ High Voltage NPN Power Transistor For Isolated Package Applications

The BUT11AF was designed for use in line operated switching power supplies in a wide range of end use applications. This device combines the latest state of the art bipolar fabrication techniques to provide excellent switching, high voltage capability and low saturation voltage.

- 1000 Volt V<sub>CES</sub> Rating
- Low Base Drive Requirements
- Isolated Overmold Package
- Improved System Efficiency
- No Isolating Washers Required
- Reduced System Cost
- High Isolation Voltage Capability (4500 V<sub>RMS</sub>)

## **BUT11AF**

POWER TRANSISTOR 5.0 AMPERES 450 VOLTS 40 WATTS



#### **MAXIMUM RATINGS**

Rating		Symbol	Value	Unit
Collector–Emitter Sustaining Voltage		V <sub>CEO(sus)</sub>	450	Vdc
Collector–Emitter Breakdown Voltage		V <sub>CES</sub>	1000	Vdc
Emitter-Base Voltage		V <sub>EBO</sub>	9.0	Vdc
RMS Isolation Voltage (For 1 sec,	Per Figure 7	V <sub>ISOL1</sub>	4500	
$T_A = 25^{\circ}C$ , Rel. Humidity < 30%)	Per Figure 8	V <sub>ISOL2</sub>	3500	V
	Per Figure 9	V <sub>ISOL3</sub>	2500	
Collector Current — Continuous — Pulsed (1)		I <sub>C</sub>	5.0 10	Adc
Base Current — Continuous — Pulsed (1)		I <sub>B</sub>	2.0 4.0	Adc
Total Power Dissipation @ T <sub>C</sub> = 25°C* Derated above 25°C		P <sub>D</sub>	40 0.32	Watts W/°C
Operating and Storage Temperature Ran	ge	T <sub>J</sub> , T <sub>stg</sub>	- 65 to +150	°C

#### THERMAL CHARACTERISTICS

Thermal Resistance — Junction to Case*	$R_{ heta JC}$	3.125	°C/W
Maximum Lead Temperature for soldering purposes 1/8" from case for 5 sec.	T <sub>L</sub>	260	°C

<sup>(1)</sup> Pulse Test: Pulse Width = 5.0 ms, Duty Cycle ≤ 10%.

<sup>\*</sup>Measurement made with thermocouple contacting the bottom insulated mounting surface of the package (in a location beneath the die), the device mounted on a heatsink, thermal grease applied, and a mounting torque of 6 to 8 in · lbs.

#### **ELECTRICAL CHARACTERISTICS** (T<sub>C</sub> = 25°C unless otherwise noted)

	Characteristic	Symbol	Min	Тур	Max	Unit	
OFF CHARACTERIS	TICS (1)		•	•	1	1	•
Collector-Emitter Sustaining Voltage (Figures 1 & 2) ( $I_C = 100 \text{ mAdc}$ , $I_B = 0$ , $L = 25 \mu\text{H}$ )		V <sub>CEO(sus)</sub>	450	_	_	Vdc	
Collector Cutoff Current $(V_{CE} = 1000 \text{ Vdc}, V_{BE} = 0)$ $(V_{CE} = 1000 \text{ Vdc}, V_{BE} = 0, T_J = 125^{\circ}C)$		I <sub>CES</sub>	- -	_ _	1.0 2.0	mAdc	
Emitter-Base Leaka (V <sub>EB</sub> = 9.0 Vdc, I <sub>C</sub>			I <sub>EBO</sub>	_	_	10	mAdc
ON CHARACTERIST	ICS (1)				•	•	l.
Collector-Emitter Sa (I <sub>C</sub> = 2.5 Adc, I <sub>B</sub> =	•		V <sub>CE(sat)</sub>	_	_	1.5	Vdc
Base-Emitter Saturation Voltage $(I_C = 2.5 \text{ Adc}, I_B = 0.5 \text{ Adc})$		V <sub>BE(sat)</sub>	_	-	1.5	Vdc	
DC Current Gain (I <sub>C</sub> = 5.0 mAdc, V <sub>0</sub>	DC Current Gain $(I_C = 5.0 \text{ mAdc}, V_{CE} = 5.0 \text{ Vdc})$		h <sub>FE</sub>	10	_ _	_ _	-
DYNAMIC CHARACT	ERISTICS						
Insulation Capacitance (Collector to External Heatsink)		Cc-hs	-	15	_	pF	
SWITCHING CHARA	CTERISTICS						
Inductive Load (Figu	res 3 & 4)						
Storage		t <sub>s</sub>	t <sub>s</sub>	_	1100	1400	ns
Fall Time	]	T <sub>J</sub> = 25°C	t <sub>fi</sub>	_	80	150	
Storage	$I_C = 2.5 \text{ Adc}, I_{B1} = 0.5 \text{ Adc}$	T 4000C	T 10000	_	1200	1500	
Fall Time		T <sub>J</sub> = 100°C	t <sub>fi</sub>	_	140	300	
Resistive Load (Figu	ires 5 & 6)						
Turn-On Time	I <sub>C</sub> = 2.5 Adc, I <sub>B1</sub> = I <sub>B2</sub> = 0.5 Adc		t <sub>on</sub>	_	_	1000	ns
Storage Time			t <sub>s</sub>	_	_	4000	
Fall Time			t <sub>f</sub>	_	_	800	

<sup>(1)</sup> Pulse Test: Pulse Width = 300  $\mu$ s, Duty Cycle  $\leq 2.0\%$ .

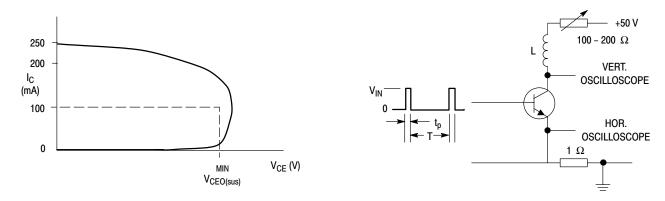


Figure 1. Oscilloscope Display for Sustaining Voltage

Figure 2. Test Circuit for V<sub>CEO(sus)</sub>

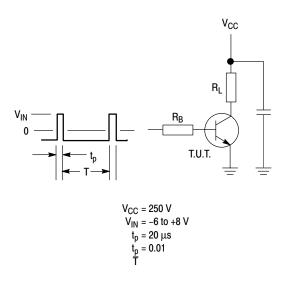


Figure 3. Test Circuit Resistive Load

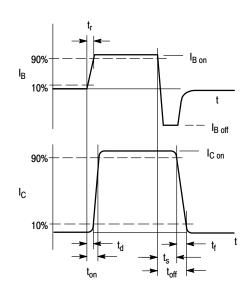


Figure 4. Switching Times Waveforms with Resistive Load

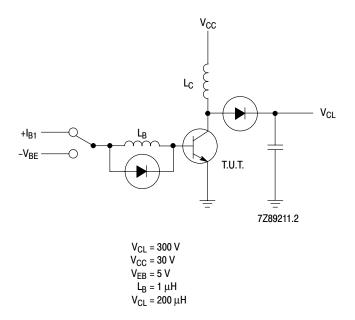


Figure 5. Test Circuit Inductive Load

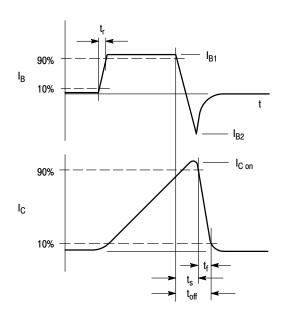


Figure 6. Switching Times Waveforms with Inductive Load

#### **TEST CONDITIONS FOR ISOLATION TESTS\***

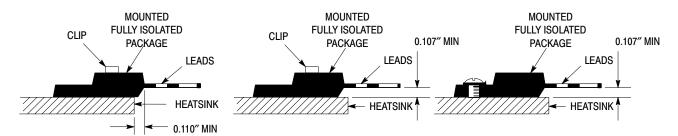


Figure 7. Screw or Clip Mounting Position for Isolation Test Number 1

Figure 8. Clip Mounting Position for Isolation Test Number 2

Figure 9. Screw Mounting Position for Isolation Test Number 3

#### **MOUNTING INFORMATION**

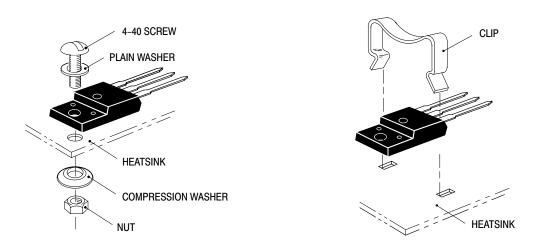


Figure 10. Typical Mounting Techniques for Isolated Package

Laboratory tests on a limited number of samples indicate, when using the screw and compression washer mounting technique, a screw torque of 6 to 8 in · lbs is sufficient to provide maximum power dissipation capability. The compression washer helps to maintain a constant pressure on the package over time and during large temperature excursions.

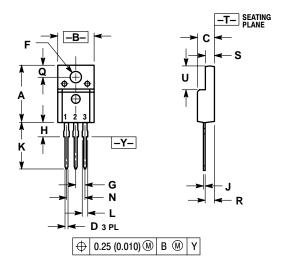
Destructive laboratory tests show that using a hex head 4–40 screw, without washers, and applying a torque in excess of 20 in · lbs will cause the plastic to crack around the mounting hole, resulting in a loss of isolation capability.

Additional tests on slotted 4–40 screws indicate that the screw slot fails between 15 to 20 in lbs without adversely affecting the package. However, in order to positively ensure the package integrity of the fully isolated device, ON Semiconductor does not recommend exceeding 10 in lbs of mounting torque under any mounting conditions.

<sup>\*</sup>Measurement made between leads and heatsink with all leads shorted together.

#### **PACKAGE DIMENSIONS**

#### TO-220 FULLPAK CASE 221D-02 **ISSUE D**



- NOTES:
  1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
  2. CONTROLLING DIMENSION: INCH.

	INCHES		MILLIN	IETERS	
DIM	MIN	MAX	MIN	MAX	
Α	0.621	0.629	15.78	15.97	
В	0.394	0.402	10.01	10.21	
С	0.181	0.189	4.60	4.80	
D	0.026	0.034	0.67	0.86	
F	0.121	0.129	3.08	3.27	
G	0.100 BSC		2.54 BSC		
Н	0.123	0.129	3.13	3.27	
J	0.018	0.025	0.46	0.64	
K	0.500	0.562	12.70	14.27	
L	0.045	0.060	1.14	1.52	
N	0.200 BSC		5.08 BSC		
Q	0.126	0.134	3.21	3.40	
R	0.107	0.111	2.72	2.81	
S	0.096	0.104	2.44	2.64	
U	0.259	0.267	6.58	6.78	

## **Notes**

## **Notes**

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