SN74LS365A, SN74LS367A, SN74LS368A

3-State Hex Buffers

These devices are high speed hex buffers with 3-state outputs. They are organized as single 6-bit or 2-bit/4-bit, with inverting or non-inverting data (D) paths. The outputs are designed to drive 15 TTL Unit Loads or 60 Low Power Schottky loads when the Enable (E) is LOW.

When the Output Enable (E) is HIGH, the outputs are forced to a high impedance "off" state. If the outputs of the 3-state devices are tied together, all but one device must be in the high impedance state to avoid high currents that would exceed the maximum ratings. Designers should ensure that Output Enable signals to 3-state devices whose outputs are tied together are designed so there is no overlap.

GUARANTEED OPERATING RANGES

Symbol	Parameter	Min	Тур	Max	Unit
VCC	Supply Voltage	4.75	5.0	5.25	V
T _A	Operating Ambient Temperature Range	0	25	70	°C
ІОН	Output Current – High			-2.6	mA
lOL	Output Current – Low			24	mA



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LOW POWER SCHOTTKY



PLASTIC N SUFFIX CASE 648



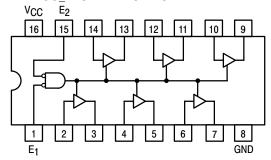
SOIC D SUFFIX CASE 751B

ORDERING INFORMATION

Device	Package	Shipping
SN74LS365AN	16 Pin DIP	2000 Units/Box
SN74LS365AD	SOIC-16	38 Units/Rail
SN74LS365ADR2	SOIC-16	2500/Tape & Reel
SN74LS367AN	16 Pin DIP	2000 Units/Box
SN74LS367AD	SOIC-16	38 Units/Rail
SN74LS367ADR2	SOIC-16	2500/Tape & Reel
SN74LS368AN	16 Pin DIP	2000 Units/Box
SN74LS368AD	SOIC-16	38 Units/Rail
SN74LS368ADR2	SOIC-16	2500/Tape & Reel

SN74LS365A, SN74LS367A, SN74LS368A

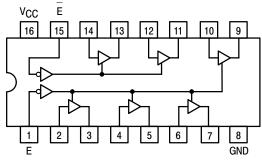
SN74LS365A HEX 3-STATE BUFFER WITH COMMON 2-INPUT NOR ENABLE



TRUTH TABLE

IN	INPUTS		OUTPUT
E ₁	E ₂	D	OUTFUT
П	П	L	L
L	L	Н	Н
Н	Х	Х	(Z)
Х	Н	Х	(Z)

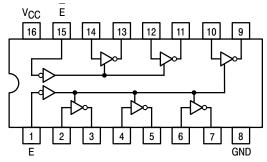
SN74LS367A HEX 3-STATE BUFFER SEPARATE 2-BIT AND 4-BIT SECTIONS



TRUTH TABLE

INP	JTS	OUTPUT
E	D	OUTFUT
L	L	L
L	Н	Н
Н	Х	(Z)

SN74LS368A HEX 3-STATE INVERTER BUFFER SEPARATE 2-BIT AND 4-BIT SECTIONS



TRUTH TABLE

INP	JTS	OUTPUT
E	D	OUTFOI
L L H	L H X	H L (Z)

SN74LS365A, SN74LS367A, SN74LS368A

DC CHARACTERISTICS OVER OPERATING TEMPERATURE RANGE (unless otherwise specified)

			Limits				
Symbol	Parameter	Min	Тур	Max	Unit	Tes	st Conditions
VIH	Input HIGH Voltage	2.0			٧	Guaranteed Input HIGH Voltage for All Inputs	
V _{IL}	Input LOW Voltage			0.8	V	Guaranteed Inpu All Inputs	t LOW Voltage for
VIK	Input Clamp Diode Voltage		-0.65	-1.5	V	V _{CC} = MIN, I _{IN} :	= –18 mA
VOH	Output HIGH Voltage	2.4	3.1		V	V _{CC} = MIN, I _{OH} = MAX, V _{IN} = V _{IH} or V _{IL} per Truth Table	
V	Outroot I OW/Vallana		0.25	0.4	V	I _{OL} = 12 mA	V _{CC} = V _{CC} MIN,
V _{OL}	Output LOW Voltage		0.35	0.5	V	I _{OL} = 24 mA	VIN = VIL or VIH per Truth Table
lozh	Output Off Current HIGH			20	μΑ	V _{CC} = MAX, V _{OUT} = 2.7 V	
lozL	Output Off Current LOW			-20	μΑ	V _{CC} = MAX, V _{OUT} = 0.4 V	
l	Input HIGH Current			20	μΑ	$V_{CC} = MAX, V_{IN} = 2.7 V$	
lН	input riiGri Curient			0.1	mA	V _{CC} = MAX, V _{IN}	y = 7.0 V
	In <u>pu</u> t LOW Current E Inputs			-0.4	mA	V _{CC} = MAX, V _{IN} = 0.4 V	
I _{IL}	D Inputs			-20	μА	V _{CC} = MAX, V _{IN} = 0.5 V Either E Input at 2.0 V	
				-0.4	mA	V _{CC} = MAX, V _{IN} = 0.4 V Both E Inputs at 0.4 V	
los	Short Circuit Current (Note 1)	-40		-225	mA	V _{CC} = MAX	
lcc	Power Supply Current LS365A, 367A			24	mA	V _{CC} = MAX	
	LS368A			21			

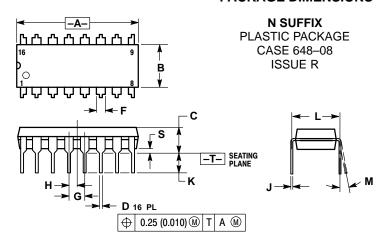
Note 1: Not more than one output should be shorted at a time, nor for more than 1 second.

AC CHARACTERISTICS ($T_A = 25^{\circ}C$, $V_{CC} = 5.0 \text{ V}$)

		Limits							
		LS36	LS365A/LS367A		LS366A/LS368A				
Symbol	Parameter	Min	Тур	Max	Min	Тур	Max	Unit	Test Conditions
t _{PLH} t _{PHL}	Propagation Delay		10 9.0	16 22		7.0 12	15 18	ns	C _L = 45 pF,
t _{PZH} t _{PZL}	Output Enable Time		19 24	35 40		18 28	35 45	ns	$R_L = 667 \Omega$
^t PHZ ^t PLZ	Output Disable Time			30 35			32 35	ns	C _L = 5.0 pF

SN74LS365A, SN74LS367A, SN74LS368A

PACKAGE DIMENSIONS

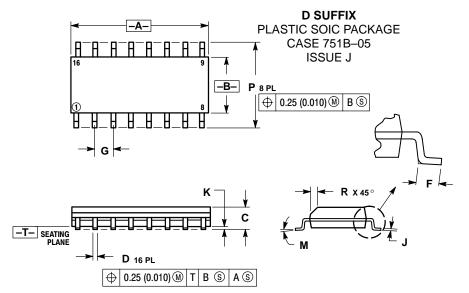


NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: INCH.
- 3. DIMENSION L TO CENTER OF LEADS WHEN FORMED PARALLEL.
- 4. DIMENSION B DOES NOT INCLUDE MOLD FLASH.

5.	ROUNDED	CORNERS	OPTIONAL.
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	INC	HES	MILLIN	IETERS	
DIM	MIN	MAX	MIN	MAX	
Α	0.740	0.770	18.80	19.55	
В	0.250	0.270	6.35	6.85	
С	0.145	0.175	3.69	4.44	
D	0.015	0.021	0.39	0.53	
F	0.040	0.70	1.02	1.77	
G	0.100	BSC	2.54 BSC		
Н	0.050	BSC	1.27 BSC		
J	0.008	0.015	0.21	0.38	
K	0.110	0.130	2.80	3.30	
L	0.295	0.305	7.50	7.74	
M	0°	10°	0°	10 °	
S	0.020	0.040	0.51	1.01	



NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: MILLIMETER.
 3. DIMENSIONS A AND B DO NOT INCLUDE
- DIMENSIONS A AND B DO NOT INCLUDE MOLD PROTRUSION.
 MAXIMUM MOLD PROTRUSION 0.15 (0.006)
- MAXIMUM MOLD PROTRUSION 0.15 (0.006) PER SIDE.
- DIMENSION D DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.127 (0.005) TOTAL IN EXCESS OF THE D DIMENSION AT MAXIMUM MATERIAL CONDITION.

	MILLIN	IETERS	INC	HES	
DIM	MIN	MIN MAX		MAX	
Α	9.80	10.00	0.386	0.393	
В	3.80	4.00	0.150	0.157	
c	1.35	1.75	0.054	0.068	
D	0.35	0.49	0.014	0.019	
F	0.40	1.25	0.016	0.049	
G	1.27	BSC	0.050 BSC		
7	0.19	0.25	0.008	0.009	
K	0.10	0.25	0.004	0.009	
М	0 °	7°	0°	7°	
Р	5.80	6.20	0.229	0.244	
R	0.25	0.50	0.010	0.019	

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