

One Watt Amplifier Transistor NPN Silicon

BDC01D

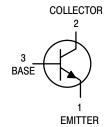
MAXIMUM RATINGS

Rating	Symbol	BDC01D	Unit
Collector–Emitter Voltage	V _{CEO}	100	Vdc
Collector-Base Voltage	V _{CBO}	100	Vdc
Emitter-Base Voltage	V _{EBO}	5.0	Vdc
Collector Current — Continuous	I _C	0.5	Adc
Total Device Dissipation @ T _A = 25°C Derate above 25°C	P _D	1.0 8.0	Watts mW/°C
Total Device Dissipation @ T _C = 25°C Derate above 25°C	P _D	2.5 20	Watts mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-55 to +150	°C



THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	125	°C/W
Thermal Resistance, Junction to Case	$R_{\theta JC}$	50	°C/W



$\textbf{ELECTRICAL CHARACTERISTICS} \ (T_A = 25^{\circ}\text{C unless otherwise noted})$

Characteristic	Symb ol	Min	Max	Unit	
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OFF CHARACTERISTICS

Collector–Emitter Voltage (I _C = 10 mA, I _B = 0)	V _{(BR)C} EO	100		Vdc
Collector Cutoff Current (V _{CB} = 100 V, I _E = 0)	I _{CBO}	_	0.1	μAdc
Emitter Cutoff Current (I _C = 0, V _{EB} = 5.0 V)	I _{EBO}	_	100	nAdc

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted) (Continued)

Characteristic	Symbol	Min	Max	Unit		
ON CHARACTERISTICS						
DC Current Gain $(I_C = 100 \text{ mA}, V_{CE} = 1.0 \text{ V})$ $(I_C = 500 \text{ mA}, V_{CE} = 2.0 \text{ V})$	h _{FE}	40 25	400 —	_		
Collector–Emitter Saturation Voltage ⁽¹⁾ (I _C = 1000 mA, I _B = 100 mA)	V _{CE(sat)}	_	0.7	Vdc		
Collector–Emitter On Voltage ⁽¹⁾ (I _C = 1000 mA, V _{CE} = 1.0 V)	V _{BE(on)}	_	1.2	Vdc		
DYNAMIC CHARACTERISTICS						
Current Gain Bandwidth Product $(I_C = 200 \text{ mA}, V_{CE} = 5.0 \text{ V}, f = 20 \text{ MHz})$		50	_	MHz		
Output Capacitance $(V_{CB} = 10 \text{ V, I}_{E} = 0, f = 1.0 \text{ MHz})$		_	30	pF		

^{1.} Pulse Test: Pulse Width \leq 300 μ s; Duty Cycle 2.0%.

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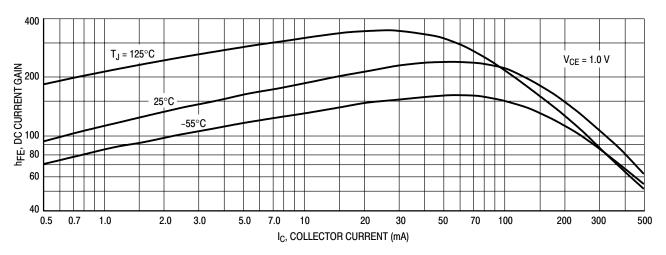


Figure 1. DC Current Gain

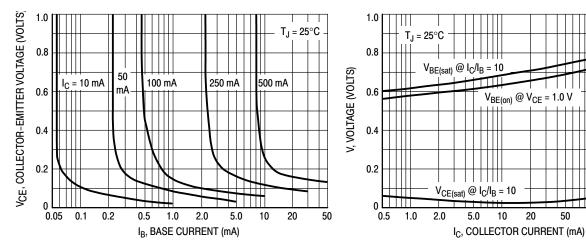


Figure 2. Collector Saturation Region

Figure 3. "On" Voltages

100

500

BDC01D

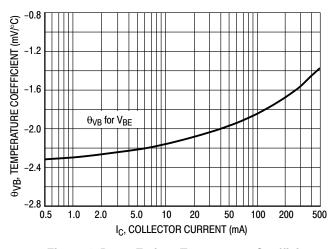


Figure 4. Base–Emitter Temperature Coefficient

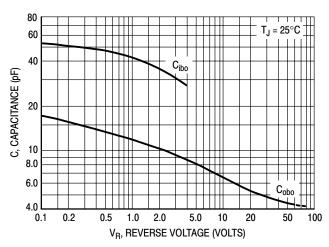


Figure 5. Capacitance

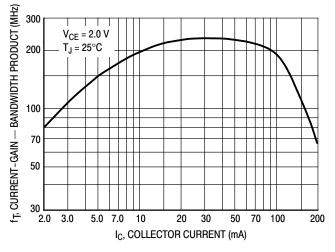


Figure 6. Current-Gain — Bandwidth Product

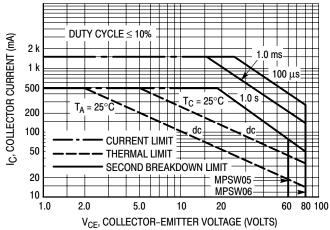
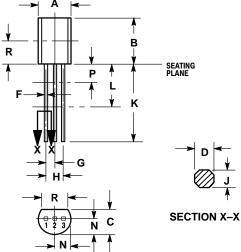


Figure 7. Active Region — Safe Operating Area

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PACKAGE DIMENSIONS

TO-92 (TO-226) **CASE 29-10 ISSUE AL**



YI F 14 **EMITTER**

COLLECTOR

BASE

NOTES

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- 2 CONTROLLING DIMENSION: INCH
- 3. CONTOUR OF PACKAGE BEYOND DIMENSION R
- IS UNCONTROLLED.

 DIMENSION F APPLIES BETWEEN P AND L. DIMENSIONS D AND J APPLY BETWEEN L AND K MIMIMUM. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIM	ETERS
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.44	5.21
В	0.290	0.310	7.37	7.87
С	0.125	0.165	3.18	4.19
D	0.018	0.021	0.457	0.533
F	0.016	0.019	0.407	0.482
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
J	0.018	0.024	0.46	0.61
K	0.500		12.70	
L	0.250		6.35	
N	0.080	0.105	2.04	2.66
Р		0.100		2.54
R	0.135		3.43	

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