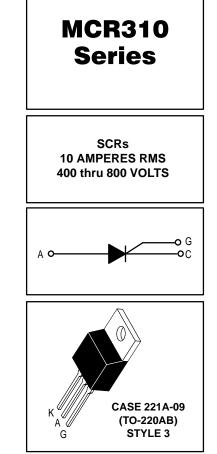
Silicon Controlled Rectifiers Reverse Blocking Triode Thyristors

... designed for industrial and consumer applications such as temperature, light and speed control; process and remote controls; warning systems; capacitive discharge circuits and MPU interface.

- Center Gate Geometry for Uniform Current Density
- All Diffused and Glass-Passivated Junctions for Parameter Uniformity and Stability
- Small, Rugged, Thermowatt Construction for Low Thermal Resistance, High Heat Dissipation and Durability
- Low Trigger Currents, 200 μA Maximum for Direct Driving from Integrated Circuits



MAXIMUM RATINGS (T_J = 25°C unless otherwise noted.)

Rating		Symbol	Value	Unit	
Ν	king MCR310-6 MCR310-8 MCR310-10	VDRM or VRRM	400 600 800	Volts	
On-State RMS Current (T _C = 75°C)		IT(RMS)	10	Amps	
Peak Non-repetitive Surge Current (1/2 Cycle, 60 Hz, T _J = -40 to 110°C)		ITSM	100	Amps	
Circuit Fusing (t = 8.3 ms)		l ² t	40	A ² s	
Peak Gate Voltage (t \leq 10 µs)		VGM	±5	Volts	
Peak Gate Current (t \leq 10 µs)		I _{GM}	1	Amp	
Peak Gate Power (t $\leq 10 \mu$ s)		PGM	5	Watts	
Average Gate Power		PG(AV)	0.75	Watt	
Operating Junction Temperature Range		ТJ	-40 to +110	°C	
Storage Temperature Range		T _{stg}	-40 to +150	°C	
Mounting Torque		-	8	inlb.	

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	$R_{\theta JC}$	2.2	°C/W
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	60	°C/W

 V_{DRM} and V_{RRM} for all types can be applied on a continuous basis. Ratings apply for zero or negative gate voltage; however, positive gate voltage shall not be applied concurrent with negative potential on the anode. Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded.

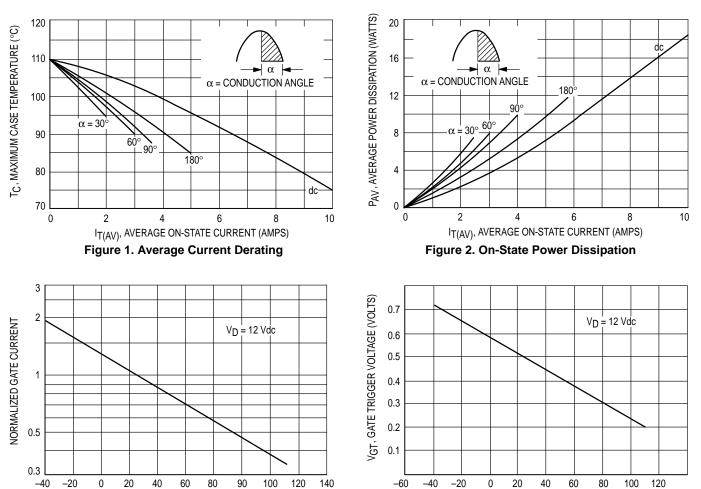


MCR310 Series

Characteristic	Symbol	Min	Тур	Мах	Unit
Peak Forward Blocking Current(1) $T_C = 110^{\circ}C$ $(T_J = 110^{\circ}C, V_D = Rated V_{DRM})$ $T_C = 25^{\circ}C$	IDRM		_	500 10	μΑ μΑ
$\begin{array}{ll} \mbox{Peak Reverse Blocking Current}(1) & T_{C} = 110^{\circ}C \\ (T_{J} = 110^{\circ}C, \ V_{R} = Rated \ V_{RRM}) & T_{C} = 25^{\circ}C \end{array}$	IRRM			500 10	μΑ μΑ
On-State Voltage (I_{TM} = 20 A Peak, Pulse Width \leq 1 ms, Duty Cycle \leq 2%)	VTM	—	1.7	2.2	Volts
Gate Trigger Current, Continuous $dc^{(2)}$ (V _D = 12 V, R _L = 100 Ω)	IGT	—	30	200	μA
Gate Trigger Voltage, Continuous dc $(V_D = 12 V, R_L = 100 \Omega)$ $(V_D = Rated V_{DRM}, R_L = 10 k\Omega, T_J = 110^{\circ}C)$	V _{GT}	 0.1	0.5 —	1.5 —	Volts
Holding Current ($V_D = 12 V$, $I_{TM} = 100 mA$)	Ч	—	_	6	mA
Critical Rate of Rise of Forward Blocking Voltage (V _D = Rated V _{DRM} , T _J = 110°C, Exponential Waveform)	dv/dt	_	10	_	V/µs
Gate Controlled Turn-On Time (V _D = Rated V _{DRM} , I _{TM} = 20 A, I _G = 2 mA)	^t gt	_	1	_	μs

1. Ratings apply for negative gate voltage or R_{GK} = 1 kΩ. Devices shall not have a positive gate voltage concurrently with a negative voltage on the anode. Devices should not be tested with a constant current source for forward and reverse blocking capability such that the voltage applied exceeds the rated blocking voltage.

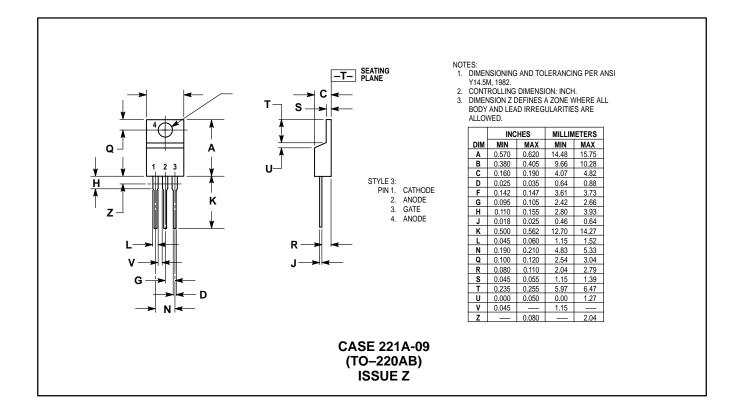
2. Does not include R_{GK} current.



TJ, JUNCTION TEMPERATURE (°C)

Figure 4. Gate Voltage

PACKAGE DIMENSIONS



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