## 3 Watt DO-41 Surmetic<sup>™</sup> 30 **Zener Voltage Regulators**

This is a complete series of 3 Watt Zener diodes with limits and excellent operating characteristics that reflect the superior capabilities of silicon-oxide passivated junctions. All this in an axial-lead, transfer-molded plastic package that offers protection in all common environmental conditions.

#### **Specification Features:**

- Zener Voltage Range 3.3 V to 200 V
- ESD Rating of Class 3 (>16 KV) per Human Body Model
- Surge Rating of 98 W @ 1 ms
- Maximum Limits Guaranteed on up to Six Electrical Parameters
- Package No Larger than the Conventional 1 Watt Package

### **Mechanical Characteristics:**

CASE: Void free, transfer-molded, thermosetting plastic FINISH: All external surfaces are corrosion resistant and leads are readily solderable

MAXIMUM LEAD TEMPERATURE FOR SOLDERING PURPOSES: 230°C, 1/16" from the case for 10 seconds **POLARITY:** Cathode indicated by polarity band **MOUNTING POSITION:** Any

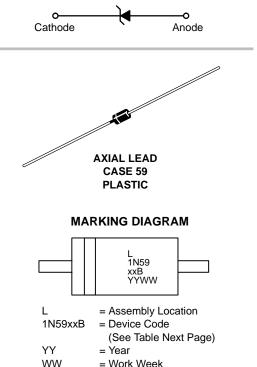
#### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Max. Steady State Power Dissipation @ T <sub>L</sub> = 75°C, Lead Length = 3/8"	PD	3	W
Derate above 75°C		24	mW/°C
Steady State Power Dissipation @ $T_A = 50^{\circ}C$	PD	1	W
Derate above 50°C		6.67	mW/°C
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	65 to +200	°C



## ON Semiconductor

http://onsemi.com



## = Work Week

#### **ORDERING INFORMATION**

Device	Package	Shipping
1N59xxB	Axial Lead	2000 Units/Box
1N59xxBRL	Axial Lead	6000/Tape & Reel
1N59xxBRR1 <sup>†</sup>	Axial Lead	2000/Tape & Reel
1N59xxBRR2 <sup>‡</sup>	Axial Lead	2000/Tape & Reel

<sup>†</sup> Polarity band **up** with cathode lead off first

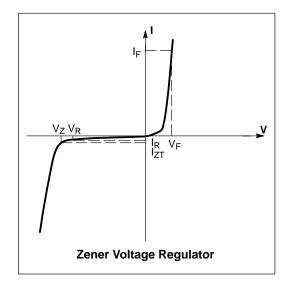
<sup>‡</sup> Polarity band **down** with cathode lead off first

Devices listed in **bold**, italic are ON Semiconductor Preferred devices. Preferred devices are recommended choices for future use and best overall value.

### **ELECTRICAL CHARACTERISTICS**

 $(T_L = 30^{\circ}C \text{ unless otherwise noted}, V_F = 1.5 \text{ V Max } @ I_F = 200 \text{ mAdc for all types})$ 

Symbol	Parameter				
Vz	Reverse Zener Voltage @ IZT				
I <sub>ZT</sub>	Reverse Current				
Z <sub>ZT</sub>	Maximum Zener Impedance @ I <sub>ZT</sub>				
I <sub>ZK</sub>	Reverse Current				
Z <sub>ZK</sub>	Maximum Zener Impedance @ IZK				
I <sub>R</sub>	Reverse Leakage Current @ V <sub>R</sub>				
V <sub>R</sub>	Breakdown Voltage				
١ <sub>F</sub>	Forward Current				
V <sub>F</sub>	Forward Voltage @ I <sub>F</sub>				
I <sub>ZM</sub>	Maximum DC Zener Current				



		Zener Voltage (Note 2)				Zener Impedance (Note 3)			Leakage Current		
Device	Device	,	V <sub>Z</sub> (Volts)		@ I <sub>ZT</sub>	Z <sub>ZT</sub> @ I <sub>ZT</sub>	Z <sub>ZT</sub> @ I <sub>ZT</sub> Z <sub>ZK</sub> @ I <sub>ZK</sub>		<sub>K</sub> I <sub>R</sub> @ V <sub>R</sub>		I <sub>ZM</sub>
(Note 1)	Marking	Min	Nom	Max	mA	Ω	Ω	mA	μ <b>Α Μax</b>	Volts	mA
1N5913B	1N5913B	3.14	3.3	3.47	113.6	10	500	1	100	1	454
1N5917B	1N5917B	4.47	4.7	4.94	79.8	5	500	1	5	1.5	319
1N5919B	1N5919B	5.32	5.6	5.88	66.9	2	250	1	5	3	267
1N5920B	1N5920B	5.89	6.2	6.51	60.5	2	200	1	5	4	241
1N5921B	1N5921B	6.46	6.8	7.14	55.1	2.5	200	1	5	5.2	220
1N5923B	1N5923B	7.79	8.2	8.61	45.7	3.5	400	0.5	5	6.5	182
1N5924B	1N5924B	8.65	9.1	9.56	41.2	4	500	0.5	5	7	164
1N5925B	1N5925B	9.50	10	10.50	37.5	4.5	500	0.25	5	8	150
1N5926B	1N5926B	10.45	11	11.55	34.1	5.5	550	0.25	1	8.4	136
1N5927B	1N5927B	11.40	12	12.60	31.2	6.5	550	0.25	1	9.1	125
1N5929B	1N5929B	14.25	15	15.75	25.0	9	600	0.25	1	11.4	100
1N5930B	1N5930B	15.20	16	16.80	23.4	10	600	0.25	1	12.2	93
1N5931B	1N5931B	17.10	18	18.90	20.8	12	650	0.25	1	13.7	83
1N5932B	1N5932B	19.00	20	21.00	18.7	14	650	0.25	1	15.2	75
1N5933B	1N5933B	20.90	22	23.10	17.0	17.5	650	0.25	1	16.7	68
1N5934B	1N5934B	22.80	24	25.20	15.6	19	700	0.25	1	18.2	62
1N5935B	1N5935B	25.65	27	28.35	13.9	23	700	0.25	1	20.6	55
1N5936B	1N5936B	28.50	30	31.50	12.5	28	750	0.25	1	22.8	50
1N5937B	1N5937B	31.35	33	34.65	11.4	33	800	0.25	1	25.1	45
1N5938B	1N5938B	34.20	36	37.80	10.4	38	850	0.25	1	27.4	41
1N5940B	1N5940B	40.85	43	45.15	8.7	53	950	0.25	1	32.7	34
1N5941B	1N5941B	44.65	47	49.35	8.0	67	1000	0.25	1	35.8	31
1N5942B	1N5942B	48.45	51	53.55	7.3	70	1100	0.25	1	38.8	29
1N5943B	1N5943B	53.20	56	58.80	6.7	86	1300	0.25	1	42.6	26
1N5944B	1N5944B	58.90	62	65.10	6.0	100	1500	0.25	1	47.1	24
1N5945B	1N5945B	64.60	68	71.40	5.5	120	1700	0.25	1	51.7	22
1N5946B	1N5946B	71.25	75	78.75	5.0	140	2000	0.25	1	56	20
1N5947B	1N5947B	77.90	82	86.10	4.6	160	2500	0.25	1	62.2	18
1N5948B	1N5948B	86.45	91	95.55	4.1	200	3000	0.25	1	69.2	16
1N5950B	1N5950B	104.5	110	115.5	3.4	300	4000	0.25	1	83.6	13
1N5951B	1N5951B	114	120	126	3.1	380	4500	0.25	1	91.2	12
1N5952B	1N5952B	123.5	130	136.5	2.9	450	5000	0.25	1	98.8	11
1N5953B	1N5953B	142.5	150	157.5	2.5	600	6000	0.25	1	114	10
1N5954B	1N5954B	152	160	168	2.3	700	6500	0.25	1	121.6	9
1N5955B	1N5955B	171	180	189	2.1	900	7000	0.25	1	136.8	8
1N5956B	1N5956B	190	200	210	1.9	1200	8000	0.25	1	152	7

1. TOLERANCE AND TYPE NUMBER DESIGNATION

TOLERANCE AND TYPE NUMBER DESIGNATION Tolerance designation – device tolerance of ±5% are indicated by a "B" suffix.
ZENER VOLTAGE (V<sub>2</sub>) MEASUREMENT ON Semiconductor guarantees the zener voltage when measured at 90 seconds while maintaining the lead temperature (T<sub>L</sub>) at 30°C ±1°C, 3/8″ from the diode body.
ZENER IMPEDANCE (Z<sub>2</sub>) DERIVATION The zener impedance is derived from 60 seconds AC voltage, which results when an AC current having an rms value equal to 10% of the DC zener current (I<sub>ZT</sub> or I<sub>ZK</sub>) is superimposed on I<sub>ZT</sub> or I<sub>ZK</sub>.

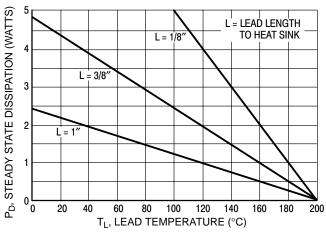
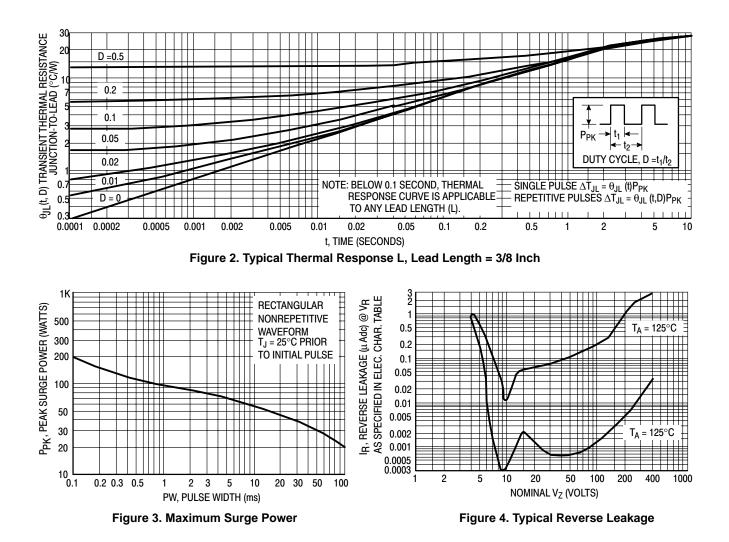


Figure 1. Power Temperature Derating Curve



## **APPLICATION NOTE**

Since the actual voltage available from a given zener diode is temperature dependent, it is necessary to determine junction temperature under any set of operating conditions in order to calculate its value. The following procedure is recommended:

Lead Temperature, T<sub>L</sub>, should be determined from:

$$\mathsf{T}_{\mathsf{L}} = \theta_{\mathsf{L}\mathsf{A}} \, \mathsf{P}_{\mathsf{D}} + \mathsf{T}_{\mathsf{A}}$$

 $\theta_{LA}$  is the lead-to-ambient thermal resistance (°C/W) and P<sub>D</sub> is the power dissipation. The value for  $\theta_{LA}$  will vary and depends on the device mounting method.  $\theta_{LA}$  is generally 30–40°C/W for the various clips and tie points in common use and for printed circuit board wiring.

The temperature of the lead can also be measured using a thermocouple placed on the lead as close as possible to the tie point. The thermal mass connected to the tie point is normally large enough so that it will not significantly respond to heat surges generated in the diode as a result of pulsed operation once steady-state conditions are achieved. Using the measured value of  $T_L$ , the junction temperature may be determined by:

$$\mathsf{T}_\mathsf{J} = \mathsf{T}_\mathsf{L} + \Delta \mathsf{T}_\mathsf{J}\mathsf{L}$$

 $\Delta T_{JL}$  is the increase in junction temperature above the lead temperature and may be found from Figure 2 for a train of power pulses (L = 3/8 inch) or from Figure 10 for dc power.

$$\Delta T_{JL} = \theta_{JL} P_{D}$$

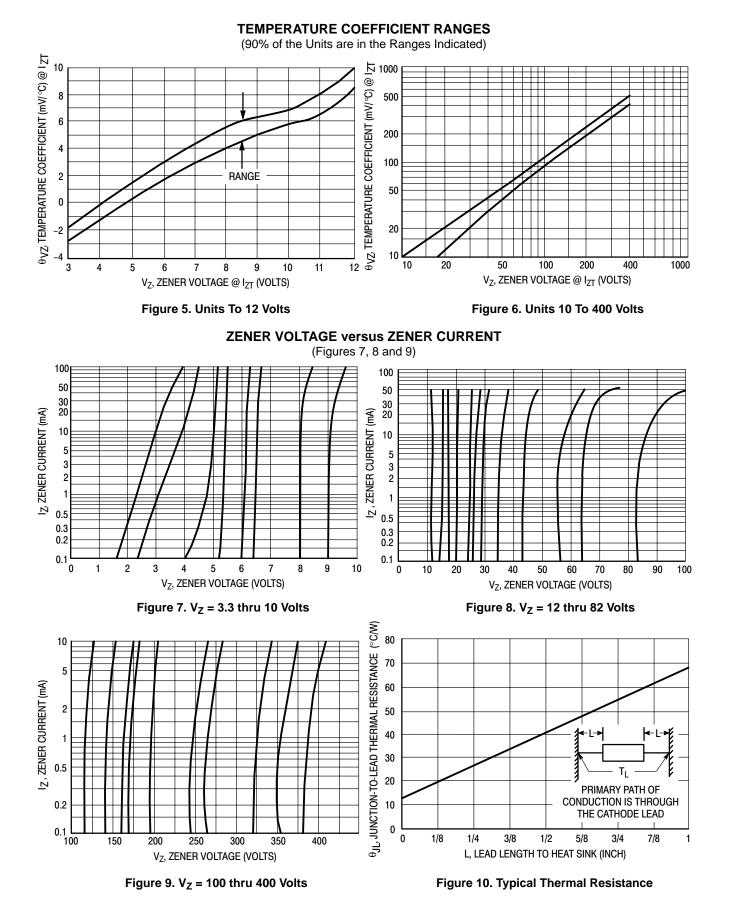
For worst-case design, using expected limits of  $I_Z$ , limits of  $P_D$  and the extremes of  $T_J$  ( $\Delta T_J$ ) may be estimated. Changes in voltage,  $V_Z$ , can then be found from:

$$\Delta \mathsf{V} = \theta_{\mathsf{VZ}} \, \Delta \mathsf{T}_{\mathsf{J}}$$

 $\theta_{VZ}$ , the zener voltage temperature coefficient, is found from Figures 5 and 6.

Under high power-pulse operation, the zener voltage will vary with time and may also be affected significantly by the zener resistance. For best regulation, keep current excursions as low as possible.

Data of Figure 2 should not be used to compute surge capability. Surge limitations are given in Figure 3. They are lower than would be expected by considering only junction temperature, as current crowding effects cause temperatures to be extremely high in small spots resulting in device degradation should the limits of Figure 3 be exceeded.

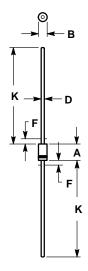


## **OUTLINE DIMENSIONS**

# Zener Voltage Regulators – Axial Leaded

## 3 Watt DO-41 Surmetic<sup>™</sup> 30

PLASTIC DO-41 CASE 59-10 ISSUE R



- NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH. 3. 59-04 OBSOLETE, NEW STANDARD 59-09. 4. 59-03 OBSOLETE, NEW STANDARD 59-10. 5. ALL RULES AND NOTES ASSOCIATED WITH JEDEC DO-41 OUTLINE SHALL APPLY 6. POLARITY DENOTED BY CATHODE BAND. 7. LEAD DIAMETER NOT CONTROLLED WITHIN F DIMENSION.

	INC	HES	MILLIMETERS			
DIM	MIN	MAX	MIN	MAX		
Α	0.161	0.205	4.10	5.20		
В	0.079	0.106	2.00	2.70		
D	0.028	0.034	0.71	0.86		
F		0.050		1.27		
K	1.000		25.40			

Surmetic is a trademark of Semiconductor Components Industries, LLC.

**ON Semiconductor** and without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights on the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer.

#### PUBLICATION ORDERING INFORMATION

#### Literature Fulfillment:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA

Phone: 303–675–2175 or 800–344–3860 Toll Free USA/Canada Fax: 303–675–2176 or 800–344–3867 Toll Free USA/Canada Email: ONlit@hibbertco.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada

JAPAN: ON Semiconductor, Japan Customer Focus Center 4–32–1 Nishi–Gotanda, Shinagawa–ku, Tokyo, Japan 141–0031 Phone: 81–3–5740–2700 Email: r14525@onsemi.com

ON Semiconductor Website: http://onsemi.com

For additional information, please contact your local Sales Representative.