

# P6SMB11CAT3 Series

## 600 Watt Peak Power Zener Transient Voltage Suppressors

### Bidirectional\*

The SMB series is designed to protect voltage sensitive components from high voltage, high energy transients. They have excellent clamping capability, high surge capability, low zener impedance and fast response time. The SMB series is supplied in ON Semiconductor's exclusive, cost-effective, highly reliable Surmetic™ package and is ideally suited for use in communication systems, automotive, numerical controls, process controls, medical equipment, business machines, power supplies and many other industrial/consumer applications.

#### Specification Features:

- Working Peak Reverse Voltage Range – 9.4 to 77.8 V
- Standard Zener Breakdown Voltage Range – 11 to 91 V
- Peak Power – 600 Watts @ 1 ms
- ESD Rating of Class 3 (>16 KV) per Human Body Model
- Maximum Clamp Voltage @ Peak Pulse Current
- Low Leakage < 5  $\mu$ A Above 10 V
- UL 497B for Isolated Loop Circuit Protection
- Response Time is Typically < 1 ns

#### Mechanical Characteristics:

**CASE:** Void-free, transfer-molded, thermosetting plastic

**FINISH:** All external surfaces are corrosion resistant and leads are readily solderable

**MAXIMUM CASE TEMPERATURE FOR SOLDERING PURPOSES:**  
260°C for 10 Seconds

**LEADS:** Modified L-Bend providing more contact area to bond pads

**POLARITY:** Polarity band will not be indicated

**MOUNTING POSITION:** Any

#### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Peak Power Dissipation (Note 1.) @ $T_L = 25^\circ\text{C}$ , Pulse Width = 1 ms	$P_{PK}$	600	W
DC Power Dissipation @ $T_L = 75^\circ\text{C}$ Measured Zero Lead Length (Note 2.) Derate Above $75^\circ\text{C}$	$P_D$	3.0	W
Thermal Resistance from Junction to Lead	$R_{\theta JL}$	40 25	mW/ $^\circ\text{C}$ $^\circ\text{C}/\text{W}$
DC Power Dissipation (Note 3.) @ $T_A = 25^\circ\text{C}$ Derate Above $25^\circ\text{C}$	$P_D$	0.55	W
Thermal Resistance from Junction to Ambient	$R_{\theta JA}$	4.4 226	mW/ $^\circ\text{C}$ $^\circ\text{C}/\text{W}$
Operating and Storage Temperature Range	$T_J, T_{stg}$	-65 to +150	$^\circ\text{C}$

1. 10 X 1000  $\mu$ s, non-repetitive
2. 1" square copper pad, FR-4 board
3. FR-4 board, using ON Semiconductor minimum recommended footprint, as shown in 403A case outline dimensions spec.

\*Please see P6SMB6.8AT3 to P6SMB200AT3 for Unidirectional devices.



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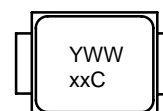
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PLASTIC SURFACE MOUNT  
ZENER OVERVOLTAGE  
TRANSIENT SUPPRESSORS  
9.4–78 VOLTS  
600 WATT PEAK POWER



SMB  
CASE 403A  
PLASTIC

#### MARKING DIAGRAM



Y = Year  
WW = Work Week  
xxC = Specific Device Code  
(See Table Next Page)

#### ORDERING INFORMATION

Device †	Package	Shipping
P6SMBxxCAT3	SMB	2500/Tape & Reel

Devices listed in **bold, italic** are ON Semiconductor **Preferred** devices. **Preferred** devices are recommended choices for future use and best overall value.

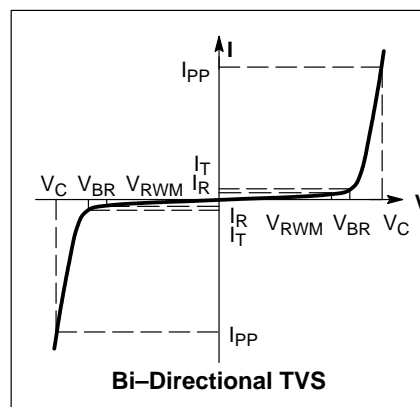
†The "T3" suffix refers to a 13 inch reel.

## P6SMB11CAT3 Series

### ELECTRICAL CHARACTERISTICS

(T<sub>A</sub> = 25°C unless otherwise noted)

Symbol	Parameter
I <sub>PP</sub>	Maximum Reverse Peak Pulse Current
V <sub>C</sub>	Clamping Voltage @ I <sub>PP</sub>
V <sub>RWM</sub>	Working Peak Reverse Voltage
I <sub>R</sub>	Maximum Reverse Leakage Current @ V <sub>RWM</sub>
V <sub>BR</sub>	Breakdown Voltage @ I <sub>T</sub>
I <sub>T</sub>	Test Current
ΘV <sub>BR</sub>	Maximum Temperature Coefficient of V <sub>BR</sub>



### ELECTRICAL CHARACTERISTICS (Devices listed in bold, italic are ON Semiconductor Preferred devices.)

Device	Device Marking	V <sub>RWM</sub> (Note 4.)	I <sub>R</sub> @ V <sub>RWM</sub>	Breakdown Voltage			V <sub>C</sub> @ I <sub>PP</sub> (Note 6.)		ΘV <sub>BR</sub>	
				V <sub>BR</sub> Volts (Note 5.)			@ I <sub>T</sub>	V <sub>C</sub>		I <sub>PP</sub>
		Volts	μA	Min	Nom	Max	mA	Volts		Amps
P6SMB11CAT3	11C	9.4	5	10.5	11.05	11.6	1	15.6	38	0.075
P6SMB12CAT3	12C	10.2	5	11.4	12	12.6	1	16.7	36	0.078
P6SMB13CAT3	13C	11.1	5	12.4	13.05	13.7	1	18.2	33	0.081
P6SMB15CAT3	15C	12.8	5	14.3	15.05	15.8	1	21.2	28	0.084
P6SMB16CAT3	16C	13.6	5	15.2	16	16.8	1	22.5	27	0.086
P6SMB18CAT3	18C	15.3	5	17.1	18	18.9	1	25.2	24	0.088
P6SMB20CAT3	20C	17.1	5	19	20	21	1	27.7	22	0.09
P6SMB22CAT3	22C	18.8	5	20.9	22	23.1	1	30.6	20	0.09
P6SMB24CAT3	24C	20.5	5	22.8	24	25.2	1	33.2	18	0.094
P6SMB27CAT3	27C	23.1	5	25.7	27.05	28.4	1	37.5	16	0.096
P6SMB30CAT3	30C	25.6	5	28.5	30	31.5	1	41.4	14.4	0.097
<b><i>P6SMB33CAT3</i></b>	<b><i>33C</i></b>	<b><i>28.2</i></b>	<b><i>5</i></b>	<b><i>31.4</i></b>	<b><i>33.05</i></b>	<b><i>34.7</i></b>	<b><i>1</i></b>	<b><i>45.7</i></b>	<b><i>13.2</i></b>	<b><i>0.098</i></b>
P6SMB36CAT3	36C	30.8	5	34.2	36	37.8	1	49.9	12	0.099
P6SMB39CAT3	39C	33.3	5	37.1	39.05	41	1	53.9	11.2	0.1
P6SMB43CAT3	43C	36.8	5	40.9	43.05	45.2	1	59.3	10.1	0.101
P6SMB47CAT3	47C	40.2	5	44.7	47.05	49.4	1	64.8	9.3	0.101
P6SMB51CAT3	51C	43.6	5	48.5	51.05	53.6	1	70.1	8.6	0.102
P6SMB56CAT3	56C	47.8	5	53.2	56	58.8	1	77	7.8	0.103
P6SMB62CAT3	62C	53	5	58.9	62	65.1	1	85	7.1	0.104
P6SMB68CAT3	68C	58.1	5	64.6	68	71.4	1	92	6.5	0.104
P6SMB75CAT3	75C	64.1	5	71.3	75.05	78.8	1	103	5.8	0.105
P6SMB82CAT3	82C	70.1	5	77.9	82	86.1	1	113	5.3	0.105
P6SMB91CAT3	91C	77.8	5	86.5	91	95.5	1	125	4.8	0.106

- A transient suppressor is normally selected according to the working peak reverse voltage (V<sub>RWM</sub>), which should be equal to or greater than the DC or continuous peak operating voltage level.
- V<sub>BR</sub> measured at pulse test current I<sub>T</sub> at an ambient temperature of 25°C.
- Surge current waveform per Figure 2 and derate per Figure 3 of the General Data – 600 Watt at the beginning of this group.

## P6SMB11CAT3 Series

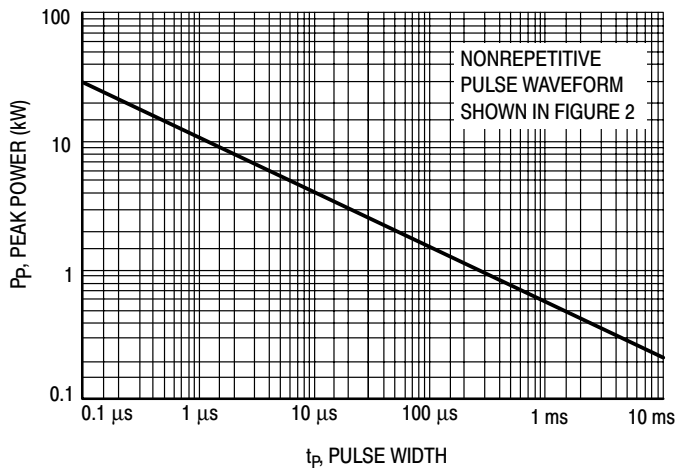


Figure 1. Pulse Rating Curve

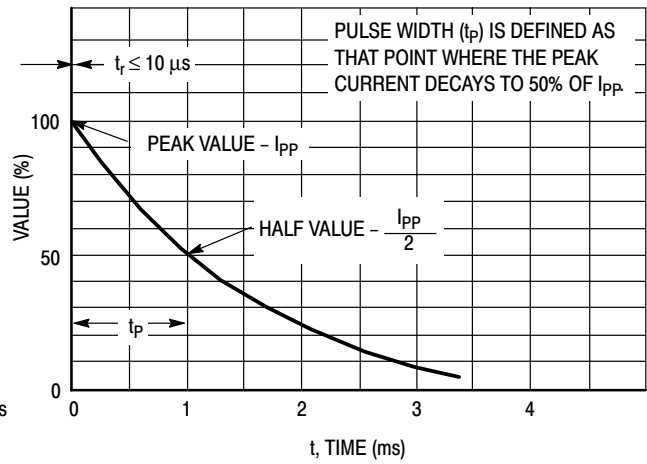


Figure 2. Pulse Waveform

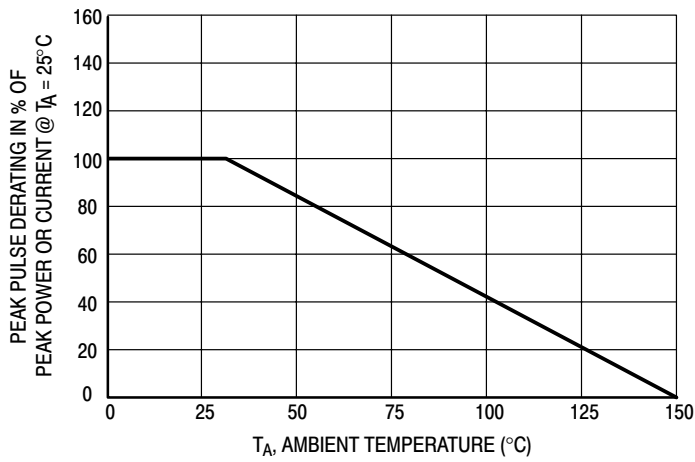
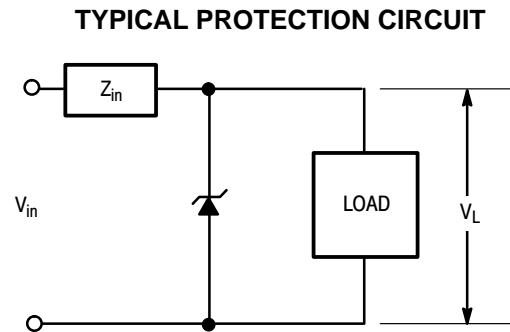


Figure 3. Pulse Derating Curve



## APPLICATION NOTES

### RESPONSE TIME

In most applications, the transient suppressor device is placed in parallel with the equipment or component to be protected. In this situation, there is a time delay associated with the capacitance of the device and an overshoot condition associated with the inductance of the device and the inductance of the connection method. The capacitive effect is of minor importance in the parallel protection scheme because it only produces a time delay in the transition from the operating voltage to the clamp voltage as shown in Figure 4.

The inductive effects in the device are due to actual turn-on time (time required for the device to go from zero current to full current) and lead inductance. This inductive effect produces an overshoot in the voltage across the equipment or component being protected as shown in Figure 5. Minimizing this overshoot is very important in the application, since the main purpose for adding a transient suppressor is to clamp voltage spikes. The SMB series have a very good response time, typically < 1 ns and negligible inductance. However, external inductive effects could produce unacceptable overshoot. Proper circuit layout,

minimum lead lengths and placing the suppressor device as close as possible to the equipment or components to be protected will minimize this overshoot.

Some input impedance represented by  $Z_{in}$  is essential to prevent overstress of the protection device. This impedance should be as high as possible, without restricting the circuit operation.

### DUTY CYCLE DERATING

The data of Figure 1 applies for non-repetitive conditions and at a lead temperature of 25°C. If the duty cycle increases, the peak power must be reduced as indicated by the curves of Figure 6. Average power must be derated as the lead or ambient temperature rises above 25°C. The average power derating curve normally given on data sheets may be normalized and used for this purpose.

At first glance the derating curves of Figure 6 appear to be in error as the 10 ms pulse has a higher derating factor than the 10  $\mu$ s pulse. However, when the derating factor for a given pulse of Figure 6 is multiplied by the peak power value of Figure 1 for the same pulse, the results follow the expected trend.

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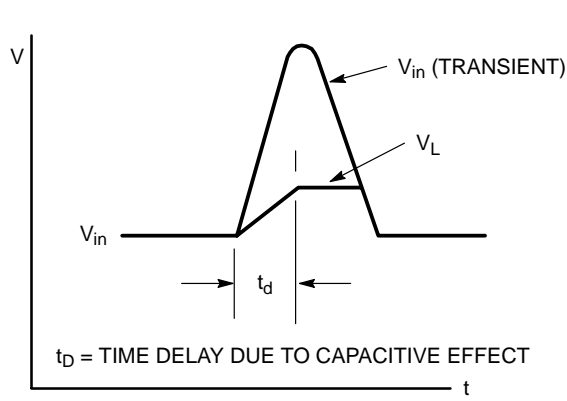


Figure 4.

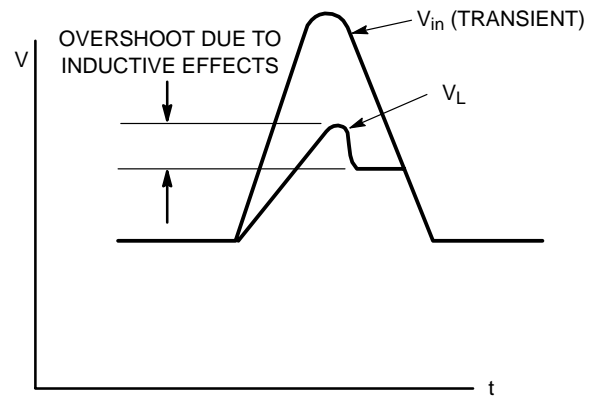


Figure 5.

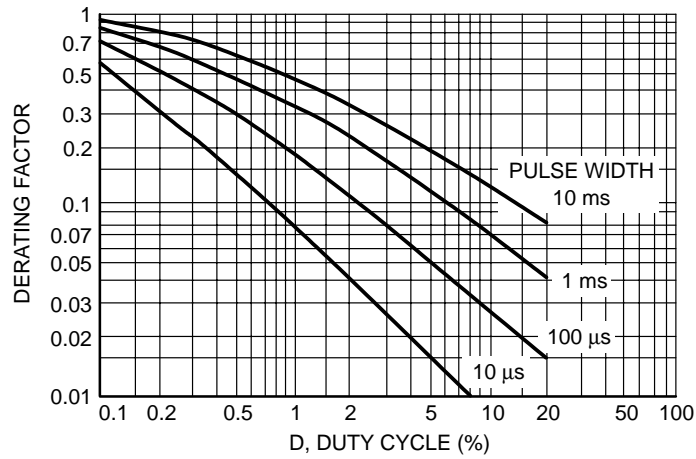


Figure 6. Typical Derating Factor for Duty Cycle

## UL RECOGNITION

The entire series has *Underwriters Laboratory Recognition* for the classification of protectors (QVGV2) under the UL standard for safety 497B and File #116110. Many competitors only have one or two devices recognized or have recognition in a non-protective category. Some competitors have no recognition at all. With the UL497B recognition, our parts successfully passed several tests

including Strike Voltage Breakdown test, Endurance Conditioning, Temperature test, Dielectric Voltage-Withstand test, Discharge test and several more.

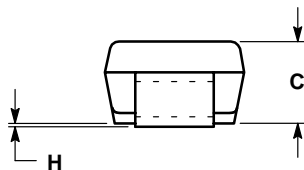
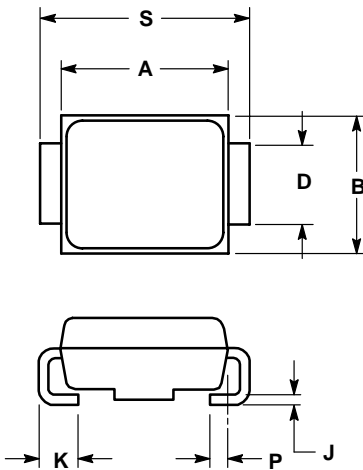
Whereas, some competitors have only passed a flammability test for the package material, we have been recognized for much more to be included in their Protector category.

OUTLINE DIMENSIONS

# Transient Voltage Suppressors – Surface Mounted

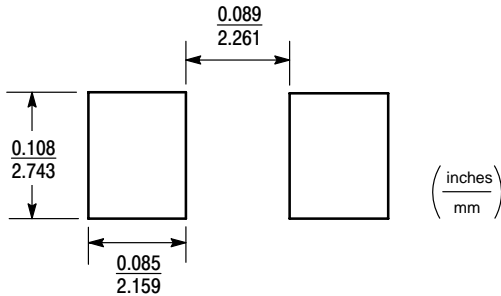
## 600 Watt Peak Power

SMB  
DO-214AA  
CASE 403A-03  
ISSUE D



- NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
  2. CONTROLLING DIMENSION: INCH.
  3. D DIMENSION SHALL BE MEASURED WITHIN DIMENSION P.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.160	0.180	4.06	4.57
B	0.130	0.150	3.30	3.81
C	0.075	0.095	1.90	2.41
D	0.077	0.083	1.96	2.11
H	0.0020	0.0060	0.051	0.152
J	0.006	0.012	0.15	0.30
K	0.030	0.050	0.76	1.27
P	0.020 REF		0.51 REF	
S	0.205	0.220	5.21	5.59



SMB Footprint

## **Notes**

# P6SMB11CAT3 Series

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