1-of-2 Decoder/ Demultiplexer

The NL7SZ19 is a high-performance 1-to-2 Decoder/Demultiplexer operating from a 1.65 V to 5.5 V supply. High impedance TTL compatible inputs significantly reduce current loading to input drivers while TTL compatible outputs offer improved switching noise performance. A $V_{\rm 1}$ specification of 5.5 V allows the inputs to be safely driven from 5.5 V devices. The NL7SZ19 is suitable for memory address decoding and other TTL level bus oriented applications.

Features

- High-Speed Propagation Delay t_{PD} 2.7 nS (Typ), Load 50 pF @ 5.0 V
- 32 mA Output Drive Capability
- Power Down Impedance Inputs/Outputs in High-Z
- Broad V_{CC} Operating Range 1.65 V to 5.5 V
- Surface Mount Technology SC-70, 6-Lead Packaging
- OVT* on Inputs Facilitates 5.0 V to 3.0 V Translation

Typical Applications

- Cell Phones
- PDAs
- Digital Cameras
- Video Cameras
- Hand-Held Sound Devices

Important Information

- ESD Protection: HBM >2000 V, MM >200 V
- Latch-Up Max Rating: 200 mA
- Pin to Pin Compatible with Fairchild's NC7SC19

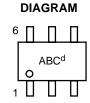


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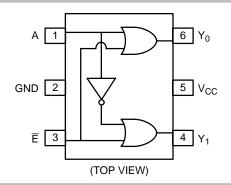


SOT-363/SC70-6/SC-88 DF SUFFIX CASE 419B



MARKING

ABC = Device Marking d = Date Code



PIN/FUNCTION TABLE

Pin	Function
А	Data Input
Ē	Decoder/Multiplexer Pin
Y ₀	Output 0
Y ₁	Output 1

TRUTH TABLE

Ē	Α	$Y_0 = A + \overline{E}$	$Y_1 = \overline{A} + \overline{E}$
L	L	L	Н
L	Н	Н	L
Н	Х	Н	Н

X = Don't Care

ORDERING INFORMATION

Device	Package	Shipping
NL7SZ19DFT1	SOT-363/ SC70-6/ SC-88	3000 / Tape & Reel

^{*}Over Voltage Tolerance (OVT) enables input and output pins to function outside (higher) of their operating voltages, with no damage to the devices or to signal integrity.

MAXIMUM RATINGS

Symbol	Rating		Value	Unit	
V _{CC}	DC Supply Voltage	-0.5 to +7.0	V		
V _{IN}	DC Input Voltage	-0.5 to +7.0	V		
V _{OUT}	DC Output Voltage		-0.5 to +7.0	V	
I _{IK}	DC Input Diode Current	@ V ₁ < -0.5 V	-50	mA	
I _{OK}	DC Output Diode Current	@ V ₁ < -0.5 V	-50	mA	
l _{out}	DC Output Sink Current		±50	mA	
I _{CC}	DC Supply Current per Supply Pin	±100	mA		
I _{GND}	DC Ground Current per Ground Pin	±100	mA		
T _{STG}	Storage Temperature Range	-65 to +150	°C		
T _L	Lead Temperature, 1 mm from Case for 10 Second	260	°C		
TJ	Junction Temperature Under Bias	+150	°C		
$\theta_{\sf JA}$	Thermal Resistance (Note 1)		250	°C/W	
P _D	Power Dissipation in Still Air at 85°C		180	mW	
MSL	Moisture Sensitivity	Level 1	-		
F _R	Flammability Rating	UL 94 V-0 @ 0125 in	-		
V _{ESD}	ESD Withstand Voltage	Human Body Model (Note 2) Machine Model (Note 3) Charged Device Model (Note 4)	> 2000 > 150 n/a	V	

Maximum Ratings are those values beyond which damage to the device may occur. Exposure to these conditions or conditions beyond those indicated may adversely affect device reliability. Functional operation under absolute maximum-rated conditions is not implied. Functional operation should be restricted to the Recommended Operating Conditions.

- 1. Measured with minimum pad spacing on an FR4 board, using 10 mm-by-1 inch, 2 ounce copper trace no air flow.
- Tested to EIA/JESD22-A114-A.
 Tested to EIA/JESD22-A115-A.
- 4. Tested to JESD22-C101-A.

RECOMMENDED OPERATING CONDITIONS

Symbol	Rating	Value	Unit	
V _{CC}	DC Supply Voltage	1.65 to 5.5	V	
V _{CC}	DC Supply Voltage, Data Retention		1.5 to 5.5	V
V _{IN}	Input Voltage		0 to 5.5	V
V _{OUT}	Output Voltage	0 to 5.5	V	
T _A	Operating Temperature	-40 to 85	°C	
t _r , t _f	Input Rise and Fall Times	V_{CC} @ 1.8 ± 0.15 V V_{CC} @ 2.5 ± 0.2 V V_{CC} @ 3.3 ± 0.3 V V_{CC} @ 5.0 ± 0.5 V	0 to 20 0 to 20 0 to 10 0 to 5	nS/V
θ_{JA}	Thermal Resistance		350	°C/W

DC ELECTRICAL CHARACTERISTICS

				V _{CC}	,	T _A = 25°C		$T_A = -40^{\circ}C \text{ to } 85^{\circ}C$		
Symbol	Parameter	Con	dition	(V)	Min	Тур	Max	Min	Max	Unit
V _{IH}	High-Level Input Voltage			1.65 2.3 to 5.5	0.75 V _{CC} 0.70 V _{CC}			0.75 V _{CC} 0.70 V _{CC}		V
V _{IL}	Low-Level Output Voltage			1.65 2.3-5.5			0.25 V _{CC} 0.30 V _{CC}		0.25 V _{CC} 0.30 V _{CC}	V
	High-Level	V V 07	I _{OH} = -100 μa	1.65 2.3 3.0 4.5	1.55 2.20 2.90 4.40	1.65 2.30 3.00 4.50		1.55 2.20 2.90 4.40		
V _{ОН}	Output Voltage	$V_{IN} = V_{IH}$ or V_{IL}	$I_{OH} = -3.0 \text{ mA}$ $I_{OH} = -8.0 \text{ mA}$ $I_{OH} = -16 \text{ mA}$ $I_{OH} = -24 \text{ mA}$ $I_{OH} = -32 \text{ mA}$	1.65 2.3 3.0 3.0 4.5	1.29 1.90 2.40 2.30 3.80	1.47 2.10 2.75 2.63 4.15		1.29 1.90 2.40 2.30 3.80		V
	Low-Level	V V	I _{OL} = 100 μa	1.65 2.3 3.0 4.5		0.0 0.0 0.0 0.0	0.10 0.10 0.10 0.10		0.10 0.10 0.10 0.10	
V _{OL}	Output Voltage	$V_{IN} = V_{IL} \text{ or } V_{IH}$	$I_{OL} = 3.0 \text{ mA}$ $I_{OL} = 8.0 \text{ mA}$ $I_{OL} = 16 \text{ mA}$ $I_{OL} = 24 \text{ mA}$ $I_{OL} = 32 \text{ mA}$	1.65 2.3 3.0 3.0 4.5		0.09 0.12 0.20 0.30 0.32	0.24 0.30 0.40 0.55 0.55		0.24 0.30 0.40 0.55 0.55	V
I _{IN}	Input Leakage Current	V _{IN} = 5.5 V, GND		0.0 to 5.5			± 0.1		±1.0	μΑ
l _{OFF}	Power-Off Leakage Current	V _{IN} or V _{OUT} = 5.5 V		0.0			1.0		10	μΑ
Icc	Quiescent Supply Current	V _{IN} = 5.5 V, GND		1.65 to 5.5			1.0		10	μΑ

AC ELECTRICAL CHARACTERISTICS

			$T_A = 25^{\circ}C$ $T_A = -40^{\circ}C \text{ to } 85^{\circ}C$		T _A = 25°C		C to 85°C			
Symbol	Parameter	Condition	V _{CC}	Min	Тур	Max	Min	Max	Unit	Figure
t _{PLH}	Propagation Delay A or • to Y ₀ or Y ₁	$C_L = 15 \text{ pF}$ $R_D = 1.0 \text{ M}\Omega$	$\begin{array}{c} 1.8 \pm 0.15 \\ 2.5 \pm 0.2 \\ 3.3 \pm 0.3 \\ 5.0 \pm 0.5 \end{array}$	2.5 1.2 0.8 0.5	6.2 3.6 2.9 2.4	10.5 6.0 4.1 3.2	2.5 1.2 0.8 0.5	11 6.4 4.5 3.5	nS	Figures 1 & 3
THE		$C_L = 50 \text{ pF}$ $R_D = 500 \text{ M}\Omega$	3.3 ± 0.3 5.0 ± 0.5	1.2 0.8	3.2 2.7	5.1 4.0	1.2 0.8	5.4 4.3	nS	Figures 1 & 3
C _{IN}	Input Capacitance		0		2.3				pF	
C _{PD}	Power Dissipation Capacitance	Note 5	3.3 5.0		10.5 12.8				pF	Figure 2

^{5.} C_{PD} is defined as the value of the internal equivalent capacitance which is derived from dynamic operating current consumption (I_{CCD}) at no output loading and operating at 50% duty cycle (see Figure 2). C_{PD} is related to I_{CCD} dynamic operating current by the expression: I_{CCD} = (C_{PD}) (V_{CC}) (f_{IN}) + (I_{CCD}static).

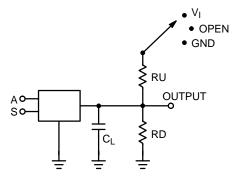


Figure 1. AC Test Circuit

 C_L Includes Load and Stray Capacitance Input PRR = 1.0 MHz; t_W = 500 ns

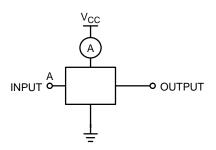


Figure 2. I_{CCD} Test Circuit

 $\begin{aligned} & \text{Input} = \text{AC Waveform; } t_{\text{r}} = t_{\text{f}} = 1.8 \text{ nS} \\ & \text{PRR} = 10 \text{ MHz; } \text{Duty Cycle} = 50\% \\ & \text{S Input} = \text{GND or x} \end{aligned}$

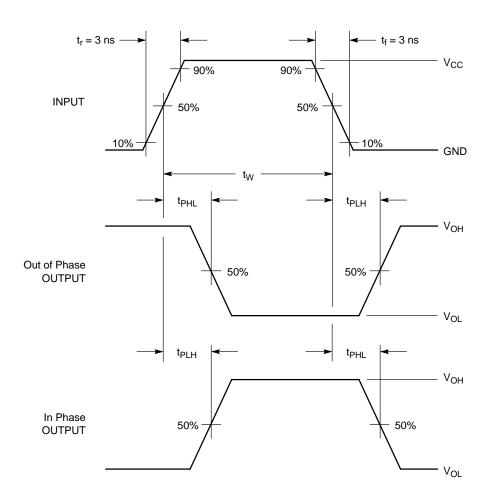
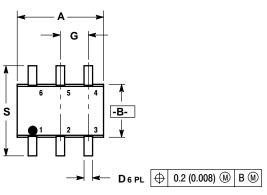


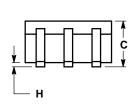
Figure 3. AC Waveforms

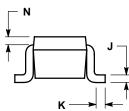
PACKAGE DIMENSIONS

SOT-363/SC70-6/SC-88 **DF SUFFIX**

6-LEAD PACKAGE CASE 419B-02 ISSUE P







- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. 419B-01 OBSOLETE, NEW STANDARD 419B-02.

	INC	HES	MILLIN	IETERS	
DIM	MIN	MAX	MIN	MAX	
Α	0.071	0.087	1.80	2.20	
В	0.045	0.053	1.15	1.35	
С	0.031	0.043	0.80	1.10	
D	0.004	0.012	0.10	0.30	
G	0.026	0.026 BSC		BSC	
Н		0.004		0.10	
J	0.004	0.010	0.10	0.25	
K	0.004	0.012	0.10	0.30	
N	0.008	REF	0.20 REF		
S	0.079	0.087	2.00	2.20	

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